

Specification for

Harmonized system of quality assessment for electronic components —

Fixed resistors for use in electronic
equipment —

Part 05.01 Blank detail specification:

Fixed resistor networks in which not all
resistors are individually measurable —
Assessment level E

Amendments issued since publication

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National foreword

This Part of this British Standard has been prepared under the direction of the Electronic Components Standards Committee. It is identical with IEC Publication 115-7-1 (QC 400501):1984 *“Fixed resistors for use in electronic equipment. Blank detail specification: Fixed resistor networks in which not all resistors are individually measurable. Assessment level E”* published by the International Electrotechnical Committee (IEC).

This standard is a harmonized specification within the IECQ system of quality assessment for electronic components.

Terminology and conventions. The text of the International Standard has been approved as suitable for publication as a British Standard without deviation. Some terminology and certain conventions are not identical with those used in British Standards.

Cross-references. The British Standard harmonized with QC 001001 and QC 001002 is BS 9000 *“General requirements for a system for electronic components of assessed quality — Part 1:1981 Specification of basic rules and procedures”*.

International Standard	Corresponding British Standard
IEC 115-1:1982 (QC 400000:1982)	BS 9940 <i>Fixed resistors for use in electronic equipment</i> Part 0:1983 <i>Generic specification</i> (Identical)
IEC 115-7:1984 (QC 400500:1984)	BS 9940 <i>Fixed resistors for use in electronic equipment</i> Part 05.00:1985 <i>Sectional specification: Fixed resistor networks in which not all resistors are individually measurable</i> (Identical)
IEC 410:1973	BS 6001:1972 <i>Sampling procedures and tables for inspection by attributes</i> (Technically equivalent)

Scope. This standard lists the ratings, characteristics and inspection requirements which shall be included as mandatory requirements in accordance with BS 9940-05.00.

Detail specification layout. In the event of conflict between the requirements of this specification and the provisions of BS 9000, the latter shall take precedence except that the front page will be in accordance with BS 9000 *Circular Letter No. 15*.

A British Standard does not purport to include all the necessary provisions of a contract. Users of British Standards are responsible for their correct application.

Compliance with a British Standard does not of itself confer immunity from legal obligations.

Summary of pages

This document comprises a front cover, an inside front cover, pages i and ii, pages 1 to 11 and a back cover.

This standard has been updated (see copyright date) and may have had amendments incorporated. This will be indicated in the amendment table on the inside front cover.

Introduction

Blank detail specification

A blank detail specification is a supplementary document to the Sectional Specification and contains requirements for style and layout and minimum content of detail specifications. Detail specifications not complying with these requirements shall not be considered as being in accordance with IEC specifications nor shall they so be described.

In the preparation of detail specifications the content of Sub-clause 1.4 of the Sectional Specification shall be taken into account.

The numbers between brackets on the first page correspond to the following information which shall be inserted in the position indicated.

Identification of the detail specification

- [1] The “International Electrotechnical Commission” or the National Standards Organization under whose authority the detail specification is drafted.
- [2] The IEC or National Standards number of the detail specification, date of issue and any further information required by the national system.
- [3] The number and issue number of the IEC or national Generic Specification.
- [4] The IEC number of the blank detail specification.

Identification of the resistor network

- [5] A short description of the type of resistor network.
Three cases must be considered:
 - the detail specification concerns a style of network which is not a “Qualifying test network” nor an associated one to a “qualifying test network” (see Sub-clause 1.6.9 of the Sectional Specification).
 - the detail specification concerns a style of network which could be used as a “Qualifying test network” (see Sub-clause 1.6.9 and Sub-clause 3.1 of the Sectional Specification). This should be then stated.
 - the detail specification concerns a style of network which could be qualified by association to a “qualifying test network” (see Sub-clause 1.6.9 and Sub-clause 3.1 of the Sectional Specification). This should be stated and the number of the detail specification of the qualifying test network given.
- [6] Electrical circuit drawing showing all resistors and connections contained in the network. Terminal pin number shall be shown.
Individual resistors shall be marked as R_1 , R_2 , R_3 , etc.
- [7] Information on typical construction (when applicable).
NOTE When the resistor network is not designed for use on printed board applications, this shall be clearly stated in the detail specification in this position.
- [8] Outline drawing with main dimensions which are of importance for interchangeability and/or reference to the national or international documents for outlines. Alternatively, this drawing may be given in an appendix to the detail specification. Pin 1 shall be shown for identification purposes.
- [9] Application or group of applications covered and/or assessment level.
NOTE The assessment level(s) to be used in a detail specification shall be selected from the sectional specification, Subclause 3.3.3. This implies that one blank detail specification may be used in combination with several assessment levels, provided the grouping of the tests does not change.
- [10] Reference data on the most important properties, to allow comparison between the various network types.