

Specification for

**Harmonized system of
quality assessment for
electronic
components —
Family specification —**

**Digital integrated TTL-SCHOTTKY
circuits —
Series 54S, 64S, 74S, 84S**

Amendments issued since publication

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National foreword

This British Standard has been prepared under the direction of the Electronic Components Standards Committee. It is identical with CENELEC Electronic Components Committee (CECC) 90102 *“Family specification: Digital integrated TTL SCHOTTKY circuits Series 54S, 64S, 74S, 84S”*.

This standard is a harmonized specification with in the CECC system.

Terminology and conventions. The text of the CECC specification has been accepted as suitable for publication, without deviation, as a British Standard. Certain terminology and conventions are used, however, that are not identical with those used in British Standards. Attention is particularly drawn to the following.

The comma has been used throughout as a decimal marker. In British Standards it is current practice to use a full point on the baseline as the decimal marker.

Cross references. The British Standard harmonized with CECC 00100 is BS E9000 *“General requirements for electronic components of assessed quality harmonized with the CENELEC Electronic Component Committee System”* Part 1 *“Basic rules”*.

The following International Standards are referred to in the text and for each there is a corresponding British Standard; these are listed below

International Standard	Corresponding British Standard
CECC 90000:1976	BS CECC 90000:1977 <i>Harmonized system of quality assessment for electronic components: Generic specification: Monolithic integrated circuits</i> (Identical)
CECC 90100:1976	BS CECC 90100:1977 <i>Harmonized system of quality assessment for electronic components: Sectional specification: Digital monolithic integrated circuits</i> (Identical)
IEC 191-2:1966	BS 3934:1965 <i>Dimensions of semiconductor devices</i> (Related)

Scope. This standard lists the general information for ratings, characteristics and inspection requirements for a series of integrated circuits which shall be included as mandatory requirements in detail specifications in accordance with BS CECC 90000.

A British Standard does not purport to include all the necessary provisions of a contract. Users of British Standards are responsible for their correct application.

Compliance with a British Standard does not of itself confer immunity from legal obligations.

Summary of pages

This document comprises a front cover, an inside front cover, pages i and ii, the CECC title page, pages ii to iv, pages 1 to 8 and a back cover.

This standard has been updated (see copyright date) and may have had amendments incorporated. This will be indicated in the amendment table on the inside front cover.

English version

Harmonized System of Quality Assessment for
Electronic Components

FAMILY SPECIFICATION:

DIGITAL INTEGRATED TTL-SCHOTTKY CIRCUITS

Series 54 S, 64 S, 74 S, 84 S

Système Harmonisé d'Assurance de la Qualité
des Composants Electroniques

SPECIFICATION DE FAMILLE:

CIRCUITS INTEGRES DIGITAUX TTL-SCHOTTKY

Séries 54 S, 64 S, 74 S, 84 S

Harmonisiertes Gütebestätigungssystem für
Bauelemente der Elektronik

FAMILIEN-SPEZIFIKATION:

DIGITALE INTEGRIERTE TTL-SCHOTTKY- SCHALTUNGEN

Serien 54 S, 64 S, 74 S, 84 S



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