BRITISH STANDARD

BS CECC
90115:1994

Specification for

Harmonized system of quality assessment for electronic components — Blank detail specification —

Digital gate array integrated circuits

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National foreword

This British Standard has been prepared under the direction of the Electronic Components Standards Policy Committee (ECL/-). It is identical with CECC 90115:1993 *Harmonized system of quality assessment for electronic components. Blank detail specification: Digital gate array integrated circuits* published by the European Committee for Electrotechnical Standardization (CENELEC) Electronic Components Committee (CECC).

CECC 90115 was prepared by CECC WG9 Semiconductor integrated circuits and the United Kingdom participation in the drafting was provided by Technical Committee ECL/24, Semiconductors.

Scope. The standard is related to non-finished products, where the customer is involved in their design, layout and specification. As a consequence, this standard describes requirements for "tools" (e.g. for design, verification test, and measurement), rather than requirements for components.

The application of this specification requires that the relevant process technology and the associated manufacturing line are already qualified.

Textual errors. When adopting the text of the International Standard, the textual errors listed below were discovered.

In the last line of the table in **2.2**, "capacitives" should read "capacitive".

In the second paragraph of **4.3**, "caracteristics" should read "characteristics".

Cross-references

International Standard	Corresponding British Standard
CECC 90000	BS CECC 90000:1991 *Harmonized system of quality assessment for electronic components. Generic specification: Monolithic integrated circuits* [Identical]
CECC 90100	BS EN 90100:1993 *Harmonized system of quality assessment for electronic components. Sectional specification: Digital monolithic integrated circuits* [Identical]
IEC 747-1	BS 6493 *Semiconductor devices* Part 1 *Discrete devices* Section 1.1:1984 *General* [Identical]
IEC 748	Part 2 *Integrated circuits* [All Parts are identical]

The British Standard which implements the CECC Rules of Procedure is BS 9000-2:1991 *General requirements for a system for electronic components of assessed quality — Part 2: Specification for the national implementation of the CECC system*.

Detail specification. Detail specification shall comply with the requirements of this Blank Detail Specification and BS CECC 00111-4:1991 *Rules of Procedure 11. Specifications. Part 4: Regulations for CECC detail specification*.

A British Standard does not purport to include all the necessary provisions of a contract. Users of British Standards are responsible for their correct application.

Compliance with a British Standard does not of itself confer immunity from legal obligations.

Summary of pages

This document comprises a front cover, an inside front cover, pages i and ii, the CECC title page, pages ii to iv, pages 1 to 6 and a back cover.

This standard has been updated (see copyright date) and may have had amendments incorporated. This will be indicated in the amendment table on the inside front cover.

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Förderverein für Elektrotechnische Normung (FEN) e. V.
Cenelec Electronic Components Committee

CECC

Système Harmonisé d'Assurance de la Qualité
des Composants Electroniques

SPECIFICATION PARTICULIERE CADRE:

CIRCUITS INTEGRES PREDIFFUSES

Harmonized System of Quality Assessment for
Electronic Components

BLANK DETAIL SPECIFICATION:

DIGITAL GATE ARRAY INTEGRATED CIRCUITS

Harmonisiertes Gütebestätigungssystem für
Bauelemente der Elektronik

VORDRUCK FÜR BAUARTSPEZIFIKATION:

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