

Specification for

**Harmonized system of
quality assessment for
electronic
components —
Blank detail
specification —**

**Integrated line transmitters and
receivers**

Amendments issued since publication

Amd. No.	Date of issue	Comments
8295	August 1994	Indicated by a sideline in the margin

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National foreword

This British Standard has been prepared under the direction of the Electronic Components Standards Committee. It is identical with CENELEC Electronic Components Committee (CECC) 90301:1985 “*Harmonized system of quality assessment for electronic components. Blank detail specification: Integrated line transmitters and/or receivers*” as amended by Amendment No. 1 published in 1994.

Terminology and conventions. The text of the CECC specification has been approved as suitable for publication as a British Standard without deviation. Some terminology and certain conventions are not identical with those used in British Standards; attention is drawn especially to the following.

The comma has been used as a decimal marker. In British Standards it is current practice to use a full point on the baseline as the decimal marker.

Cross-references. The British Standard which implements CECC 00100 is BS 9000 “*General requirements for a system for electronic components of assessed quality*” Part 2 “*Specification for national implementation of CECC basic rules and rules of procedure*”.

International Standards	Corresponding British Standards
CECC 90000:1982	BS CECC 90000:1982 <i>Harmonized system of quality assessment for electronic components. Generic specification: Monolithic integrated circuits</i> (Identical)
CECC 90300:1985	BS CECC 90300:1985 <i>Harmonized system of quality assessment for electronic components. Sectional specification: Interface monolithic integrated circuits</i> (Identical)

The Technical Committee has reviewed the provisions of IEC 147, to which reference is made in the text, and has decided that they are acceptable for use in conjunction with this standard.

A related British Standard for IEC 147 is BS 9300 “*Semiconductor devices of assessed quality: generic data and methods of test*”.

Scope. This standard lists the ratings, characteristics and inspection requirements which shall be included as mandatory requirements in accordance with BS CECC 90300 in any detail specification for these devices.

Detail specification layout. The front page layout of detail specifications released to BS CECC family or blank detail specifications will be in accordance with BS 9000 Circular Letter No. 15.

A British Standard does not purport to include all the necessary provisions of a contract. Users of British Standards are responsible for their correct application.

Compliance with a British Standard does not of itself confer immunity from legal obligations.

Summary of pages

This document comprises a front cover, an inside front cover, pages i and ii, the CECC title page, page ii, pages 1 to 18 and a back cover.

This standard has been updated (see copyright date) and may have had amendments incorporated. This will be indicated in the amendment table on the inside front cover.

English version

Harmonized System of Quality Assessment for
Electronic Components

BLANK DETAIL SPECIFICATION:

INTEGRATED LINE TRANSMITTERS AND/OR RECEIVERS

Système Harmonisé d'Assurance de la Qualité
des Composants Electroniques

SPECIFICATION PARTICULIERE CADRE:

EMETTEURS ET/OU RECEPTEURS DE LIGNE INTEGRES

Harmonisiertes Gütebestätigungssystem für
Bauelemente der Elektronik

VORDRUCK
FÜR BAUARTSPEZIFIKATION:

INTEGRIERTE LEITUNGSTREIBER UND/ODER -EMPFÄNGER



1

Issue
Edition
Ausgabe

CECC 90301

1985

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Foreword

The CENELEC Electronic Components Committee (CECC) is composed of those member countries of the European Committee for Electrotechnical Standardization (CENELEC) who wish to take part in a harmonized System for electronic components of assessed quality.

The object of the System is to facilitate international trade by the harmonization of the specifications and quality assessment procedures for electronic components, and by the grant of an internationally recognized Mark, or Certificate, of Conformity. The components produced under the System are thereby accepted by all member countries without further testing.

This specification has been formally approved by the CECC, and has been prepared for those countries taking part in the System who wish to issue national harmonized specifications for INTEGRATED LINE TRANSMITTERS AND/OR RECEIVERS. It should be read in conjunction with the current regulations for the CECC System.

At the date of printing of this specification the member countries of the CECC are Austria, Belgium, Denmark, Finland, France, Germany, Ireland, Italy, the Netherlands, Norway, Portugal, Spain, Sweden, Switzerland, and the United Kingdom,

Preface

This blank detail specification (BDS) was prepared by CECC WG9 “INTEGRATED CIRCUITS”.

It is based, wherever possible, on the Publications of the International Electrotechnical Commission and in particular on IEC 147: Essential ratings and characteristics of semiconductor devices and general principles of measuring methods.

The text of this BDS was circulated to the CECC for voting in the document indicated below and was ratified by the President of the CECC for printing as a CECC Specification.

Documents	Date of voting	Report on the voting
CECC(Secretariat)1437	October 1983	CECC(Secretariat)1552

It is recognized that the layout proposed cannot apply to all detail specifications based on this document. For example when several similar devices are covered by the same detail specification, it may be convenient to give the limiting values in a table.

In accordance with the decision of the CECC Management Committee this specification is published initially in English and French. The German text will follow as soon as it has been prepared.

1 Front page

The front page of the DS shall be laid out as shown on the following page.

The numbers between square brackets correspond to the following indications which shall be given:

Identification of the DS and of the component:

[1] The name of the National Standards Organization under whom authority the DS is published and, if applicable, the organization from whom the DS is available.

[2] The CECC Symbol and the CECC number allotted to the DS by the CECC General Secretariat.

[3] The number and issue number of the CECC generic or sectional specification as relevant; also national reference if different.

[4] If different from the CECC number, the national number of the DS, date of issue and any further information required by the national system, together with any amendment numbers.

[5] Type number, a short description of the type by:

- function (for example: specified input voltage line receiver)
- number of independent circuits per package
- number and kind of inputs and outputs
- material and type of construction (silicon, monolithic, multichip, bipolar, MOS)
- performance, for example high output power, high speed, low power consumption
- electrostatic sensitivity (where appropriate).

[6] Information on typical construction (if applicable)

For [5] and [6] the text to be given in the DS should be suitable for an entry in CECC 00200 or CECC 00300.

[7] An outline drawing with main dimensions which are of importance for interchangeability, and/or reference to the appropriate national or international document for outlines. Alternatively, this drawing may be given in an appendix to the DS.

[8] Quality assessment level(s)

[9] Reference data giving information on the most important properties of the component, which comparison between the various component types intended for the same, or for similar, applications.

Identification of the component and supplementary information:

Description of the materials for the package (for example, glass, ceramic, silicone) and information relating to the mounting (welding, soldering), lead material and finish.

Inside the sketch of the package, the terminal connections to the inputs, outputs or other important points of the circuit shall be identified. This can be shown by a functional block diagram.

Description of the numbering of the terminals with the identification of pin number 1.

Marking on the device in accordance with the GS (see 2.5 of CECC 90000).

Layout for front page of detail specification

Specification available from : [1]	CECC 90 301-xxx [2] 	
	Page	Total number of pages :
ELECTRONIC COMPONENTS OF ASSESSED QUALITY IN ACCORDANCE WITH : [3]	[4]	
OUTLINE AND DIMENSIONS [7] (First angle projection)	[5]•	
	[6]	
	[8]	
[9]		
Information about manufacturers who have components qualified to this detail specification is available in the current CECC 00 200 : Qualified Products List.		