

Specification for

Harmonized system of quality assessment for electronic components — Blank detail specification —

Digital microprocessor integrated
circuits

The European Standard EN 190110:1994 has the status of a
British Standard

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Amendments issued since publication

Amd. No.	Date of issue	Comments
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National foreword

This British Standard has been prepared under the direction of the Electronic Components Standards Committee (ECL/-). It is identical with CECC 90110:1993 *Harmonized system of quality assessment for electronic components. Blank detail specification: Digital microprocessor integrated circuits*, published by the European Committee for Electrotechnical Standardization (CENELEC) Electronic Components Committee (CECC).

This standard is a harmonized specification within the CECC system.

In 1994 the CENELEC Electronic Components Committee (CECC) accepted CECC 90110:1993 with Amendments 1 and 2 as European Standard EN 190110:1994.

Scope. This standard lists the ratings, characteristics and inspection requirements which shall be included as mandatory requirements in accordance with BS CECC 90100 in any detail specification for these devices.

Cross references

International Standard ^a	Corresponding British Standard
CECC 90000	BS CECC 90000:1991 <i>Harmonized system of quality assessment for electronic components. Generic specification: monolithic integrated circuits</i> (Identical)
CECC 90100	BS EN 190100:1993 <i>Harmonized system of quality assessment for electronic components. Sectional specification: digital monolithic integrated circuits</i> (Identical)
CECC 90200	BS CECC 90200:1991 <i>Harmonized system of quality assessment for electronic components: sectional specification: analogue monolithic integrated circuits</i> (Identical)
CECC 90300	BS CECC 90300:1991 <i>Harmonized system of quality assessment for electronic components. Sectional specification: interface monolithic integrated circuits</i> (Identical)
IEC 747	BS 6493 <i>Semiconductor devices</i> Part 1. <i>Discrete devices</i> (Identical)
IEC 748	Part 2. <i>Integrated circuits</i> (Identical)

^a Undated in the text.

The Technical Committee has reviewed the provisions of CECC 00200, to which reference is made in the text, and has decided that they are acceptable for use in conjunction with this standard.

The British Standard which implements the CECC Rules of Procedure is BS 9000 *General requirements for a system for electronic components of assessed quality — Part 2:1991 Specification for the national implementation of the CECC system.*

Detail specifications. Detail specifications shall comply with the requirements of this Blank Detail Specification and BS CECC 00111 *Rule of Procedure 11. Specifications.*

A British Standard does not purport to include all the necessary provisions of a contract. Users of British Standards are responsible for their correct application.

Compliance with a British Standard does not of itself confer immunity from legal obligations.

Summary of pages

This document comprises a front cover, an inside front cover, pages i to iv, the EN title page, pages 2 to 16 and a back cover.

This standard has been updated (see copyright date) and may have had amendments incorporated. This will be indicated in the amendment table on the inside front cover.

Descriptors: Quality, electronic components, digital microprocessor integrated circuits

English version

Blank Detail Specification: Digital Microprocessor Integrated Circuits

Spécification particulière cadre:
Microprocesseurs logiques à circuits
intégrés

Vordruck für Bauartspezifikation:
Integrierte Mikroprozessor- Schaltungen

This European Standard was approved by the CENELEC Electronic Components Committee (CECC) on 8 May 1994. CENELEC members are bound to comply with CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the General Secretariat of the CECC or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the CECC General Secretariat has the same status as the official versions.

CENELEC members are the national electrotechnical committees of Austria, Belgium, Denmark, Finland, France, Germany, Greece, Iceland, Ireland, Italy, Luxembourg, Netherlands, Norway, Portugal, Spain, Sweden, Switzerland, and United Kingdom. The membership of the CECC is identical, with the exception of the national electrotechnical committees of Greece, Iceland and Luxembourg.

CECC

European Committee for Electrotechnical Standardization
Comité Européen de Normalisation Electrotechnique
Europäisches Komitee für Elektrotechnische Normung

Central Secretariat: rue de Stassart 35, B-1050 Brussels

Foreword

The CENELEC Electronic Components Committee (CECC) is composed of those member countries of the European Committee for Electrotechnical Standardization (CENELEC) who wish to take part in a harmonized System for electronic components of assessed quality.

The object of the System is to facilitate international trade by the harmonization of the specifications and quality assessment procedures for electronic components, and by the grant of an internationally recognized Mark, or Certificate, of Conformity. The components produced under the System are thereby acceptable in all member countries without further testing.

This European Standard was prepared by CECC WG 9, "Integrated Circuits".

The text of the draft based on document CECC 90110 Issue 2:1993 was submitted to the formal vote for conversion to a European Standard; together with the voting report, circulated as document CECC(Secretariat)3545 it was approved by CECC as EN 190110 on 8 May 1994.

The following dates were fixed:

- latest date of announcement of the EN at national level (doa) 1994-08-15
- latest date of publication of an identical national standard^a (dop) 1995-02-15
- latest date of withdrawal of conflicting national standards^a (dow) 1996-02-15

^a National Standard (excluding National implementation of IECQ Specifications)

This specification has been formally approved by the CECC, and has been prepared for those countries taking part in the System who wish to issue national harmonized specifications for DIGITAL MICROPROCESSOR INTEGRATED CIRCUITS AND ASSOCIATED DIGITAL PERIPHERAL INTEGRATED CIRCUITS. It should be read in conjunction with the current regulations for the CECC System.

At the date of printing of this specification, the member countries of the CECC are Austria, Belgium, Denmark, Finland, France, Germany, Ireland, Italy, the Netherlands, Norway, Portugal, Spain, Sweden, Switzerland and the United Kingdom, and copies of it can be obtained from the addresses shown on the blue fly sheet.

Preface

This blank detail specification (BDS) was prepared by CECC WG9: "Integrated circuits".

It is based, wherever possible, on the Publications of the International Electrotechnical Commission and in particular on IEC 747: *Semiconductor devices — Discrete devices and integrated circuits* and on IEC 748: *Semiconductor devices — Integrated circuits*.

The text of this second issue consists of the text of CECC 90110 Issue 1:1986, amended in accordance with the ratified new material introduced by the following document:

Document	Date of Voting	Report on the Voting
CECC (Secretariat)2667	February 1991	CECC (Secretariat)2789

It is recognized that the layout proposed cannot be applied to all detail specifications based on this document. For instance, it may be preferable to indicate the limiting values in the form of a table when several similar devices appear in the same detail specification.

AVIS

In accordance with the decision of the CECC Management Committee, this specification is published initially in English and French. The German text will follow as soon as it has been prepared.

This Issue 2 of CECC 90110 shall become effective for all new qualification approvals from **15 October 1993**. Issue 1 remains valid for existing qualification approvals.