BSI Standards Publication

Printed boards and printed board assemblies - Design and use

Part 6-4: Land pattern design - Generic requirements for dimensional drawings of surface mounted components (SMD) from the viewpoint of land pattern design

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National foreword

This British Standard is the UK implementation of EN IEC 61188-6-4:2019. It is identical to IEC 61188-6-4:2019.

The UK participation in its preparation was entrusted to Technical Committee EPL/501, Electronic Assembly Technology.

A list of organizations represented on this committee can be obtained on request to its secretary.

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Printed boards and printed board assemblies - Design and use - Part 6-4: Land pattern design - Generic requirements for dimensional drawings of surface mounted components (SMD) from the viewpoint of land pattern design (IEC 61188-6-4:2019)

Cartes imprimées et cartes imprimées équipées - Conception et utilisation - Partie 6-4: Conception de la zone de report - Exigences génériques pour les dessins dimensionnels de composants montés en surface (CMS) du point de vue de la conception de la zone de report (IEC 61188-6-4:2019)

Leiterplatten und Flachbaugruppen - Konstruktion und Anwendung - Teil 6-4: Allgemeine Anforderungen an SMD-Maßzeichnungen hinsichtlich der Konstruktion des Anschlussflächenbilds (IEC 61188-6-4:2019)

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European foreword

The text of document 91/1561/FDIS, future edition 1 of IEC 61188-6-4, prepared by IEC/TC 91 "Electronics assembly technology" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN 61188-6-4:2019.

The following dates are fixed:

- latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement (dop) 2020-03-06
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IEC 60191-1	NOTE	Harmonized as EN IEC 60191-1
IEC 60191-6	NOTE	Harmonized as EN 60191-6
IEC 60191-6-1	NOTE	Harmonized as EN 60191-6-1
IEC 61188-5-1	NOTE	Harmonized as EN 61188-5-1
IEC 61188-5-2	NOTE	Harmonized as EN 61188-5-2
IEC 61188-5-3	NOTE	Harmonized as EN 61188-5-3
IEC 61188-5-4	NOTE	Harmonized as EN 61188-5-4
IEC 61188-5-5	NOTE	Harmonized as EN 61188-5-5
IEC 61188-5-6	NOTE	Harmonized as EN 61188-5-6
IEC 61188-5-8	NOTE	Harmonized as EN 61188-5-8
IEC 61191-1	NOTE	Harmonized as EN IEC 61191-1
IEC 61191-2	NOTE	Harmonized as EN 61191-2

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Annex ZA
(normative)

Normative references to international publications with their corresponding European publications

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.
NOTE 1 Where an International Publication has been modified by common modifications, indicated by (mod), the relevant EN/HD applies.
NOTE 2 Up-to-date information on the latest versions of the European Standards listed in this annex is available here: www.cenelec.eu.

Publication	Year	Title	EN/HD	Year
IEC 60194	-	Terms and definitions for printed circuits	-	-
IEC 60194-2	-	Printed boards design, manufacture and assembly - Vocabulary - Part 2: Common usage in electronic technologies as well as printed board and electronic assembly technologies	-	-

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

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PRINTED BOARDS AND PRINTED BOARD ASSEMBLIES – DESIGN AND USE –

Part 6-4: Land pattern design – Generic requirements for dimensional drawings of surface mounted components (SMD) from the viewpoint of land pattern design

FOREWORD

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International Standard IEC 61188-6-4 has been prepared by IEC technical committee 91: Electronics assembly technology.

The text of this International Standard is based on the following documents:

FDIS	Report on voting
91/1561/FDIS	91/1572/RVD

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

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A list of all parts in the IEC 61188 series, published under the general title *Printed boards and printed board assemblies – Design and use*, can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "http://webstore.iec.ch" in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn,
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PRINTED BOARDS AND PRINTED BOARD ASSEMBLIES – DESIGN AND USE –

Part 6-4: Land pattern design – Generic requirements for dimensional drawings of surface mounted components (SMD) from the viewpoint of land pattern design

1 Scope

This part of IEC 61188 specifies generic requirements for dimensional drawings of SMD from the viewpoint of land pattern design.

The purpose of this document is to prevent land pattern design issues caused by lack of information and/or misuse of the information from SMD outline drawing as well as to improve the utilization of IEC 61188 series.

This document is applicable to the SMD of semiconductor devices and electrical components.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60194, *Printed board design, manufacture and assembly – Terms and definitions*

IEC 60194-2, *Printed board design, manufacture and assembly – Vocabulary – Part 2: Common usage in electronic technologies as well as printed board and electronic assembly technologies*

3 Terms, definitions and symbols

For the purposes of this document, the terms and definitions given in IEC 60194 and IEC 60194-2 apply, and the reference symbols are shown in Table 1.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at http://www.electropedia.org/
- ISO Online browsing platform: available at http://www.iso.org/obp

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Table 1 – Reference symbols used in this document

Reference symbol	Definition
A	SMD height (from the mounting surface to the package upper surface)
A_1	Stand-off height (distance from the mounting surface to the package bottom)
A_2	Package height
A_3	Standard heel height for terminal
A_4	Terminal height
A_5	Terminal height (thickness)
A_6	SMD height (from the mounting surface to the package upper surface, maximum)
A_7	SMD height from the mounting surface to the package top (excluding moving part)
A_8	SMD height from the mounting surface to the package top (at the lock lever open state)
A_9	SMD height from the mounting surface to the upper surface of cap
$Øb_1$	Terminal diameter for ball
$Øb_2$	Ball diameter
C, C_1, C_2	Row spacing. Distance between land centers
CY_1	Courtyard width
CY_2	Courtyard length
D	Package width
D'	Solder balls area width (distance between the centres of the ball of both ends)
E	Package length
E'	Solder balls area length (distance between the centres of the ball of both ends)
E_2	Position of auxiliary terminals
E_3	Upper surface cap width
F_1, F_2	Clearance between signal and center (GND) terminal
F_3	Distance from package-end to terminal-end of signal terminal
F_4	Distance between signal terminals at the package corners
G, G_1, G_2	Distance between lands. Measured from inside edges
H_D	SMD total width
H_E	SMD total length
H_T	Height from the tray stage to the package top
i	Terminal inflection point
J_b	Protrusion length of land over the component terminal
J_H	Heel protrusion length
J_S	Side protrusion length
J_T	Toe protrusion length
k_1, k_2, k_4	Land pattern length
k_3	Distance between land patterns
K_5, K_6	Terminal notch length (toe)
K_7, K_8	Terminal notch height (heel)
L	Terminal flat part length (mounting surface side)
L_0, L_1	Length from the package end to a terminal tip
L_3	Position of auxiliary terminals
L_4	Auxiliary terminal pitch
L_5	Package length at the lock lever open state

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Reference symbol	Definition
L_6	Upper surface cap position
L_7	Upper surface cap length
L_P, L_{P1}, L_{P2}	Terminal length (mounting surface side), Projected terminal length (when part of the terminal is away from the mounting surface)
L_{P0}	Terminal length (upper side)
$ØM$	Diameter of auxiliary terminal
N	Coplanarity to mounting surface
$\boxed{P}$	Pitch
q	Land pattern design parameter
R	Terminal bend radius (inside)
R_2	Terminal bend radius (outside)
S	Distance between the terminals. Measured from inside edges
T_T	Tray height from the tray stage to the surface that supports SMD
u_1, u_2	Allowance for courtyard
$ØW$	SMD diameter (terminal)
W_1, W_2	Terminal width
W_3	Bottom centre (GND) terminal length
W_4	Bottom centre (GND) terminal width
W_G	Groove width
W_P	Position tolerance at terminal end
W_V	Variation range of terminal tip outer position
X	Land width
Y	Land length
$ØY$	Land diameter
Z, Z_1, Z_2	Distance between lands. Measured from outside edges
α, β	(Datum symbols)
η	Terminal horizontal angle formed by the groove centre line and the line form the egress to the tip of the terminal
θ	Terminal angle

4 Applicable SMDs

Applicable SMDs are as described below:

a) end-terminal type (components with rectangular or square terminal and cylindrical components with end cap terminal);

NOTE 1 In IEC 61191-2, "toe" solder fillet height is specified but "heel" solder fillet height is not specified for this type of SMD.

b) gull-wing terminals type (e.g. SOP, QFP), inward L-shaped ribbon terminals type and under-body L type (e.g. vertical form aluminium electrolytic capacitor, connector);

NOTE 2 In IEC 61191-2, "heel" solder fillet height is required but "toe" solder fillet height is not required for these SMD.

c) bottom surface terminal type (e.g. BGA, QFN and LGA) and flat lug terminals type.

NOTE 3 These SMDs have characteristics different from a) or b).

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5 Requirements

5.1 Figures and dimensional symbols

The figures shown in this document are indicated as examples of typical SMDs. However, it is not the purpose to specify the rule of drawings (such as how to pull out a dimension line and dimensional symbols to be used).

The dimensional symbols in this document are used to show the various cases in order to indicate common requirements for land pattern designs. This document is not intended to integrate dimensional symbols.

NOTE The dimensional symbols used in this document are referring to existing International Standards and industry standards. It is difficult to integrate the dimensional symbols because the definitions of the dimensional symbols are different between these standards. For this reason, the dimensional symbols in this document can differ depending on the figure. The dimensional symbols in this document give priority to coordination with land pattern shape notation given by the IEC 61188-5 series. As a result, the notation for some SMDs can differ from the industry standards.

5.2 Common requirements

5.2.1 General

The common requirements for dimensional drawings of SMDs from the viewpoint of land pattern design are described in 5.2.2 to 5.2.10.

To explain a basic relation between dimensional drawings of SMDs and land pattern design, a representative case is shown in Figure 1 (gull-wing terminals type; 4-pin S-terminals). The quoted reference symbols in 5.2.2 to 5.2.10 are based on Figure 1.

5.2.2 Requirements for solder joint fillet design

To design a land that is a part of the land pattern and makes a solder joint to each terminal of the SMD, the dimensional drawing of the SMD shall have the terminal's dimensions (L_P and W_1 in Figure 1) and the dimensions that specify the location of the terminal (H_E and P in Figure 1). Each dimension should be indicated by a nominal value with its tolerances.

The reasons are as shown below:

a) As a principle, each terminal of the SMD will have its own land.

Land width (X) and land length (Y) are given by the following formulas:

$$X = W_1 + 2 \times J_S$$

$$Y = J_T + L_P + J_H$$

where

X is the land width [mm];

Y is the land length [mm];

W_1 is the terminal width [mm];

L_P is the terminal length [mm];

J_T is the toe protrusion length [mm];

J_H is the heel protrusion length [mm];

J_S is the side protrusion length [mm].

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