BS EN IEC 61189-5-501:2021

BSI Standards Publication

Test methods for electrical materials, printed boards and other interconnection structures and assemblies

Part 5-501: General test methods for materials and assemblies — Surface insulation resistance (SIR) testing of solder fluxes

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National foreword

This British Standard is the UK implementation of EN IEC 61189-5-501:2021. It is identical to IEC 61189-5-501:2021.

The UK participation in its preparation was entrusted to Technical Committee EPL/501, Electronic Assembly Technology.

A list of organizations represented on this committee can be obtained on request to its committee manager.

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EUROPEAN STANDARD

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English Version

Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 5-501: General test methods for materials and assemblies - Surface insulation resistance (SIR) testing of solder fluxes (IEC 61189-5-501:2021)

Méthodes d'essai pour les matériaux électriques, les cartes imprimées et autres structures d'interconnexion et ensembles - Partie 5-501: Méthodes d'essai générales pour les matériaux et les ensembles - Essais de résistance d'isolement en surface (RIS) des flux de brasage (IEC 61189-5-501:2021)

Prüfverfahren für Elektromaterialien, Leiterplatten und andere Verbindungsstrukturen und Baugruppen - Teil 5-501: Allgemeine Prüfverfahren für Materialien und Baugruppen - Prüfung des Oberflächenisolationswiderstands (SIR) von Lotflussmitteln (IEC 61189-5-501:2021)

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European foreword

The text of document 91/1645/CDV, future edition 1 of IEC 61189-5-501, prepared by IEC/TC 91 “Electronics assembly technology” was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN IEC 61189-5-501:2021.

The following dates are fixed:

- latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement (dop) 2021-12-02
- latest date by which the national standards conflicting with the document have to be withdrawn (dow) 2024-03-02

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In the official version, for Bibliography, the following notes have to be added for the standards indicated:

IEC 61189-1	NOTE	Harmonized as EN 61189-1
IEC 61189-3	NOTE	Harmonized as EN 61189-3
IEC 61190-1-1	NOTE	Harmonized as EN 61190-1-1
IEC 61190-1-2:2014	NOTE	Harmonized as EN 61190-1-2:2014 (not modified)
IEC 61191-1	NOTE	Harmonized as EN IEC 61191-1
ISO 9455-1	NOTE	Harmonized as EN 29455-1
ISO 9455-2	NOTE	Harmonized as EN ISO 9455-2
ISO 9455-17	NOTE	Harmonized as EN ISO 9455-17

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Annex ZA
(normative)

Normative references to international publications with their corresponding European publications

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

NOTE 1 Where an International Publication has been modified by common modifications, indicated by (mod), the relevant EN/HD applies.

NOTE 2 Up-to-date information on the latest versions of the European Standards listed in this annex is available here: www.cenelec.eu.

Publication	Year	Title	EN/HD	Year
IEC 60068-1	2013	Environmental testing - Part 1: General and guidance	EN 60068-1	2014
IEC 60068-2-58	-	Environmental testing - Part 2–58: Tests - Test Td: Test methods for solderability, resistance to dissolution of metallization and to soldering heat of surface mounting devices (SMD)	EN 60068-2-58	-
IEC 60068-2-67	-	Environmental testing - Part 2–67: Tests - Test Cy: Damp heat, steady-state, accelerated test primarily intended for components	EN 60068-2-67	-
IEC 60068-2-78	-	Environmental testing - Part 2–78: Tests - Test Cab: Damp heat, steady-state	EN 60068-2-78	-
IEC 60194-2	-	Printed boards design, manufacture and assembly - Vocabulary - Part 2: Common usage in electronic technologies as well as printed board and electronic assembly technologies	-	-
IEC 61189-5-504	-	Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 5–504: General test methods for materials and assemblies - Process ionic contamination testing (PICT)	EN IEC 61189-5-504	-

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IEC/TR 61189-5-506	-	Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 5–506: General test methods for materials and assemblies - An intercomparison evaluation to implement the use of fine-pitch test structures for surface insulation resistance (SIR) testing of solder fluxes in accordance with IEC 61189-5-501	-	-
IEC 61190-1-3	-	Attachment materials for electronic assembly - Part 1–3: Requirements for electronic grade solder alloys and fluxed and non-fluxed solid solder for electronic soldering applications	EN IEC 61190-1-3	-
IEC 61249-2-7	-	Materials for printed boards and other interconnecting structures - Part 2–7: Reinforced base materials clad and unclad - Epoxide woven E-glass laminated sheet of defined flammability (vertical burning test), copper-clad	EN 61249-2-7	-

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