

Implementation of
ISO/IEC 9549:1990

Information technology — Galvanic isolation of balanced interchange circuits

Committees responsible for this British Standard

The preparation of this British Standard was entrusted by the Information Systems Technology Standards Policy Committee (IST/-) to Technical Committee IST/6, upon which the following bodies were represented:

- Association for Payment Clearing Services
- British Computer Society
- British Telecommunications plc
- Business Equipment and Information Technology Association
- Department of Trade and Industry (Information Technology Standards Unit)
- Department of Trade and Industry (National Physical Laboratory)
- Electricity Supply Industry in England and Wales
- Electronic Engineering Association
- HM Treasury (Central Computer and Telecommunications Agency)
- Information Technology Users' Standards Association
- Institute of Data Processing Management
- Inter-universities Computing Committee
- Joint Network Team
- National Computing Centre Ltd.
- National Health Service
- OFTEL (Office of Telecommunications)
- Post Office
- Telecommunication Engineering and Manufacturing Association
- Telecommunications Managers' Association

This British Standard, having been prepared under the direction of the Information Systems Technology Standards Policy Committee, was published under the authority of the Board of BSI and comes into effect on 28 February 1991

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National foreword

This British Standard reproduces verbatim ISO/IEC 9549:1990 and implements it as the UK national standard.

This British Standard is published under the direction of the Information Systems Technology Standards Policy Committee whose Technical Committee IST/6 has the responsibility to:

- aid enquirers to understand the text
- present to the responsible international committee any enquiries on interpretation, or proposals for change, and keep UK interests informed;
- monitor related international and European developments and promulgate them in the UK.

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Summary of pages

This document comprises a front cover, an inside front cover, pages i and ii, the ISO/IEC title page, pages ii to iv, pages 1 to 6 and a back cover.

This standard has been updated (see copyright date) and may have had amendments incorporated. This will be indicated in the amendment table on the inside front cover.

INTERNATIONAL STANDARD

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Information technology — Galvanic isolation of balanced interchange circuits

*Systèmes de traitement de l'information — Isolation galvanique des
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