

UTE

Spécification française harmonisée
Système CENELEC d'assurance de la qualité
12, Place des Etats-Unis
75783 PARIS CEDEX 16

Téléphone : (1) 47 23 72 57

Télex : 620434F

CECC 90 101-005

EDITION 1 - Novembre 1987

ISSUE 1 - November 1987



Page 0

Nombre total de pages : 16
Total number of pages :

COMPOSANTS ELECTRONIQUES SOUS ASSURANCE
DE QUALITE CONFORMEMENT A :
*ELECTRONIC COMPONENTS OF ASSESSED QUALITY
IN ACCORDANCE WITH :*

GS : CECC 90 000 (NF C 86-020)

SS : CECC 90 100 (NF C 86-200)

FS : CECC 90 101 (NF C 86-213)

UTE C 86-213/B 25

Adoptée le 16 novembre 1987

BS9403 F073 to F085

Issue 1

October, 1977

DESSIN ET DIMENSIONS :
OUTLINE AND DIMENSIONS :

CEI 191-2 - Types 050 G 04 - 050 G 03
IEC 191-2 - Types 050 G 04 - 050 G 03

Référence française : F 105 - F 105A
French reference :

IDENTIFICATION DES CONNEXIONS
TERMINAL CONNECTIONS

Voir Tableau II
See Table II

SPECIFICATION PARTICULIERE POUR
MICROSTRUCTURES LOGIQUES SELON FS 90 101
DETAIL SPECIFICATION FOR DIGITAL INTEGRATED CIRCUITS IN ACCORDANCE WITH FS 90 101

54/64/74 06

INVERSEURS A SORTIES COLLECTEUR OUVERT
HAUTE TENSION

**HEX INVERTER BUFFER/DRIVERS WITH OPEN
COLLECTOR HIGH VOLTAGE OUTPUTS**

TECHNOLOGIE DE FABRICATION :
TYPICAL CONSTRUCTION :

Circuit monolithique bipolaire au silicium, boîtiers
avec ou sans cavité.

*Silicon monolithic bipolar integrated circuit,
cavity/non-cavity packages.*

NIVEAUX D'ASSURANCE DE QUALITE : P, Y, L
ASSESSMENT LEVELS :

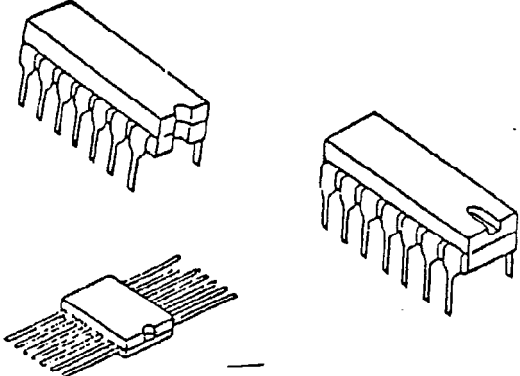
AMENDMENTS TO THE FOLLOWING CLAUSES

- § 2 - Add:
Most negative low level input voltage at an input current : $I_{IKB} = -12 \text{ mA}$, $V_{IKB} = -1,5 \text{ V}$
- § 3 To be replaced by CECC 90 000/3 § 2.5
- § 4 To be deleted
- § 5 To be replaced by CECC 90 000/3 § 2.6
- § 6 To be replaced by CECC 90 000/3 § 4.10.1
- § 7 All inspection level and AQL are those specified in 90 100/3.
Page 7, sub-group A3(a) : for I_{IL} parameter, replace $V_{IN} = 0.8 \text{ V}$ by $V_{IN} = 0.4 \text{ V}$
Replace A3b by A4a, A3c by A4b, A4 by A5.
Add A8 of 90 100/3 (Assessment level P only).
Apply to A2 extreme temperature testing with A7a/A7b 90 000 inspection requirements.
For groups B, C, D, apply 90 100/3.
Sub-groups A6, A7 : not applicable.

Se reporter à la Liste des Produits Homologués CECC 00 200 en vigueur pour connaître les fabricants dont les composants conformes à cette spécification particulière sont homologués.
Information about manufacturers who have components qualified to this detail specification is available in the current CECC 00 200 : Qualified Products List.

**Documents**

Document Sharing Hub

TEXAS INSTRUMENTS LIMITED, MANTON LANE, BEDFORD. TELEPHONE: BEDFORD 67466 TELEX: 82178	BS 9403 F073 to F085 ISSUE I, October 1977
ELECTRONIC COMPONENTS OF ASSESSED QUALITY : DETAIL SPECIFICATION	
Outlines not to scale  For detail dimensions 14 Lead Flat-Pack - see Appendix I 14 Lead Dual-in-Line - BS 3934 SO-87B	SILICON MONOLITHIC TTL HEX. INVERTER BUFFER/ DRIVERS WITH OPEN COLLECTOR HIGH VOLTAGE OUTPUTS
	FULL ASSESSMENT LEVEL AND APPLICATION CATEGORIES S1, S2, S3 AND S4
	TYPES 5406 6406 7406 See Clause 5
	CERAMIC/GLASS HERMETICALLY SEALED ENCAPSULATION OR DUAL-IN-LINE PLASTIC ENCAPSULATION

1. LIMITING CONDITIONS OF USE (Not for inspection purposes)

Absolute values (non-simultaneous)

V_{CC}	Maximum continuous positive supply voltage	7.0	V
	Maximum continuous negative supply voltage	0.5	V
	Maximum continuous positive input voltage	5.5	V
	Maximum continuous negative input voltage	0.5	V
	Maximum collector supply voltage	30.0	V
$-I_{IL}$	Maximum input current	1.6	mA
	Maximum reverse input current	1.0	mA
T_{amb}	Ambient operating temperature range	5406	-55 to +125 °C
		6406	-40 to + 85 °C
		7406	0 to + 70 °C
T_{stg}	Storage temperature range		-65 to +150 °C
	Shock (0.5 ms duration)		14 700 m/s ²
	Vibration		196 m/s ²

BS 9403 F073-F085
ISSUE 1,
JANUARY, 1977

2. RECOMMENDED CONDITIONS OF USE AND ASSOCIATED CHARACTERISTICS

(Not for inspection purposes)

These characteristics apply over the ambient operating temperature range unless otherwise stated.

			Min.	Max.	Unit.
V_{CC}	Positive supply voltage	5406	4.5	5.5	V
		6406 & 7406	4.75	5.25	V
V_{IH}	HIGH state input voltage	5406	2.0	5.5	V
		6406 & 7406	2.0	5.25	V
I_{CCL}	Total supply current per package at V_{CC} max. value LOW state output		-	51.0	mA
I_{CCH}	Total supply current per package at V_{CC} max. HIGH state output		-	48.0	mA
V_{OH}	HIGH state output voltage		-	30.0	V
V_{IL}	LOW state input voltage		-	0.8	V
V_{OL}	LOW state output voltage		-	0.7	V
$I_{IH(1)}$	HIGH state input current V_{IH} = maximum value		-	1.0	mA
$I_{IH(2)}$	HIGH state input current V_{OH} = 2.4 V		-	40.0	μ A
$-I_{IL}$	LOW state input current V_{OL} = 0.4 V (per load)		-	1.6	mA
I_{OL}	LOW state output current	5406	-	30.0	mA
		6406 & 7406	-	40.0	mA
$-I_{OHX}$	HIGH state output current		-	250.0	μ A
t_{PHL}	HIGH to LOW state propagation time at 25 °C C_L = 15 pF R_L = 110 Ω		-	23.0	ns
t_{PLH}	LOW to HIGH state propagation time at 25 °C C_L = 15 pF R_L = 110 Ω		-	15.0	ns

BS 9403 F073 to F085
ISSUE 1,
JANUARY, 1977

TABLE I

FUNCTION TABLE (each inverter)

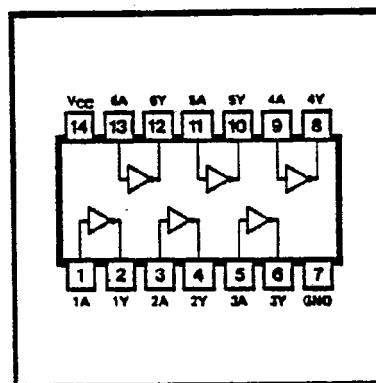
INPUTS	OUTPUT
L	H
H	L

H = HIGH state

L = LOW state

TABLE II

LOGIC CONNECTIONS



BS 9403 F073 to F085

ISSUE I,
JANUARY, 1977

3. MARKING

3.1 Each integrated circuit shall bear the following markings :

- (a) Terminal Identification.
- (b) The Manufacturer's type number.
- (c) The factory identification code.
- (d) The date code.

3.2. Each package containing one or more of these circuits shall bear the markings (b), (c) and (d) above and in addition :

- (e) The B.S. detail specification number.

4. RELATED DOCUMENTS

This specification shall be read in conjunction with :

BS 3934 'Dimensions of Semiconductor Devices'.

BS 6001 'Sampling Procedures and Tables for Inspection by Attributes'.

BS 9000 'General Requirements for Electronic Components of Assessed Quality'.

PD 9002 'Qualified Products List for Electronic Components of Assessed Quality'.

BS 9400 'Integrated Electronic Circuits of Assessed Quality : Generic Data and Methods of Test'.

5. ORDERING INFORMATION

Orders for these integrated circuits shall contain the following minimum information :

The integrated circuit type and package identification suffix to BS 9403 as defined below :

TYPE		APPLICATION CATEGORY				FULL ASSESSMENT LEVEL
		S1	S2	S3	S4	
<u>CERAMIC</u> Dual-in-line (J) and Flat-pack (W)	5406	F073	F074	F075	F076	F077
	6406		F078	F079	F080	F081
	7406		F082	F083	F084	F085
<u>PLASTIC</u> Dual-in-line (N)	5406					F077
	6406					F081
	7406					F085
IDENTIFICATION PREFIX			SNB	SNN	SNX	SNS

EXAMPLE:

SNB 5406J to BS 9403 F074 defines a 5406 ceramic dual-in-line device to Application Category S2.