

BRITISH STANDARDS INSTITUTION, 2 PARK STREET, LONDON, W1A 2BS	Page 1 of 16	CECC 90102 - 014 ISSUE 1 NOVEMBER 1981	
ELECTRONIC COMPONENTS OF ASSESSED QUALITY IN ACCORDANCE WITH: BS CECC 90 000 BS CECC 90 100 and family specification for TTL Schottky-circuits (BS CECC 90102)	BS CECC 90102 - 014 ISSUE 1 NOVEMBER 1981		
Detail specification for Quadruple 2-input positive And gates Device types: 54S08 64S08 74S08 84S08			
TYPICAL CONSTRUCTION Silicon monolithic bipolar Cavity and non-cavity packages			
Outline and dimensions Refer to IEC 191 1-50a, type A 50 B Dual-in-Line (plastic or ceramic) refer to Appendix A1.3 Flatpack (metal or ceramic)	Assessment levels R, S, T, V		

1. Type description

1.1 Truth table

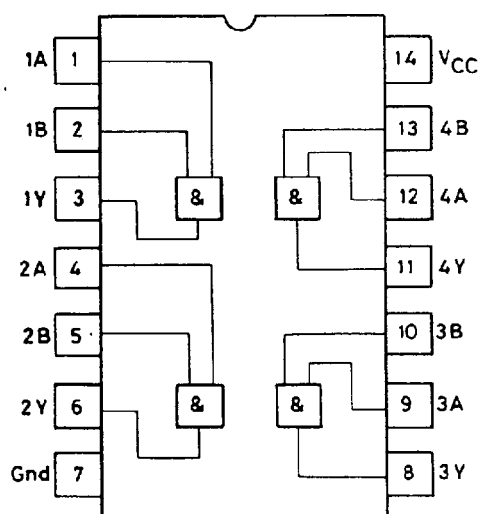
INPUTS		OUTPUT
A	B	
L	L	L
L	H	L
H	L	L
H	H	H

H = High level
L = Low level

See relevant Qualified Products List for the availability
of components qualified to this detail specification

1.2 Terminal identification, type designation and marking

1.2.1 Terminal identification



Quadruple 2-input positive And gates

1.2.2 Type designation

TEMPERATURE RANGE	PACKAGE		
	Ceramic Dual-in-Line	Plastic Dual-in-Line	Ceramic Flatpack
0 + 70	74S08J	N	W
-25 + 85	84S08J		
-40 + 85	64S08J		
-55 +125	54S08J		

1.2.3 Marking

Each integrated circuit shall bear the following markings:

- (1) Terminal identification
- (2) Type designation
- (3) Date code
- (4) Factory identification code (unless derived from trademark)
- (5) Manufacturers trade name or mark
- (6) Mark of conformity (unless a certificate of conformity is used)

1.3 Ordering information

Orders for these integrated circuits shall contain the following minimum information:

Manufacturers integrated circuit type designation, screening procedures and assessment levels as detailed to this CECC specification number.

(For details of screening procedures see Appendix A1.1).

2. Limiting conditions of use (not for inspection purposes)

2.1	Minimum and maximum continuous supply voltage	V_{CC}						-0,5V +7,0V
2.2	Minimum and maximum input voltages							
2.2.1	Minimum and maximum input voltage	V_I						-0,5V +5,5V
2.2.2	Maximum input voltage between multiple emitter transistor inputs	V_{II}						+5,5V
				54S	64S	74S	84S	
2.3	Minimum and maximum operating ambient temperatures	$T_{amb}(^{\circ}C)$	min. max.	-55 +125	-40 +85	0 +70	-25 +85	
2.4	Minimum and maximum storage temperatures	$T_{stg}(^{\circ}C)$	min. max.	-65 +150	(All types)			

3. Recommended operating conditions and associated characteristics (not for inspection purposes)

3.1 Recommended operating conditions.

These apply over the operating ambient temperature range unless otherwise stated.

Description	Symbol	Type	Value	Unit
Positive supply voltage range	V_{CC}	54S 64/74/84S	4,5 - 5,5 4,75 - 5,25	V V
Minimum value of High level input voltage	V_{IHB}	All types	2,0	V

3.1 (contd)

Description	Symbol	Type	Value	Unit
Maximum value of High level input voltage	V_{IHA}	All types	5,5	V
Minimum value of Low level input voltage	V_{ILB}	All types	0	V
Maximum value of Low level input voltage	V_{ILA}	All types	0,8	V
Maximum value of Low level output current for each output	I_{OLA}	All types	20,0	mA
Maximum value of High level output current	I_{OHA}	All types	-1,0	mA
D.C. noise margin at Low level ($V_{ILA} - V_{OLA}$)	V_{NL}	All types	0,3	V
at High level ($V_{OHB} - V_{IHB}$)	V_{NH}	54S	0,5	V
		64/74/84S	0,7	V

3.2 Associated characteristics

3.2.1 Minimum and maximum values are given for the worst case operation over the ambient temperature range, except for those conditions marked with an asterisk (*)

	54S	64S	74S	84S	
T _{amb} min.	-55	-40	0	-25	°C
T _{amb} max.	+125	+85	+70	+85	
V _{CC} min.	4,5	4,75			V
V _{CC} max.	5,5	5,25			

3.2.2

Characteristics	Conditions	$V_{CC}(V)$	Symbol	Min.	Max.	Unit
Total supply current at High level	$V_{IN}(1)$ to give Low level output	V_{CC} max.	I_{CCH}	-	32,0	mA
Total supply current at Low level	$V_{IN}(1)$ to give High level output	V_{CC} max.	I_{CCL}	-	57,0	mA
High level input current	$V_{IH}(1) = 5,5V$	V_{CC} max.	$I_{IH}(1)$	-	1,0	mA
	$V_{IH}(2) = 2,7V$	V_{CC} max.	$I_{IH}(2)$	-	50,0	μA
Low level input current	$V_{IL} = 0,5V$	V_{CC} max.	$-I_{IL}$	-	2,0	mA
Output voltage	54S $-I_{OH} = 1,0mA$ 64/74/84S $-I_{OH} = 1,0mA$ All types $I_{OL} = 20,0mA^{(2)}$	V_{CC} min.	V_{OH}	2,5	-	V
			V_{OH}	2,7	-	V
			$V_{OL}(1)$	-	0,5	V
Output short circuit current	$V_{IN} = V_{CC}$ min. ⁽¹⁾ $V_O = 0V$ ⁽³⁾	V_{CC} max.	$-I_{OS}$	40,0	100,0	mA
Propagation times	$T_{amb} = 25^{\circ}C^*$ test conditions as in Sub-group A5	5,0V				
High to Low level			t_{PHL}	-	7,5	ns
Low to High level			t_{PLH}	-	7,0	ns

- (1) To all inputs, (2) Fan out = 10,
 (3) Only one output at a time, duration $t \leq 1$ sec
 *See clauses 3.1 and 3.2.1 of this specification