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EIA STANDARD

EIA-62391-2

Fixed Electric Double-Layer Capacitors for Use in Electronic Equipment – Part 2: Sectional Specification – Electric Double- Layer Capacitors for Power Application

EIA-62391-2

(IEC 62391-2:2006, IDT)

October 2014



Electronic Components Industry Association

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

**FIXED ELECTRIC DOUBLE-LAYER CAPACITORS
FOR USE IN ELECTRONIC EQUIPMENT –****Part 2: Sectional specification –
Electric double layer capacitors for power application**

FOREWORD

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International Standard IEC 62391-2 has been prepared by IEC technical committee 40: Capacitors and resistors for electronic equipment.

This bilingual version corresponds to the monolingual English version, published in 2006-04.

The text of this standard is based on the following documents:

FDIS	Report on voting
40/1641/FDIS	40/1713/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

The French version of this standard has not been voted upon.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

IEC 62391 consists of the following parts, under the general title *Fixed electric double-layer capacitors for use in electronic equipment*:

Part 1: Generic specification

Part 2: Sectional specification – Electric double-layer capacitors for power application

The sectional specification mentioned above does have a blank detail specification being a supplementary document, containing requirements for style, layout and minimum content of detail specifications.

The committee has decided that the contents of this publication will remain unchanged until the maintenance result date indicated on the IEC web site under "<http://webstore.iec.ch>" in the data related to the specific publication. At this date, the publication will be

- reconfirmed;
- withdrawn;
- replaced by a revised edition, or
- amended.

FIXED ELECTRIC DOUBLE-LAYER CAPACITORS FOR USE IN ELECTRONIC EQUIPMENT –

Part 2: Sectional specification – Electric double-layer capacitors for power application

1 General

1.1 Scope

This part of IEC 62391 applies to electric double-layer capacitors for power application.

Electric double-layer capacitors for power are intended for applications that require discharge currents in the range from mA to A. The characteristics of the capacitors include such performance as relatively high capacitance and low internal resistance, which is applicable to Class 3 of the measurement classification specified in IEC 62391-1.

The definition of power density and its calculating procedure should be in accordance with Annex A.

1.2 Object

The object of this standard is to prescribe preferred ratings and characteristics and to select from IEC 62391-1 the appropriate quality assessment procedures, tests and measuring methods and to give general performance requirements for this type of capacitor. Test severities and requirements prescribed in detail specifications referring to this sectional specification shall be of equal or higher performance level; lower performance levels are not permitted.

1.3 Normative references

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60063, *Preferred number series for resistors and capacitors*

IEC 60068-1, *Environmental testing – Part 1: General and guidance*

IEC 60384-1, *Fixed capacitors for use in electronic equipment – Part 1: Generic specification*

IEC 60410, *Sampling plans and procedures for inspection by attributes*

IEC 62391-1, *Fixed electric double-layer capacitors for use in electronic equipment – Part 1: Generic specification*

IEC 62391-2-1, *Fixed electric double-layer capacitors for use in electronic equipment – Part 2-1: Electric double-layer capacitors for power application – Assessment level EZ*

ISO 3, *Preferred numbers – Series of preferred numbers*

1.4 Information to be given in a detail specification

Detail specifications shall be derived from the relevant blank detail specification.

Detail specifications shall not specify requirements inferior to those of the generic, sectional or blank detail specification. When more severe requirements are included, they shall be listed in 1.9 of the detail specification and indicated in the test schedules, for example by an asterisk.

NOTE The information given in 1.4.1 may, for convenience, be presented in tabular form.

The following information shall be given in each detail specification and the values quoted shall preferably be selected from those given in the appropriate clause of this sectional specification.

1.4.1 Outline drawing and dimensions

There shall be an illustration of the surface mount capacitors as an aid to easy recognition and for comparison of the surface mount capacitors with others.

Dimensions and their associated tolerances, which affect interchangeability and mounting, shall be given in the detail specification. All dimensions shall preferably be stated in millimetres; however, when the original dimensions are given in inches, the converted metric dimensions in millimetres shall be added.

Normally, numerical values shall be given for the length of the body, the width and height of the body and wire spacing, or, for cylindrical types, the body diameter and length, and the length and diameter of the terminations. When necessary, for example, when a number of case sizes are covered by a detail specification, the dimensions and their associated tolerances shall be placed in a table below the drawing.

When the configuration is other than described above, the detail specification shall state such dimensional information as will adequately describe the capacitor. When the capacitor is not designed for use on printed boards, this shall be clearly stated in the detail specification.

1.4.2 Mounting

The detail specification shall specify the method of mounting to be applied for normal use and for the application of the vibration and the bump or shock test. The capacitors shall be mounted by their normal means. The design of the capacitor may be such that special mounting fixtures are required for its use. In this case, the detail specification shall describe the mounting fixtures and they shall be used in the application of the vibration and bump or shock tests.

1.4.3 Rating and characteristics

The ratings and characteristics shall be in accordance with the relevant clauses of this specification, together with the following.

1.4.3.1 Rated capacitance range

See 2.2.1.

NOTE When products approved to the detail specification have different ranges, the following statement should be added: "The range of capacitance values available in each voltage range is given in IEC QC 001005."

1.4.3.2 Particular characteristics

Additional characteristics may be listed, when they are considered necessary to specify adequately the component for design and application purposes.

1.4.3.3 Soldering

The detail specification shall prescribe the test methods, severities and requirements applicable to the solderability and the resistance to soldering heat tests.

1.4.4 Marking

The detail specification shall specify the content of the marking on the capacitor and on the package. Deviations from 1.6 of this sectional specification shall be specifically stated.

1.5 Terminology

For the purposes of this document, the terms and definitions of IEC 62391-1, together with the following, apply.

1.5.1

surface mount capacitor

capacitor whose small dimensions and nature or shape of terminations make it suitable for surface mounting

1.5.2

electric double layer capacitors for power application

capacitors intended for the applications that require discharge currents in the range from mA to A

NOTE The characteristics of the capacitors include such performance as relatively high capacitance and low internal resistance, which is applicable to Class 3 of the measurement classification specified in IEC 62391-1.

1.6 Marking

IEC 62391-1, 2.4, applies with the following details.

1.6.1 The information given in the marking is normally selected from the following list; the relative importance of each item is indicated by its position in the list:

- a) polarity of the terminations (unless identified by the construction);
- b) rated capacitance;
- c) rated voltage (d.c. voltage may be indicated by the symbol --- or —);
- d) style (in accordance with 1.1);
- e) year and month (or week) of manufacture;
- f) manufacturer's name or trade mark;
- g) manufacturer's type designation;
- h) classification of processing lead terminals (if applicable).

1.6.2 Capacitors shall bear a), b) and c) in 1.6.1, and as many of the rest of the items as possible which should be as legible as possible. Indication of marked items on a capacitor shall not overlap.

1.6.3 Any marking shall be legible and not easily smeared or removed by rubbing with the finger.