



ANSI/EIA-956-2006
Approved: December 12, 2006

EIA STANDARD

ALUMINUM ELECTROLYTIC CHIP CAPACITOR WITH POLMER CATHODE

EIA/ECA-956

DECEMBER 2006



Electronic Components, Assemblies & Materials Association

THE ELECTRONIC COMPONENTS SECTOR OF THE ELECTRONIC INDUSTRIES ALLIANCE



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(From Standards Proposal No. 4985 formulated under the cognizance of the P-2.2 Committee on Aluminum, Film and Mica Capacitors).

Published by

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Technology Strategy & Standards Department
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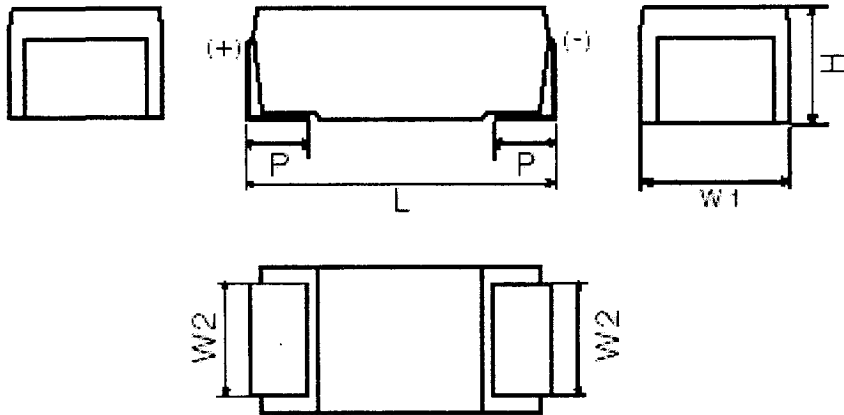
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ELECTRONICS INDUSTRIES ASSOCIATION	EIA/ECA-956								
Outline Drawing: (Third Angle Projection)	Aluminum Electrolytic Chip Capacitor with Polymer Cathode								
									
(Other shapes permitted within the dimensions given) <u>Notes:</u> 1. Dimensions - see Table 1. 2. Recommended Land Patterns -- see 1.1 3. Markings - see 3.0	Polar, Nonhermetically Sealed Chip Capacitor with Conductive Polymer Electrical Circuit (-) (+) •-----) -----•								
<u>Specifications:</u> <table> <tr> <td>Rated Capacitance Range</td><td>2.2 to 470μF (120Hz 20°C)</td></tr> <tr> <td>Capacitance Tolerance</td><td>$\pm$20% (120Hz 20°C)</td></tr> <tr> <td>Rated Voltage Range</td><td>2 ~ 16 VDC</td></tr> <tr> <td>Category Temperature Range</td><td>- 40°C ~ 105°C</td></tr> </table>		Rated Capacitance Range	2.2 to 470 μ F (120Hz 20°C)	Capacitance Tolerance	$\pm$ 20% (120Hz 20°C)	Rated Voltage Range	2 ~ 16 VDC	Category Temperature Range	- 40°C ~ 105°C
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