



12 V 200 W OBD2 DC/DC – Converter

1 Introduction	4
1.1 Purpose	4
1.2 Applicability.....	4
1.3 Remarks	4
2 References.....	4
2.1 External Standards/Specifications	4
2.2 GM Standards/Specifications	4
2.3 Additional References	4
3 Requirements.....	5
3.1 System/Subsystem/Component/Part Definition.....	5
3.1.1 Mounting Surfaces.....	5
3.1.2 Housing View (Package).....	5
3.1.3 Appearance & ME Requirements	5
3.1.3.1 Attachments	5
3.1.4 Finish	5
3.1.5 Content.....	6
3.1.5.1 Physical Content	6
3.1.5.2 Functional Content	6
3.1.6 DC/DC Converter Voltage Support.....	7
3.1.6.1 DC/DC Converter Parasitic Current	7
3.1.6.2 DC/DC Converter Overload Protection	7
3.1.6.3 Converter Functional Overview.....	8
3.1.6.4 Power Output Cycle Timing	8
3.1.6.5 Ramping Output Voltage Characteristics	8
3.1.6.6 Output Voltage Leveling after AutoCrank.....	9
3.1.6.7 OBD2 Functions.....	12
3.1.6.8 Watchdog Timer Functionality	13
3.1.7 Operating Modes	13
3.1.7.1 Wake-Up Mode	13
3.1.7.2 Sleep Mode.....	13
3.1.7.3 Auto Start Mode	13
3.1.8 Design Guidelines.....	13

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September 2015

Page 1 of 37



Documents
Document Sharing Hub

3.1.9 Ambient Environment	13
3.1.10 Interfaces.....	13
3.3 Design and Construction	13
3.3.1 Materials, Processes and Parts Selection Guidelines	13
3.3.1.1 Material Guidelines.....	13
3.3.1.2 Processes Guidelines.....	14
3.3.1.3 Parts Guidelines.....	14
3.3.2 Design Guidelines and Constraints	14
3.3.3 Identification and Marking	14
3.3.3.1 Part, Component and Module Identification and Traceability	14
3.3.3.2 GM Trademark.....	15
3.3.4 Mechanical Requirements.....	15
3.3.4.1 Mass.....	15
3.3.4.2 Basic DC/DC Converter Dimensions (envelope shape).....	15
3.3.4.3 Packaging Envelope for DC/DC Converter	15
3.3.4.4 Packaging Designation.....	15
3.3.5 Serial Interface	15
3.3.5.1 Powertrain Expansion Bus.....	15
3.3.6 Diagnostics.....	16
3.3.6.1 DC Converter Actuator Voltage ADC Value	16
3.3.6.2 DC Converter Actuator Voltage ADC Value Alive Rolling Count.....	17
3.3.6.3 DC Converter Actuator Voltage ADC Value Protection Value.....	17
3.3.6.4 DC Converter Internal Health Status.....	17
3.3.6.5 Central Processing Unit Register(s) Fault	17
3.3.6.6 PSW (Program Status Word) Register Fault	17
3.3.6.7 Stack Register Fault	18
3.3.6.8 Flash Fault	18
3.3.6.9 Ram Fault	18
3.3.6.10 DC Converter Internal Health Status Alive Rolling Count.....	18
3.3.6.11 DC Converter Internal Health Status Protection Value	18
3.3.6.12 DC Converter Run Crank Terminal Status	19
3.3.6.13 DC Converter Run Crank Terminal Status Alive Rolling Count.....	19
3.3.6.14 DC Converter Run Crank Terminal Status Protection Value.....	19
3.3.6.15 DC Converter Crank Control Terminal Status	19
3.3.6.16 DC Converter Crank Control Terminal Status Alive Rolling Count	19
3.3.6.17 DC Converter Crank Control Terminal Status Protection Value	19
3.3.6.19 DC Converter Over Temperature Self Protection Active.....	20
3.3.6.20 PT-EXT BUS DC/DC Signal Overview (Tx-signals).....	20
3.3.7.1 Functional Checks during Vehicle Production	22

GM Confidential

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September 2015

Page 2 of 37

**Documents**

Document Sharing Hub

3.3.8 Electrical Connection Requirements.....	23
3.3.8.1 DC/DC Converter Connector Pin-Out	23
3.3.8.2 Proposal: Connector requirements – Harness Interface	23
3.3.8.3 DC/DC Harness Connector	24
3.3.8.4 Electrical Mechanization.....	24
3.3.8.5 DC/DC Converter Connector Pin-Out	25
3.3.8.6 Extended Crank Relay Control Signal Input Function - Pulse Solution.....	28
3.3.8.7 Output Switching Variants	29
3.3.8.8 Failsafe/Diagnostics	29
3.3.8.9 Fusing.....	29
3.3.9 General Voltage Stabilization Requirements	29
3.3.9.1 Cross Wire Definition.....	30
3.3.10 Conditions	31
3.3.10.1 Environmental Conditions.....	31
3.3.10.2 Temperature Requirements.....	31
3.3.10.3 Electromagnetic Compatibility.....	31
3.9.11 Test Conditions.....	31
3.9.11.1 Test Pulse.....	31
3.11 Service Requirements.....	32
3.11.1 Serviceability	32
3.11.1.1 Service Infrastructure	32
3.11.1.2 Roadside Services	33
3.11.1.3 Maintenance	33
3.11.1.4 Repair	33
3.11.1.5 Plates, Labels, and Identification Stamps.....	34
3.11.1.6 Service Parts Engineering	34
4 Validation	35
4.1 Validation Test Plan	35
4.2 A/D/V Plan and VCRI	35
4.3 Facility Information.....	35
4.4 EMC	35
4.5 Power Temperature Cycling and Thermal Shock.....	35
4.6 Software Validation Test	36
4.7 Highly Accelerated Life Test (HALT).....	36
4.8 DFMEA, DRBFM and DRBTR.....	36
4.9 Test Reports	36
4.10 Continued Compliance.....	37
5 Provisions for Shipping.....	37
5.1 ESD Immunity.....	37

GM Confidential

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September 2015

Page 3 of 37

5.2 Physical Integrity.....	37
6 Notes	37
6.1 Glossary	37
6.2 Acronyms, Abbreviations, and Symbols.....	37
7 Additional Paragraphs	37
8 Coding System.....	37
9 Release and Revisions.....	37

1 Introduction

Note: Nothing in this standard supercedes applicable laws and regulations.

Note: In the event of conflict between the English and domestic language, the English language shall take precedence.

1.1 Purpose. This standard establishes the technical requirements for a 200 W DC/DC boost converter, which shall be integrated into the Global-A network architecture especially for OBD2 application.

1.2 Applicability. The DC/DC converter provides continuous power to all infotainment and communication devices to achieve uninterrupted performance from radios, CD players etc. It shall also be used with Navigation systems, which may run into boot mode after engine cranking due to voltage drop effects. Continuously reporting the internal functional and health status to the OBD2 master makes this variant compliant with OBD2 requirements.

1.3 Remarks. This standard is applicable to GM vehicles, which use the Electrical Architecture, produced for sale in the GM North American, Latin American, European, and Asian Pacific regions as early as Model Year 2016.

The 200 W DC-DC Converter will contain one each of the following:

- Converter Module
- Attachment Provision

2 References

Note: Only the latest approved standards are applicable unless otherwise specified.

2.1 External Standards/Specifications.

ISO 11469 ISO/IEC 17025

2.2 GM Standards/Specifications.

GMW3116	GMW3172	GMW14082	GMW15862
GMW3059	GMW3191	GMW14241	GMW16331
GMW3091	GMW3232	GMW14334	
GMW3097	GMW4710	GMW14573	
GMW3103	GMW8287	GMW15010	

2.3 Additional References.

AIAG QS-9000
GMSAE156
GM1829
GM8272
TLE-050046

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Page 4 of 37



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3 Requirements

3.1 System/Subsystem/Component/Part Definition. The required physical contents for the Subsystem are identified in the following paragraphs.

12 V StopStart vehicles will require some means of supporting the electrical loads during an AutoStart event to prevent the system voltage from voltage drops, which might produce boot/reset effects on control units or malfunction on mechatronic parts. This unit uses a boost converter circuitry, which allows the direct connection to the vehicle battery. The DC/DC Converter, which is mounted in the interior compartment or trunk, shall be used to provide stabilized DC output power for Stop-/Start appliances. The subsystem integration must comply with TLE-050046.

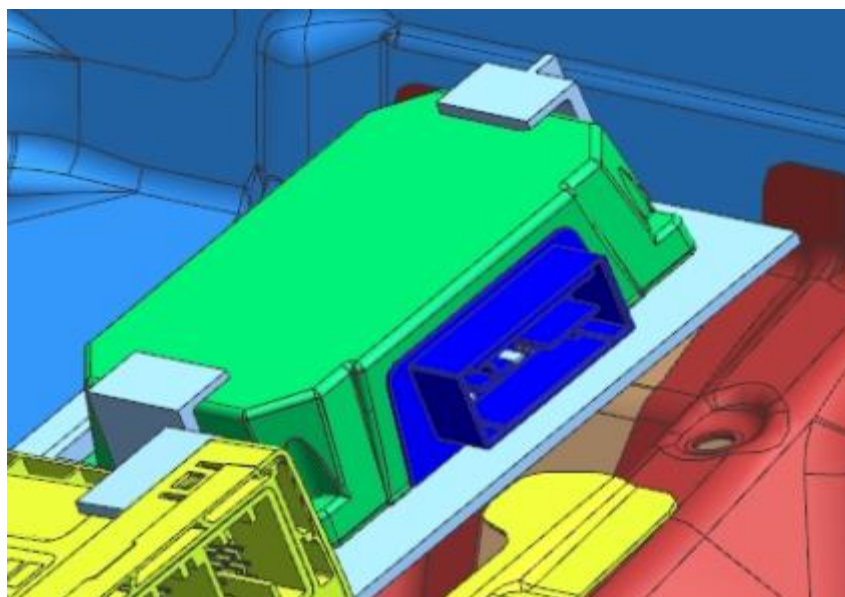
3.1.1 Mounting Surfaces. The DC/DC Converter shall be mounted using an additional bracket in snap in technology. If it is not possible due to mounting surface to ensure a safe mounting (depending on Car Line requirements) also additional fixation features shall be foreseen on the housing.

Mounting holes shall be provided in the packaging envelope dimension, there shall be no external flap or similar. Additional mounting features must meet the housing material to avoid wearing, break or loosening effects.

- The subsystem components shall mechanically interface with the DC/DC converter.
- The DC/DC metal case parts shall galvanically be compatible with ferrous and non-ferrous metals and plating and coatings commonly used in automotive structures and fasteners in order to minimize corrosion (no galvanic effect with other mounting components)
- If the snap in solution is not applicable, the DC/DC Converter case shall provide four M6 thread holes, which should withstand a torque of (4 ± 1) Nm clamping force without performance degradation or case damage. The holes shall be provided without negative impact on the leak tightness.

3.1.2 Housing View (Package). See Figure 1.

Figure 1: NG200 W DC/DC Package with Bracket



3.1.3 Appearance & ME Requirements. The DC/DC Converter case shall have no sharp corners.

Plastic Housing: The DC/DC Converter case shall have a non-reflective black, gray, or silver exterior.

Aluminum Housing: The DC/DC Converter aluminum housing shall not be processed (painted, etc.) in any way to have full cooling functionality and keep the aluminum looking.

3.1.3.1 Attachments. Fasteners selected by the platform shall meet the requirement for 20 mm bolt head and tool clearance for tightening.

3.1.4 Finish. The DC/DC converter shall have smooth corners and edges for all exposed surfaces.

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Page 5 of 37

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3.1.5 Content.

3.1.5.1 Physical Content. The converter assembly packaging and dimensional requirements shall be specified in the packaging drawing and 3D math data.

The DC/DC Converter consists of the following major items, microcontroller(s), sensing components, PT-Expansion data bus.

- An identical DC/DC printed circuit board (PCB) shall be used to meet the necessary functionality required by different platforms including variants in performance.
- The DC/DC microcontroller shall come from a family where an upgraded microcontroller with the same footprint but more resources is available and be qualified prior to Integration Vehicle Bench deliveries.
- A bypass shall provide a low impedance connection from Bat+ to consumer Bat+ input.

The converter shall provide reverse polarity protection non-sensitive to voltage peaks and/or voltage fluctuation.

3.1.5.1.1 Hardware/Software Variants.

- The DC/DC Converter shall meet with 3D-reference data coming from GM, if first application was already introduced at GM to be able to provide required electrical interfaces and matching requirements.
- Dependent on technical requirements different part numbers shall be provided.

3.1.5.1.2 Pre-Fuse. The DC/DC converter requires separate external Pre-Fuses (not included in the DC/DC module!).

The maximum input continuous current is 44 A at 6.5 V input voltage. The input current depends on the battery input voltage assuming a DC/DC efficiency of $\geq 85\%$ at 200 W (2 s) and 85 % at 240 W (200 ms).

3.1.5.1.3 DC Manufacturing Guidelines. The manufacturing process shall comprehend the following guidelines and constraints:

- Eliminate PCB re-work operations
- Eliminate hand soldering of components

The manufacturing process shall comprehend the following guidelines and constraints:

- Limit / minimize board flexure in PCB separation process
- Limit / minimize fatigue damage from manufacturing process, e.g. sonic welding, cutting, sawing, routing, etc.
- Minimize part handling / damage from handling, stacking, etc.
- Eliminate / reduce potential for ionic contaminants in PCB assembly
- Assure 100 % coverage in PCB through-holes for solder flux process
- Control / minimize build variation in manufacture process
- Consider error-proof methods in manufacture / assembly
- Avoid wave soldering

3.1.5.2 Functional Content. (See Figure 2).

- The DC/DC Converter shall be capable to provide a stabilized output voltage during cranking, which requires a maximum power of 240 W for 200 ms maximum at 6.5 V and 200 W for 2 s maximum at 6.0 V.
- The controller shall provide internal signal monitoring and data transfer via Powertrain Expansion Bus.
- The DC/DC converter shall not be reprogrammable without disfiguring the device.
- The DC/DC Converter shall have a reduced quiescent current consumption during sleep mode (100 μ A).
- The efficiency of the Boost converter shall be $\geq 85\%$ over the entire operation input voltage range
- The converter hardware shall be protected by current limitation against short circuits and high current during the stabilization mode.
- High or low voltage events shall not be used for software controlled shutoffs. The DC/DC shall always continue to work within parameters, which are not hazardous to the DC/DC.
- The DC/DC shall survive cranking voltage transients of $T \leq 2$ s duration.

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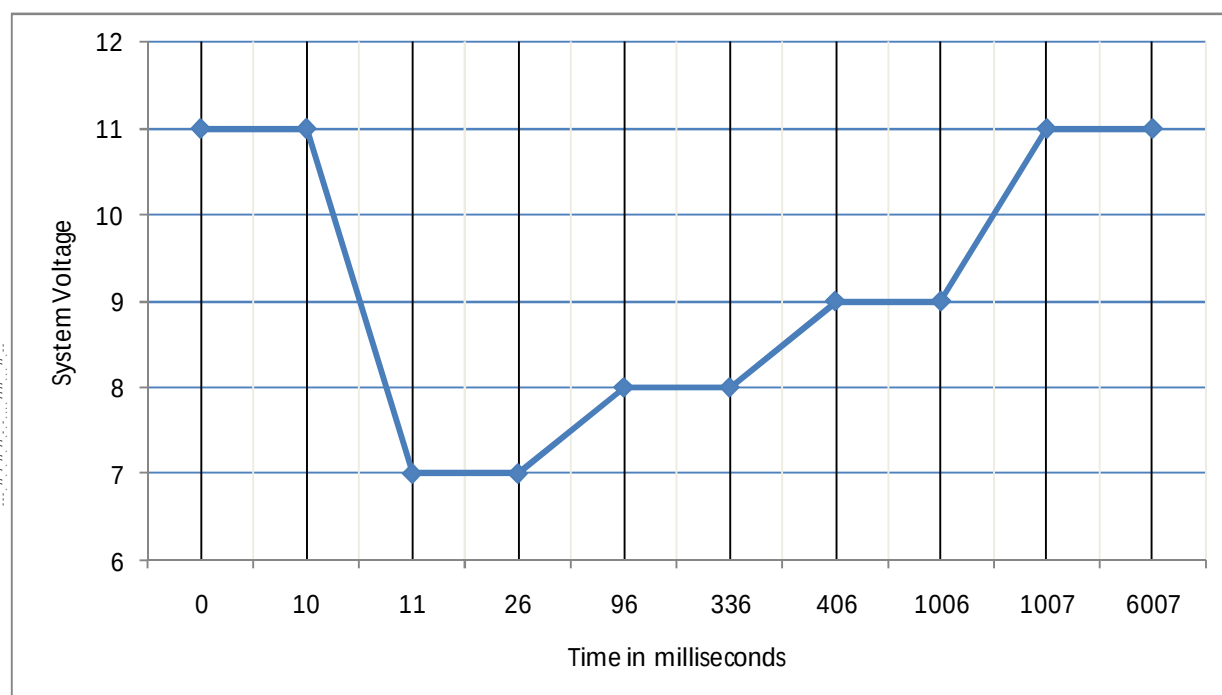
Page 6 of 37



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Figure 2: Functional Content



- i. The software shall be the same for all different hardware variants as long as different functionality is not requested by the GM engineer.
- j. The DC/DC converter shall be easily integrated into all existing GM PPEI architectures.
- k. The DC/DC converter connection system shall be designed to GMW3191.
- l. The DC/DC converter shall be capable of a minimum of 10 installation/Cycles without any degradation or loss of performance.
- m. The DC/DC converter shall provide microprocessor and peripheral health/integrity data to the ECM according to paragraph 3.3.6.
- n. The DC/DC converter shall provide un-scaled, un-filtered, raw sensor ADC values to the ECM.
- o. The DC/DC converter shall communicate with the ECM via the Powertrain Expansion Bus.
- p. The DC/DC shall not interfere with other electronic components by radiation or electromagnetic emissions (EMC).

3.1.6 DC/DC Converter Voltage Support. The DC/DC provides stabilized output voltage level during cranking to allow voltage sensitive functions to operate when battery power drops down for 2 s. To be able to achieve this function, the DC/DC uses a boost circuitry without additional need for charging storage elements, like caps.

3.1.6.1 DC/DC Converter Parasitic Current. The parasitic current during operation shall be < 500 μ A as measured with operating voltage of 12.8 V at +25 °C. Parasitic current is defined as the time average value current measured with the external electrical load switched off. The CAN communication shall be inactive during that test.

3.1.6.2 DC/DC Converter Overload Protection.

- a. All inputs and outputs shall be capable of sustaining an open circuit.
- b. All inputs and outputs, except grounds, shall be capable of direct or resistive shorts to battery voltage.
- c. Each I/O pin shall be capable of direct or resistive shorts to ground without sustaining damage.
- d. Upon removal of any of the fault conditions a through c above the particular input or output shall return to normal behavior within 100 ms (500 ms in Sleep Mode) without requiring any operator action or hardware reset.
- e. The DC/DC Converter shall not be damaged due to shorts in output circuitry.
- f. Polyfuses are not suitable for this technical application, because of the high output current (17 A nominal, 20 A maximum at 12 V output).

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Page 7 of 37



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3.1.6.3 Converter Functional Overview.

- The DC/DC converter shall provide a system voltage stabilization for supported components, which are sensitive to voltage drops during an AutoStart event
- The output voltage shall be kept at a constant level during cranking. Specific algorithms ensure, that the customer doesn't take notice of dimming or flickering effects before, during and after the cranking ($\Delta U = \pm 1.0$ V maximum)
- Cold start functionality shall be inhibited by using the Ignition (Run/Crank) and crank control input signals
- The DC/DC shall always attempt to continue to work during low or high input voltages events, taking no protective default actions
- The unit shall meet vibration requirements according to GMW3172.
- The DC/DC shall be capable of supplying power to electrical loads in Accessory, Run and RAP Power Modes as well as parasitic current to loads in Off Power Mode
- Functional and microprocessor/peripheral health monitoring information shall be provided to the ECM via the PT Expansion bus

3.1.6.4 Power Output Cycle Timing.

- a. The voltage stabilization shall start by a hardwired trigger using the starter relay control signal coming from the ECM including a pre-crank pulse that the DC/DC may use to prepare for an upcoming crank event.
- b. The maximum duration of the stabilization time shall be calculated to 2 s for the Hardware design. After 2 s stabilization time, the converter shall switch back to the "Bridged Mode", which provides a direct connection to the battery +.
Assumption: A normal crank procedure is finished after $\approx < 800$ ms. Crank procedures > 2 s shall be regarded as abnormal cranking behavior.
- c. The DC/DC shall be ready for the next stabilization ≥ 2 s after the last stabilization procedure. This considers a crank time of $t \ll 2$ s.

3.1.6.5 Ramping Output Voltage Characteristics. To be able to avoid transient effects, like flickering, dimming etc., the DC/DC converter shall provide a voltage ramping function as shown in the following graphics. The DC/DC shall store the input voltage level before cranking and use it as starting level for the stabilization process. After the starting process was completed, the stabilized output voltage shall use a voltage ramping function aiming at the input voltage level for equality. As soon as this level has been achieved, the DC/DC shall switch from "stabilization mode" to the "bridged mode" (see Figure 3).

If the cranking process time exceeds 1 s minimum, the ramping function shall be disabled. The output voltage level shall be 14 V maximum.

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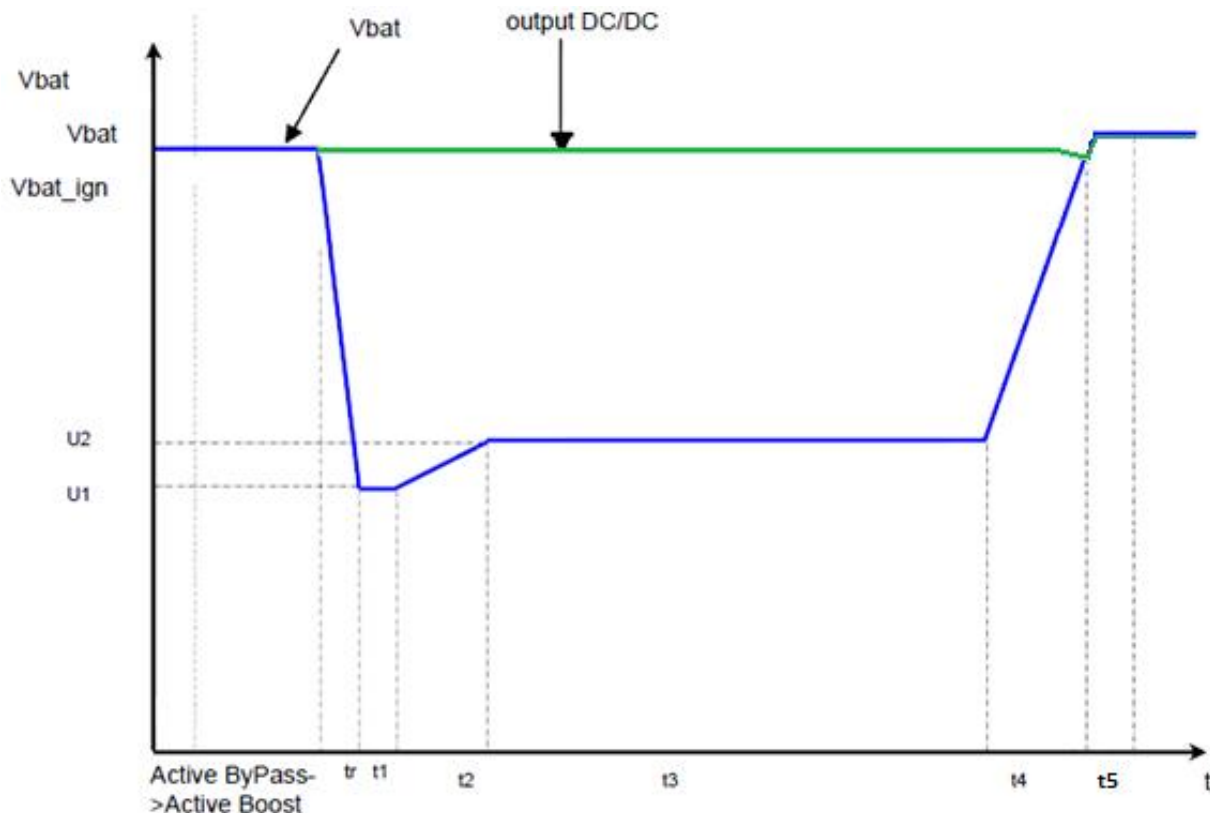
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Page 8 of 37

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Figure 3: Ramping Function



3.1.6.6 Output Voltage Leveling after AutoCrank.

Assumption:

- Battery voltage range before AutoCrank: (12 to 13) V
- Battery voltage range after AutoCrank: (12 to 15.2) V (minimum last voltage → maximum generator voltage).

Required action after AutoCrank:

- After AutoCranking the regulated output voltage level shall be aligned to the now present Battery input voltage as long as it is ≤ 14 V. If the input battery voltage exceeds the 14V, the DC/DC shall switch to the Bypass Mode immediately.
- The Bypass Mode shall be provided if a voltage alignment has been achieved or the maximum crank time has been exceeded.
- Hardware tolerance: Below 14 V, the Bypass switching point at least shall be set when $(U_{BAT} - U_{DC/DC}) \leq 0.4$ V. To help avoiding reverse current from the output stage to the battery during the Bypass Mode $U_{DC/DC}$ shall always be $\leq U_{BAT}$.
- During the leveling phase, the necessary energy shall be taken from the battery.
- Basic value for programmable voltage leveling slew rate: 1 V / 1s.

3.1.6.6.1 Operating/Test Voltage. The inverter shall function as specified in this document without interruption within the operating input voltage range of (6.5 to 16) V_{DC} (see Table 1).

Note: This applies also to the PT Expansion interface of the device.

Also, the converter shall be immune of generator output ripple voltage and supply voltage pulses.

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Page 9 of 37

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Table 1: Operating Voltage Range

Range Name	Range ID	Limit Name	Specified Limit Value
Max Overvoltage Range	VR _H	Max Voltage Range	26.0 V (1 minute)
Overvoltage Range	VR1	Normal Range Limit High	16.0 V
Normal Operating Range Range not defined in GMW3172, special for DCDC converter	VR2	Normal Range Limit Low	6.0 V
Undervoltage Range	VR4	Crank Voltage Limit (240 W, 200 ms)	6.5 V
Reverse Voltage Range	VR5	Ground	Ground Reference
Max Reverse Voltage Range	VR _L	Reverse Voltage Limit	-18.0 V

3.1.6.6.2 Operating Voltage – Hardware Requirements. The Subsystem shall meet the hardware requirements specified in Table 2, when subjected to operating voltage ranges defined earlier.

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Page 10 of 37



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Table 2: Operating Voltage, Hardware Requirements

Range	Subsystem Requirements when in this operating voltage range (Hardware)
VR _H	a. Damage to the DC/DC Converter in this range is allowed but the damage shall not result in a thermal event. b. Non-volatile memory or Flash memory shall not be altered, erroneously written, or cleared.
VR1	<u>VR1a – (18 to 26) V</u> a. No damage shall occur for 1 minute at < +50 °C, otherwise meet requirements in VR_H. b. Non-volatile memory or Flash memory shall not be altered, erroneously written, or cleared other than to record valid EDR data. c. The DC/DC Converter shall not disrupt communications (load down the bus with excessive impedance). <u>VR1b – (16 to 18) V</u> d. No damage shall occur to the DC/DC Converter. e. Non-volatile memory or Flash memory shall not be altered, erroneously written, or cleared other than to record valid EDR data. f. The DC/DC Converter shall support continuous (uninterrupted) data communications. g. The DC/DC converter shall not load down the data bus with excessive impedance.
VR2	a. No damage shall occur to the DC/DC Converter.
VR4	a. No damage shall occur to the DC/DC Converter. b. No corruption of non-volatile memory shall occur when traversing in any direction between VR4 (V < reset) and the specified reverse voltage limit or the lower voltage limit in VR5. h. The DC/DC Converter shall support continuous (uninterrupted) data communication function down to the minimum operating voltage for voltage stabilization, meaning data communication interface shall operate as long as voltage stabilization operates. i. During the entire crank waveform (for details see Figure 3 “Ramping Function” in this document) the DC/DC Converter shall support uninterrupted and fault-free transmission of bus messages. c. The DC/DC converter shall not load down the data bus with excessive impedance.
VR5	a. Same as VR4
VR _L	a. Damage is allowed in this range but the damage shall not result in a thermal event. b. Non-volatile memory or Flash memory shall not be altered, erroneously written, or cleared. c. The DC/DC Converter shall not disrupt communications (load down the bus with excessive impedance) upon returning to VR2 range.

3.1.6.6.3 Operating Voltage – Functional Requirements. The Subsystem shall meet the functional requirements specified in Table 3 when subjected to operating voltage ranges defined earlier.

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Page 11 of 37



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Table 3: Operating Voltage - Functional Requirements

Range	Subsystem Requirements when in this operating voltage range (Functional)
VR _H	a. No function is required.
VR1	<u>VR1a – (18 to 26) V</u> a. If hardware remains operational, all functions shall be in control (including data transfer), and shall be functional. b. Upon returning to the VR2 range, the DC/DC Converter shall resume normal operation within 100 ms. <u>VR1b – (16 to 18) V</u> c. If hardware remains operational, all functions shall be in control (including data transfer), and shall be functional d. Upon returning to the VR2 range, the DC/DC Converter shall resume normal operation within 100 ms.
VR2	a. Fully specified operation shall be required.
VR4	a. If hardware remains operational, all functions shall be in control (including data transfer), and shall be functional. Minimum hardware operational voltage level: 6.5 V
VR5	a. Same as VR4
VR _L	a. No function is required.

3.1.6.7 OBD2 Functions. The DC converter performs the following functions:

- Provides boosted voltage to sensitive loads during auto-start only; pass-through battery voltage to sensitive loads otherwise; based on
 - Measured input voltage
 - Read state of Run, Run/Crank(Crank Control)
- Continuously measures output voltages (pass-through and boost modes)
- Sends output voltage signals over PT-Expansion Bus to ECM
- Detects internal microprocessor and peripheral faults
- Communicates microprocessor and peripheral faults to ECM via PT-Expansion Bus
- Communicates terminal status of Run/Crank (Crank Control) to ECM via PT-Expansion Bus
- Accessory input used for module wake up and communication purposes only

The DC converter does NOT perform the following functions:

- Illuminate the MIL
- Make decisions to set or clear DTCs
- Monitor/Track trip information (OBD drive cycles)
- Take remedial action when a fault is detected

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Page 12 of 37



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