

INTERNATIONAL STANDARD



Connectors for **electrical and electronic equipment** – Tests and measurements –
Part 28-100: Signal integrity tests up to **1 000 MHz on IEC 60603-7 and**
IEC 61076-3 series connectors **2 000 MHz** – Tests 28a to 28g



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Connectors for electrical and electronic equipment – Tests and measurements –
Part 28-100: Signal integrity tests up to ~~1 000 MHz on IEC 60603-7 and~~
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INTERNATIONAL ELECTROTECHNICAL COMMISSION

CONNECTORS FOR ELECTRICAL AND ELECTRONIC EQUIPMENT – TESTS AND MEASUREMENTS –

Part 28-100: Signal integrity tests up to ~~1 000 MHz~~ on ~~IEC 60603-7 and IEC 61076-3 series connectors~~ 2 000 MHz – Tests 28a to 28g

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International Standard IEC 60512-28-100 has been prepared by subcommittee 48B: Electrical connectors, of IEC technical committee 48: Electrical connectors and mechanical structures for electrical and electronic equipment.

This second edition cancels and replaces the first edition, issued in 2013, and constitutes a technical revision.

This edition includes the following significant technical changes with respect to the previous edition:

- The title is revised from 1 000 MHz to 2 000 MHz to reflect the range of frequencies which may be tested.
- All tables and requirements have been revised up to 2 000 MHz.

The text of this International Standard is based on the following documents:

FDIS	Report on voting
48B/2756/FDIS	48B/2766/RVD

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

Future standards in this series will carry the new general title as cited above. Titles of existing standards in this series will be updated at the time of the next edition.

A list of all parts of IEC 60512 series, under the general title *Connectors for electrical and electronic equipment – Tests and measurements* can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "<http://webstore.iec.ch>" in the data related to the specific document. At this date, the document will be

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CONNECTORS FOR ELECTRICAL AND ELECTRONIC EQUIPMENT – TESTS AND MEASUREMENTS –

Part 28-100: Signal integrity tests up to ~~1 000 MHz~~ ~~on IEC 60603-7 and IEC 61076-3 series connectors~~ 2 000 MHz – Tests 28a to 28g

1 Scope

This part of IEC 60512 specifies the test methods for signal integrity and transmission performance for connectors specified in respective parts of IEC 60603-7, IEC 61076-1, IEC 61076-2, and IEC 61076-3 ~~series connectors up to 1 000 MHz~~ standards for connecting hardware applications up to 2 000 MHz. It is also suitable for testing lower frequency connectors, however, the test methodology specified in the detailed specification for any given connector remains the reference conformance test for that connector. The above list of connector series of standards does not preclude referencing this document in other connector manufacturer's specifications or published standards.

~~The test methods~~ Test procedures provided herein are:

- insertion loss, test 28a;
- return loss, test 28b;
- near-end crosstalk (NEXT) test 28c;
- far-end crosstalk (FEXT), test 28d;
- transverse conversion loss (TCL), test 28f;
- transverse conversion transfer loss (TCTL), test 28g.

Other test procedures referenced herein are:

- transfer impedance (Z_T), see IEC 60512-26-100, test 26e.
- for coupling attenuation (a_C), see IEC 62153-4-12.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60050-581, *International Electrotechnical Vocabulary (IEV) – Part 581: Electromechanical components for electronic equipment*

IEC 60169-15, *Radio-frequency connectors – Part 15: R.F. coaxial connectors with inner diameter of outer conductor 4,13 mm (0.163 in) with screw coupling – Characteristic impedance 50 ohms (Type SMA)*

IEC 60512-1, *Connectors for electronic equipment – Tests and measurements – Part 1: ~~General~~ Generic specification*

IEC 60512-26-100:~~2008~~, *Connectors for electronic equipment – Tests and measurements – Part 26-100: Measurement setup, test and reference arrangement and measurements for connectors according to IEC 60603-7 – Tests 26a to 26g*

IEC 60512-27-100, *Connectors for electronic equipment – Tests and measurements – Part 27-100: Signal integrity tests up to 500 MHz on 60603-7 series connectors – Tests 27a to 27g*

IEC PAS 60512-27-200, *Connecteurs for electrical and electronic equipment – Tests and measurements – Part 27-200: Additional specifications for signal integrity tests up to 2 000 MHz on IEC 60603-7 series connectors – Tests 27a to 27g*

IEC 60512-29-100, *Connectors for electronic equipment – Tests and measurements – Part 29-100: Signal integrity tests up to 500 MHz on M12 style connectors – Tests 29a to 29g*

IEC 60603-7, *Connectors for electronic equipment – Part 7: Detail specification for 8-way, unshielded, free and fixed connectors*

IEC 60603-7-1, *Connectors for electronic equipment – Part 7-1: Detail specification for 8-way, shielded, free and fixed connectors*

IEC 60603-7-2, *Connectors for electronic equipment – Part 7-2: Detail specification for 8-way, unshielded, free and fixed connectors, for data transmissions with frequencies up to 100 MHz*

IEC 60603-7-3, *Connectors for electronic equipment – Part 7-3: Detail specification for 8-way, shielded, free and fixed connectors, for data transmission with frequencies up to 100 MHz*

IEC 60603-7-4, *Connectors for electronic equipment – Part 7-4: Detail specification for 8-way, unshielded, free and fixed connectors, for data transmissions with frequencies up to 250 MHz*

IEC 60603-7-5, *Connectors for electronic equipment – Part 7-5: Detail specification for 8-way, shielded, free and fixed connectors, for data transmissions with frequencies up to 250 MHz*

IEC 60603-7-7, *Connectors for electronic equipment – Part 7-7: Detail specification for 8-way, shielded, free and fixed connectors for data transmission with frequencies up to 600 MHz*

IEC 60603-7-41, *Connectors for electronic equipment – Part 7-41: Detail specification for 8-way, unshielded, free and fixed connectors, for data transmissions with frequencies up to 500 MHz*

IEC 60603-7-51, *Connectors for electronic equipment – Part 7-51: Detail specification for 8-way, shielded, free and fixed connectors, for data transmissions with frequencies up to 500 MHz*

IEC 60603-7-71, *Connectors for electronic equipment – Part 7-71: Detail specification for 8-way, shielded, free and fixed connectors, for data transmission with frequencies up to 1 000 MHz*

IEC 60603-7-81, *Connectors for electronic equipment – Part 7-81: Detail specification for 8-way, shielded, free and fixed connectors, for data transmissions with frequencies up to 2 000 MHz*

IEC 60603-7-82, *Connectors for electronic equipment – Part 7-82: Detail specification for 8-way, 12 contacts, shielded, free and fixed connectors, for data transmission with frequencies up to 2 000 MHz*

IEC 61076-1, *Connectors for electronic equipment – Product requirements – Part 1: Generic specification*

IEC 61076-2, *Connectors for electronic equipment – Product requirements – Part 2: Sectional specification for circular connectors*

IEC 61076-2-109, *Connectors for electronic equipment – Product requirements – Part 2-109: Circular connectors – Detail specification for connectors with M 12 x 1 screw-locking, for data transmission frequencies up to 500 MHz*

IEC 61076-3, *Connectors for electronic equipment – Product requirements – Part 3: Rectangular connectors – Sectional specification*

IEC 61076-3-104, *Connectors for electronic equipment – Product requirements – Part 3-104: Detail specification for 8-way, shielded free and fixed connectors for data transmissions with frequencies up to ~~1 000~~ 2 000 MHz*

IEC 61076-3-110, *Connectors for electronic equipment – Product requirements – Part 3-110: Detail specification for ~~shielded~~, free and fixed connectors for data transmission with frequencies up to ~~1 000~~ 3 000 MHz*

~~IEC 61156 (all parts), Multicore and symmetrical pair/quad cables for digital communications~~

IEC 61156-1, *Multicore and symmetrical pair/quad cables for digital communications – Part 1: Generic specification*

~~IEC 61156-6, Multicore and symmetrical pair/quad cables for digital communications – Part 6: Symmetrical pair/quad cables with transmission characteristics up to 1 000 MHz – Work area wiring – Sectional specification~~

IEC 61156-9, *Multicore and symmetrical pair/quad cables for digital communications – Part 9: Cables for channels with transmission characteristics up to 2 GHz – Sectional specification*

IEC 61156-10:2016, *Multicore and symmetrical pair/quad cables for digital communications – Part 10: Cables for cords with transmission characteristics up to 2 GHz – Sectional specification*

IEC 61169-16, *Radio-frequency connectors – Part 16: RF coaxial connectors with inner diameter of outer conductor 7 mm (0,276 in) with screw coupling – Characteristic impedance 50 ohms (75 ohms) (Type N)*

IEC 62153-4-12, *Metallic communication cable test methods – Part 4-12: Electromagnetic compatibility (EMC) – Coupling attenuation or screening attenuation of connecting hardware – Absorbing clamp method*

ISO/IEC 11801-1:2017, *Information technology – Generic cabling for customer premises – Part 1: General requirements*

3 Terms, definitions and ~~acronyms~~ abbreviated terms

For the purposes of this document, the terms and definitions given in IEC 60050-581, IEC 61076-1, IEC 60512-1, IEC 60603-7, IEC 61076-3-104 and IEC 61076-3-110, and the following apply.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at <http://www.electropedia.org/>
- ISO Online browsing platform: available at <http://www.iso.org/obp>

3.1 Terms and definitions

3.1.1

intermodal <parameter or measurement>

parameter or measurement that either sources on the common mode and measures on the differential mode, or sources on the differential mode and measures on the common mode

3.1.2

mixed mode <parameter or measurement>

parameters or measurements containing differential mode, common mode, and intermodal S-matrices

3.2 ~~Acronyms~~ Abbreviated terms

a_C	coupling attenuation
CM	common mode
DM	differential mode
DUT	device under test
FEXT	far-end crosstalk loss
IEC	International Electrotechnical Commission
LCL	longitudinal conversion loss
LCTL	longitudinal conversion transfer loss
NA	network analyzer
NEXT	near-end crosstalk loss
TCL	transverse conversion loss
TCTL	transverse conversion transfer loss
SE	single ended
Z_T	transfer impedance

4 Overall test arrangement

4.1 General

This document specifies test methods and procedures for connectors.

The test methods and procedures for signal integrity and transmission performance specified herein are referenced by connector standards, specified in IEC 60603-7, IEC 60603-7-1, IEC 61076-2, 61076-3 and other standards for connecting hardware and their sectional specifications, with signal integrity specifications up to 2 000 MHz; such connector standards include IEC 60603-7-81, IEC 60603-7-82, IEC 61076-3-110, IEC 61076-3-104, and IEC 61076-2-109, which are used with twisted-pair cables having 100 Ω nominal differential characteristic impedance.

The test methods and procedures specified herein are referenced by connector standards for connecting hardware typically used with twisted-pair cables having 100 Ω nominal differential characteristic impedance, which are specified in accordance with IEC 61156-1 cable standards and its sectional specifications up to 2 000 MHz, e.g. IEC 61156-9 and IEC 61156-10.

4.2 Test instrumentation

4.2.1 General

All test instrumentation shall be capable of performing measurements over the frequency range of 1 MHz to ~~1 000~~ 2 000 MHz.

4.2.2 Vector network analyser

The test procedures hereby described require the use of a vector network analyzer. The analyzer ~~should~~ shall have the capability of full 2-port calibrations. The analyzer shall cover the frequency range of 1 MHz to ~~1 000~~ 2 000 MHz at least.

Measurements are to be taken using a mixed mode test set-up, which is often referred to as an unbalanced, modal decomposition or balun-less setup. This allows measurements of balanced devices without use of an RF balun in the signal path.

Such a configuration also allows testing with either a common or differential mode stimulus ~~and~~ or responses, ensuring that intermodal parameters can be measured without reconnection.

A 16-port network analyzer ~~is required~~ shall be used to measure all combinations of a 4-pair device without external switching, however the network analyzer shall have a minimum of 2-ports (including one bi-directional port) to enable the data to be collated and calculated.

It should be noted that the use of a 2-port analyzer will involve successive repositioning of the measurement port in order to measure any given parameter.

A 4-port network analyzer is recommended as a practical minimum number of ports, as this will allow the measurement of the full 16 term mixed mode S-parameter matrix on a given pair combination without switching or reconnection in one direction.

4.2.3 RF switching unit

In order to minimise the reconnection of the DUT for each pair combination the use of a RF switching unit is also recommended.

Each conductor of the pair or pair combination under test shall be connected to a separate port of the network analyzer, and results are processed either by internal analysis within the network analyzer or by an external application.

4.2.4 Reference loads and termination loads

Reference loads and through connections ~~are needed~~ shall be utilised for the calibration of the set-up. Requirements for the reference loads ~~are~~ shall be as given in 4.9. Termination loads ~~are needed~~ shall be utilised for termination of pairs, used and unused, which are not terminated by the network analyzer. Requirements for the termination loads ~~are~~ shall be as given in ~~4.7 and 4.10~~ 4.11.

Loads used for calibration shall be paired as explained in Annex E to ensure good symmetry at the calibration plane.

4.3 Measurement precautions

To ensure a high degree of reliability for transmission measurements, the following precautions are required.

- Consistent and stable resistor loads shall be used throughout the test sequence.
- Cable and adapter discontinuities, as introduced by physical flexing, sharp bends and restraints shall be avoided before, during and after the tests.
- Consistent test methodology and termination resistors shall be used at all stages of transmission performance qualifications.
- The relative spacing of conductors in the pairs shall be preserved throughout the tests to the greatest extent possible.

- The balance of the cables shall be maintained to the greatest extent possible by consistent conductor lengths and pair twisting to the point of load.
- The sensitivity to set-up variations for these measurements at high frequencies demands attention to details for both the measurement equipment and the procedures.
- The test setup has to be grounded appropriately.

4.4 Mixed mode S-parameter nomenclature

The test methods specified in this document are based on a balunless test setup in which all terminals of a device under test are measured and characterised as single ended (SE) ports, i.e. signals (RF voltages and currents) ~~are~~ shall be defined relative to a common earth (ground). For a device with 4 terminals, a diagram is given in Figure 1.

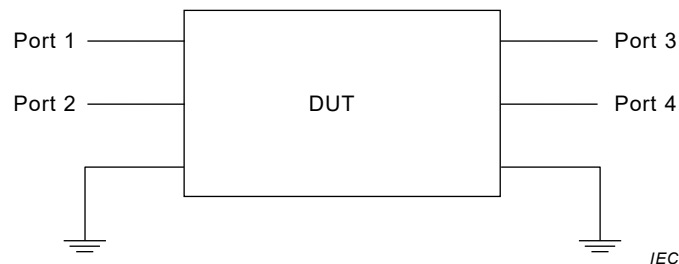


Figure 1 – Diagram of a single ended 4-port device

The 4-port device in Figure 1 ~~is~~ shall be characterised by the 16-term SE S-matrix given in Formula 1, in which the S-parameter S_{ba} expresses the relation between a single ended response on port “b” resulting from a single ended stimulus on port “a”.

$$S = \begin{bmatrix} S_{11} & S_{12} & S_{13} & S_{14} \\ S_{21} & S_{22} & S_{23} & S_{24} \\ S_{31} & S_{32} & S_{33} & S_{34} \\ S_{41} & S_{42} & S_{43} & S_{44} \end{bmatrix} \quad (1)$$

For a balanced device, each port ~~is~~ shall be considered to consist of a pair of terminals (= a balanced port) as opposed to the SE ports defined above, see Figure 2.

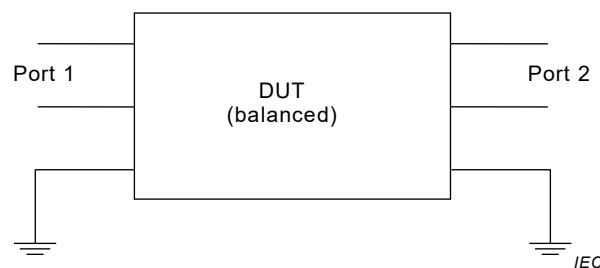


Figure 2 – Diagram of a balanced 2-port device

~~In order to characterize the balanced device, both the differential mode and the common mode signals on each balanced port shall be considered.~~ The device ~~can be~~ is characterised by a mixed mode S-matrix that includes all combinations of modes and ports, e.g. the mixed mode S-parameter S_{DC21} that expresses the relation between a differential mode response on port 2 resulting from a common mode stimulus on port 1. Using this nomenclature, the full set of mixed mode S-parameters for a 2-port ~~can be presented as~~ are given in Table 1.

Table 1 – Mixed mode S-parameter nomenclature

		Differential mode stimulus		Common mode stimulus	
		Port 1	Port 2	Port 1	Port 2
Differential mode response	Port 1	S_{DD11}	S_{DD12}	S_{DC11}	S_{DC12}
	Port 2	S_{DD21}	S_{DD22}	S_{DC21}	S_{DC22}
Common mode response	Port 1	S_{CD11}	S_{CD12}	S_{CC11}	S_{CC12}
	Port 2	S_{CD21}	S_{CD22}	S_{CC21}	S_{CC22}

A 4-terminal device can be represented both as a 4-port SE device as in Figure 1 characterised by a single ended S-matrix (Formula 1) and as a 2-port balanced device as in Figure 2 characterised by a mixed mode S-matrix (Table 1). As applying a SE signal to a port is mathematically equivalent to applying ~~superposed~~ superimposed differential and common mode signals, the SE and the mixed mode characterisations of the device are interrelated. The conversion from SE to mixed mode S-parameters is given in Annex A. Making use of this conversion, the mixed mode S-parameters may be derived from the measured SE S-matrix.

4.5 Coaxial cables and interconnect for network analyzers

Assuming that the characteristic impedance of the network analyzer is 50 Ω , coaxial cables used to interconnect the network analyzer, switching matrix and the test fixture shall be of 50 Ω characteristic impedance and of low transfer impedance (double screen or more).

These coaxial cables should be as short as possible. ~~(It is recommended that they do not exceed 1 000 mm each.)~~ Max 1 000 mm (on each port).

The screens of each cable shall be electrically bonded to a common earth (ground) plane.

To optimize dynamic range, the total interconnecting cable insertion loss should be less than ~~3 dB at 1 000 MHz~~ 7 dB at 2 000 MHz per metre.

4.6 Requirements Characteristic for switching matrices

Switches (if used) ~~shall~~ may be of a minimum of 2x4 configuration at all ports, although a switch with a higher number of ports (e.g. 2x8, 1x16, 4x16) is recommended as this can allow more complete or even total measurement of the DUT without reconnection or moving the DUT. When such switching is used, it shall be constructed such that each port ~~be~~ is configurable as ~~either~~ input, output or 50 Ω termination. All inactive ports of the switch shall be terminated with a 50 Ω impedance load.

~~The switch shall be capable of swapping the ports of the network analyser in a paired fashion to correctly connect to each conductor of the DUT transmission pair.~~

~~The switch should be constructed to minimise the different path lengths for each signal path of the pair.~~

The switch ~~shall~~ may comply ~~to~~ with the minimum switch performance recommendations given by Table 2.

Table 2 – Switch performance ~~recommendations~~ requirements

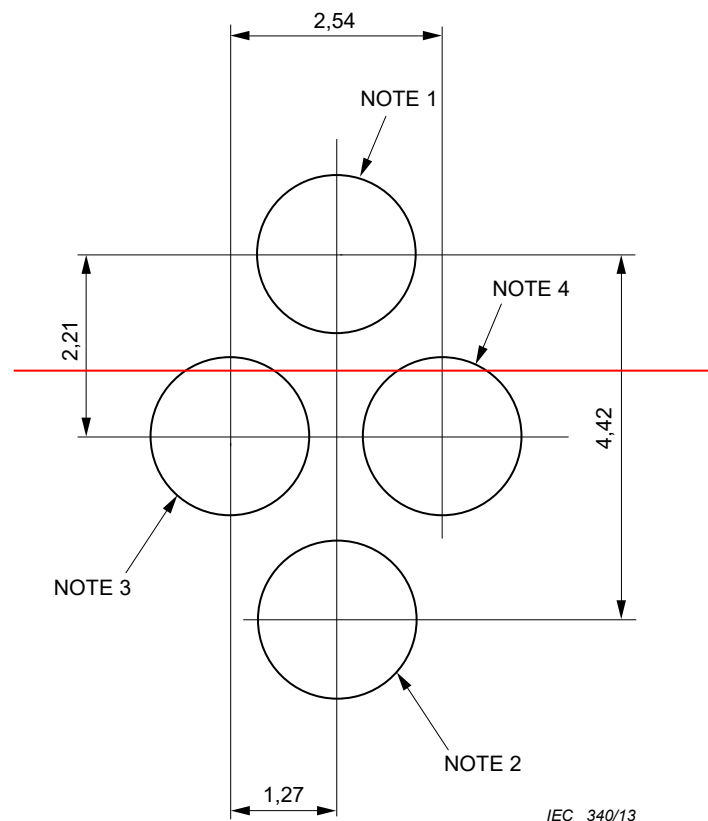
Parameter	Frequency MHz	Requirement up to 1 000 MHz
Insertion loss (dB)	$1 \leq f \leq 1\,000$	$\leq 0,5$ dB
Return loss (dB)		$\geq 68 - 20 \log(f)$ dB 40 dB max 24 dB min
Crosstalk (dB)		≥ 95 dB

Parameter dB	Requirement 1 MHz to 1 000 MHz	Requirement 1 000 MHz to 2 000 MHz
Insertion loss	0,5 dB max.	2,0 dB max.
Return loss	68-20log(f) dB min. 40 dB max. 24 dB min.	18 dB min.
Crosstalk loss	105 dB min.	102 dB min.

4.7 Test fixture requirements

~~For ease of interfacing to test fixtures, a pin and fixed connector interface with dimensions as shown in Figure 3 is recommended. Information concerning examples of fixed connectors that may be used for this interface is given in Annex B.~~

Dimensions in millimeters



IEC 340/13

NOTE 1 — Ground

NOTE 2 — Ground

NOTE 3 — First conductor in a pair

NOTE 4 — Second conductor in a pair

Figure 3 — Test interface pattern

Test fixtures shall meet the requirements of Table 3 when tested using appropriate resistor terminations at the DUT interface fixed connectors of the fixture after the network analyser has been calibrated at the end of the coaxial cables intended to interface to the fixture.

Table 3 — Test fixture requirements

Parameter	Frequency MHz	Requirement up to 1 000 MHz
SE Port (50 Ω) return loss (dB)	$1 \leq f \leq 1\,000$	$\geq 72 - 20 \log(f)$ dB 40 dB max
DM port (100 Ω) return loss (dB)		$\geq 78 - 20 \log(f)$ dB 40 dB max
CM port (100 Ω) return loss (dB)		$\geq 68 - 20 \log(f)$ dB 35 dB max
SE (50 Ω) Port to Port Isolation NEXT and FEXT		$\geq 114 - 20 \log(f)$ dB 75 dB max
DM (100 Ω) Port to Port Isolation NEXT and FEXT pair to pair		$\geq 130 - 20 \log(f)$ dB 94 dB max
DM (100 Ω) insertion loss		< 0,5 dB
TCL-LCL		$\geq 100 - 20 \log(f)$ dB

		-70 dB max
TCTL-LCTL		$\geq 90 - 20\log(f)$ dB -50 dB max

4.7.1 Test fixture types

Test fixtures are covered in three types, corresponding to three groups of connectors:

- Indirect-reference** test fixtures, for connector types utilizing a reference connector for measurement of transmission parameters, with additional specifications, e.g. crosstalk (NEXT) vector for crosstalk compensation parameters; e.g. IEC 60603-7 series (8-pole types), IEC 60603-7-2, IEC 60603-7-3, IEC 60603-7-4, IEC 60603-7-5, IEC 60603-7-41, IEC 60603-7-51, and IEC 60603-7-81.
- Direct-probe** test fixtures, for connector types utilizing single ended direct-probe measurement of transmission parameters, e.g. IEC 60603-7 series (12-pole types), IEC 60603-7-7, IEC 60603-7-71, IEC 60603-7-82, and IEC 61076-3-110.
- Specialized** test fixtures, for connector types utilizing a combination of direct-probe and indirect-reference measurement of transmission parameters, e.g. IEC 61076-3-104, and IEC 61076-2-109.

For the test fixture types a), b), and c) detail specifications, see Annex C, Annex B, and Annex C respectively.

4.8 Requirements for termination performance at calibration plane

Termination performance at the calibration plane shall meet the requirements of Table 3.

Table 3 – Requirements for terminations at calibration plane

Parameter	Frequency MHz	Requirement up to 1 000 MHz
SE Port (50 Ω) return loss (dB)	$1 \leq f \leq 1\,000$	$\geq 74 - 20\log(f)$ dB -40 dB max
DM port (100 Ω) return loss (dB)		$\geq 74 - 20\log(f)$ dB -40 dB max
DM Port to Port residual NEXT		$\geq 130 - 20\log(f)$ dB 94 dB max

Parameter	Requirement 1 MHz to 1 000 MHz	Requirement 1 000 MHz to 2 000 MHz
SE Port (50 Ω) return loss (dB)	$\geq 72 - 20\log(f)$ dB 40 dB max. 12 dB min.	12 dB min.
DM port (100 Ω) return loss (dB)	$\geq 78 - 20\log(f)$ dB 40 dB max. 20 dB min.	20 dB min.
DM Port to Port residual NEXT	$\geq 130 - 20\log(f)$ dB 94 dB max.	70 dB max.

4.9 Reference loads for calibration

To perform a 1-port or 2-port calibration of the test equipment, a short circuit, an open circuit and a reference load are required. These devices shall be used to obtain a calibration.

The reference load shall be calibrated against a calibration reference, which shall be a $50\ \Omega$ load, traceable to an international reference standard. One $50\ \Omega$ reference load shall be calibrated against the calibration reference. The reference load for calibration shall be placed in an N-type connector according to IEC 61169-16, or in an SMA-type connector according to IEC 60169-15, types ~~meant~~ suitable for panel mounting, which ~~is~~ are machined flat on the back side, see Figure 3.

The option to use SMA-type connectors, according to IEC 60169-15 is preferred.

The load shall be fixed to the flat side of the connector. A network analyzer shall be calibrated, 1-port full calibration, with the calibration reference. Thereafter, the return loss of the reference load for calibration shall be measured. The verified return loss shall be $>46\ \text{dB}$ at frequencies up to 100 MHz and $>40\ \text{dB}$ at frequencies above 100 MHz and up to the limit for which the measurements are to be carried out.

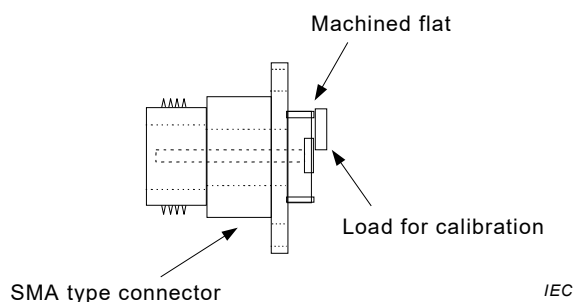


Figure 3 – Calibration of reference loads

4.10 Calibration

4.10.1 General

Isolation measurements shall be used as part of the calibration if the requirements of Table 3 are not met.

The calibration shall be equivalent to a minimum of a full 2-port SE calibration for measurements where the response and stimulus ports are the same (S_{xx11} and S_{xx22}), and a minimum of a full 4-port SE calibration for measurements where the response and stimulus ports are different (S_{xx12} and S_{xx21}).

An individual calibration shall be performed for each signal path used for the measurements.

If a complete switching matrix and 4-port network analyzer test setup is used, a full set of measurements for a 4-pair device (i.e. 16 single-ended ports), will require 28 separate 4-port calibrations, although many of the measurements within each calibration are in common with other calibrations.

A software or hardware package may be used to minimise the number of calibration measurements required.

~~The calibration shall be applied such that the calibration plane shall be at the ends of the fixed connectors of the test fixture.~~

The calibration may be performed at the test interface using appropriate calibration artefacts, or at the ends of the coaxial test cable using coaxial ~~terminations~~ standards and de-embedding data representing the test fixture.

4.10.2 Calibration test interface

Where calibration is performed at the test interface, open, short and load measurements shall be taken on each SE port concerned, and through and isolation measurements shall be taken on every pair combination of those ports.

4.10.3 Calibration at end of coaxial test cables

Where calibration is performed at the end of the coaxial test cables, open, short and load measurements shall be taken on each port concerned, and through and isolation measurements shall be taken on every pair combination of those ports. In addition, the test fixture shall then be de-embedded from the measurements. The de-embedding techniques shall incorporate a fully populated 16 port S-matrix. It is not acceptable to perform a de-embedded calibration using only reflection terms (S_{11} , S_{22} , S_{33} , S_{44}) or only near end terms (S_{11} , S_{21} , S_{12} , S_{22}).

De-embedding using reduced term S-matrices may be used for post-processing of results.

4.11 Termination loads for termination of conductor pairs

4.11.1 General

~~50 Ω wire to ground terminations shall be used on all active pairs under test. 50 Ω differential mode to ground terminations shall be used on all inactive pairs and on the opposite ends of active pairs for NEXT and FEXT testing. Inactive pairs for return loss testing shall be terminated with 50 Ω differential mode to ground terminations.~~

50 Ω loads to earth (ground) shall be used on all active wires under test for termination. 50 Ω loads to earth (ground) shall be used on all inactive wires and on the opposite ends of active wires for NEXT and FEXT testing for termination. Inactive wires for return loss testing shall be terminated with 50 Ω loads to earth (ground). See Figure 4.

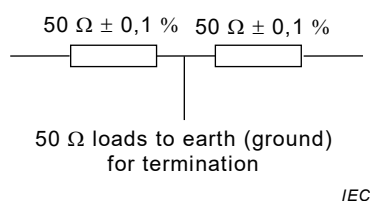


Figure 4 – Resistor termination networks

Small geometry chip resistors shall be used for the construction of resistor terminations. The two 50 Ω ~~DM~~ SE terminating resistors shall be matched to within 0,1 % at DC, and 2 % at ~~1 000~~ 2 000 MHz (corresponding to a 40 dB return loss requirement at ~~1 000~~ 2 000 MHz). ~~The length of connections to impedance terminating resistors shall be minimized. Use of soldered connections without leads is recommended.~~

4.11.2 ~~Verification of termination loads~~ Impedance matching resistor termination networks

The performance of impedance matching resistor termination networks shall be verified by measuring the return loss of the termination and the residual NEXT between any two resistor termination networks at the calibration plane.

For the return loss measurement, a 2-port SE calibration ~~is required~~ shall be done using a reference load verified according to 4.9.

After calibration, connect the resistor termination network and perform a full 2-port SE S-matrix measurement. The measured SE S-matrix shall be transformed into the associated mixed mode S-matrix to obtain the S-parameters S_{DD11} and S_{CC11} from which the differential mode return loss RL_{DM} and the common mode return loss RL_{CM} are determined. The return loss of the resistor termination network shall meet the requirements of Table 3.

For the residual NEXT measurement, a 4-port SE calibration is required. After calibration, connect the resistor termination networks and perform a full 4-port SE S-matrix measurement. The measured S-matrix shall be transformed into the associated mixed mode S-matrix to obtain the S-parameter S_{DD21} from which the residual NEXT of the terminations, $NEXT_{residual_term}$, is determined. The residual NEXT shall meet the requirements of Table 3.

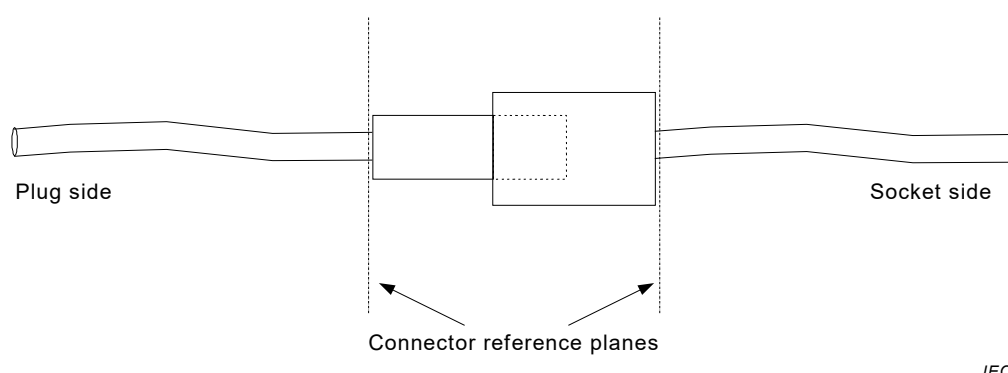
4.12 Termination of screens

If As the connector under test is normally screened, screened measurement cables shall be applied. The screen or screens of these cables shall be fixed to the earth (ground) plane as close as possible to the calibration plane.

4.13 Test specimen and reference planes

4.13.1 General

The test specimen is a mated pair of relevant connectors. The connector reference plane for the test specimen is the point at which the cable sheath enters the connector (the back end of the connector) or the point at which the internal geometry of the cable is no longer maintained, whichever is farther from the connector, see Figure 5. This definition applies to both ends of the test specimen. The ~~fixed connector~~ DUT shall be terminated in accordance with the manufacturer's instructions and shall be compatible with the measurement test set up and fixtures.



IEC

Figure 5 – Definition of reference planes

4.13.2 Interconnections between device under test (DUT) and the calibration plane

4.13.2.1 General

Twisted-pair ~~interconnect~~, coax, printed circuits or other qualified interconnections ~~are~~ shall be used between the connector reference plane of the DUT and the calibration plane. It is ~~necessary~~ required to control the characteristics of these interconnections to the best extent possible as they are beyond the calibration plane. These interconnections ~~should~~ shall be as short as practical and their CM and DM impedances shall be managed to minimize their effects on measurement. ~~Refer to Annex B for additional information about test pins which may be used to facilitate impedance management.~~

The DM return loss performance of the interconnections shall meet the requirements of Table 4. The insertion loss performance of the interconnections is assumed to be less than ~~0,1~~ 0,2 dB over the frequency range from 1 MHz to ~~1 000~~ 2 000 MHz.

~~It is recommended that all DUTs, including test free connectors, have fixed connectors with 2,54 mm spacing applied to the ends of their interconnect to facilitate a consistent interfacing with the test fixture.~~

4.13.2.2 Twisted-pair interconnect

4.13.2.2.1 General

When used, twisted-pair interconnect shall have 100 Ω nominal differential characteristic impedance. The twisted pairs should not exhibit gaps between the conductor insulation. The twisted-pair interconnect may be obtained as an individual twisted-pair interconnect, or it may be part of a cable.

Refer to Annex B for additional information about interconnections between device under test (DUT) and the calibration plane, for connector types typically utilizing twisted-pair interconnect with indirect-reference test fixtures.

Refer to Annex B for additional information about interconnections between device under test (DUT) and the calibration plane, wherein it is recommended that all DUTs, including reference test free connectors, have fixed connectors with 2,54 mm spacing applied to the ends of their interconnect to facilitate a consistent interfacing with the test fixture.

Refer to Annex B for additional information about test pins which may be used to facilitate impedance management.

The interconnect shall comply with the return loss requirements of ~~4.12.1.2~~ Table 4.

DM to earth (ground) terminations ~~are required~~ shall be used and the interconnect ~~should~~ shall be placed in an impedance managing system. The maximum length of the twisted-pair leads at each end of the DUT shall be 51 mm, however ~~it is recommended to~~ they shall be kept as short as possible.

4.13.2.2.2 Individual twisted-pair interconnect

Individual twisted-pair interconnect may be obtained from discrete twisted-pair stock or removed from sheathed cable.

Prior to attachment to the DUT, the return loss of the pair shall be tested. For this test, a 100 mm length of individual twisted-pair shall be used. The twisted-pair shall be terminated with a 50 Ω differential mode to earth (ground) resistor termination network as described in 4.11. The resistor termination network shall be attached directly to the conductors of the pair in such a way as to minimize the disturbance of the pair. Potential disturbances include gaps between the conductor insulation in the pair, melting insulation, and excess solder.

Return loss shall be tested according to ~~4.12.1.2~~ Table 4.

The twisted-pair leads are then trimmed for attachment to the DUT and the test fixture.

4.13.2.2.3 Interconnect as part of cables

Interconnect may also be obtained from a section of twisted-pair cables where the four twisted-pair interconnects are maintained in the cable sheath. This method will most often be used with free connectors cut from the ends of assembled balanced cords, but can also be used with fixed connectors.

Prior to attachment to the DUT, the return loss of each of the cable pairs within the cable shall be tested. For this test, a 100 mm length of cable shall be used. Each pair of the cable shall be terminated as described for the individual twisted-pair interconnect in 4.11. For the inactive pairs, i.e. pairs not under test, the termination shall be applied to both ends.

Return loss shall be tested according to ~~4.12.1.2~~ Table 4.

The cable shall then be terminated to the DUT per manufacturer's instructions and trimmed for attachment to the test fixture.

When this method is used with free connectors cut from assembled cords, it shall be sufficient if the return loss of the cord cable is compliant to the category ~~7_A~~ 8.1 and category 8.2 requirements of ~~IEC 61156-6~~ IEC 61156-10:2016, or if the return loss of the assembled cord is compliant to the balanced cord category ~~7_A~~ 8.2 requirements of ISO/IEC 11801-1:2017.

4.13.2.3 Direct probe single ended interconnect

When used, direct probe single ended interconnect shall have 50 Ω nominal single ended (coaxial) characteristic impedance. Coaxial cables and printed circuit transmission lines within the test fixture, connected to the DUT connector contacts, should be sufficiently matched to conform to the interconnection DM return loss requirements of 4.13.2.4.

Refer to Annex C for additional information about Interconnections between device under test (DUT) and the calibration plane, for connectors utilizing Direct-probe test fixtures.

The interconnect shall comply with the return loss requirements of Table 4.

4.13.2.4 Interconnection DM return loss requirements

The return loss of the interconnection shall be tested using the mixed mode approach as described in 4.11.2 for the verification of resistor termination networks. The interconnection shall be tested for differential mode return loss only and shall meet the requirements in Table 4.

Table 4 – Interconnection DM return loss requirements

Frequency MHz	DM return loss dB
1 ≤ f < 80	40 dB
80 ≤ f ≤ 1 000	78 – 20log(f) dB 20 dB min

Frequency MHz	DM return loss dB
1 ≤ f < 100	40
100 ≤ f ≤ 2 000	80 – 20log(f) dB 20 dB min.

Twisted-pair interconnects shall be prepared for test as described in ~~4.12.1.1.1 and 4.12.1.1.2~~ 4.13.2.2. When testing other interconnections, equivalent differential mode to earth (ground) terminations shall be applied.

4.14 Overall test setup requirements

The requirements of the overall test setup shall meet the requirements of Table 5 when tested using terminations according to 4.11.

Table 5 – Overall test setup requirements

Parameter	Frequency MHz	Requirement up to 1 000 MHz
SE port (50 Ω) return loss, (dB)	$1 < f < 1\,000$	$> 65 - 20\log(f)$ 40 dB max 10 dB min
DM port (100 Ω) return loss, (dB)		$> 68 - 20\log(f)$ 40 dB max 10 dB min
CM port (100 Ω) return loss, (dB)		$> 60 - 20\log(f)$ 35 dB max 10 dB min
SE (50 Ω) port-to-port (pair-to-pair) isolation: NEXT and FEXT		$113 - 20\log(f)$ 75 dB max.
SE (50 Ω) port-to-port (within a pair) isolation: NEXT and FEXT		$> 74 - 20\log(f)$ 75 dB max.
DM (100 Ω) port-to-port isolation: NEXT and FEXT		$130 - 20\log(f)$ 94 dB max
DM (100 Ω) insertion loss		< 1 dB
TCL, LCL		$100 - 20\log(f)$ 70 dB max
TCTL, LCTL		$> 90 - 20\log(f)$ 50 dB max

Parameter	Requirement 1 MHz to 1 000 MHz	Requirement 1 000 MHz to 2 000 MHz
SE port (50 Ω) return loss, (dB)	$> 68 - 20\log(f)$ 20 dB max. 12 dB min.	12 dB min.
DM port (100 Ω) return loss, (dB)	$> 68 - 20\log(f)$ 20 dB max. 12 dB min.	12 dB min.
CM port (50 Ω) return loss, (dB) ¹	$> 68 - 20\log(f)$ 20 dB max. 12 dB min.	12 dB min.
SE (50 Ω) port-to-port (pair-to-pair) isolation: NEXT and FEXT	$> 125 - 20\log(f)$ 80 dB max.	65 dB min.

Parameter	Requirement 1 MHz to 1 000 MHz	Requirement 1 000 MHz to 2 000 MHz
SE (50 Ω) port-to-port (within a pair) isolation: NEXT and FEXT	$> 100 - 20\log(f)$ 70 dB max.	40 dB min.
DM (100 Ω) port-to-port isolation: NEXT and FEXT	$> 140 - 20\log(f)$ 94 dB max.	80 dB min.
DM (100 Ω) port-to-port isolation: near-end-to-far-end ²	$> 140 - 20\log(f)$ 94 dB max.	80 dB min.
DM (100 Ω) insertion loss	< 12 dB	< 12 dB
TCL, LCL	$> 90 - 20\log(f)$ 50 dB max.	30 dB min.
TCTL, LCTL	$> 90 - 20\log(f)$ 50 dB max.	30 dB min.
¹ DUT common mode impedance is usually specified as 50 Ω , therefore for measurements, a transformation is required as specified in Annex A. For the case of balunless measurements the common mode impedance measurement and thus return loss is derived as specified in Annex A. ² The requirement can be met either through separation or shielding applied between fixtures.		

5 Connector measurements up to ~~1 000~~ 2 000 MHz

5.1 General

~~The measurements made in this clause are of a mated free connector and fixed connector. Compliance to this standard for a particular interface does not ensure interoperability with other interfaces qualified to this standard e.g. IEC 61076-3-104 is not interoperable with IEC 61076-3-110.~~

~~It is assumed that the performance variation of all free connectors of a given interface can be ignored. Consequently, it is not necessary to qualify the free connectors used for the connecting hardware performance measurements.~~

The measurements made in this clause are on a free connector mated with a fixed connector.

5.2 Insertion loss, test 28a

5.2.1 Object

The object of this test is to measure the insertion loss of a connecting hardware pair. Insertion loss is defined [here](#) as the additional attenuation that is caused by a connecting hardware pair inserted in a communication cable.

5.2.2 Connecting hardware insertion loss

Connecting hardware shall be tested for insertion loss in one direction using at least one free connector.

5.2.3 Test method

Insertion loss is evaluated from the mixed mode parameter S_{DD21} for each conductor pair. The mixed mode S-parameters are derived by transformation of the SE S-matrix.

5.2.4 Test set-up

The test set-up consists of a network analyzer and two test fixtures as described in Clause 4. An illustration of the test set-up, which also shows the termination principles, is shown in Figure 6. Resistor termination networks in accordance with 4.11 shall be applied for all inactive pairs. Interconnects (if used) shall be prepared and controlled per 4.13.2.

5.2.5 Procedure

5.2.5.1 Calibration

A full 4-port SE calibration shall be performed at the calibration planes in accordance with 4.10. Reference loads used for calibration shall be in accordance with 4.9.

5.2.5.2 Measurement

The DUT shall be arranged in a test set-up according to 5.2.4 and Figure 6, including proper termination of the active and inactive pairs. A full SE S-matrix measurement shall be performed. The measured SE S-matrix shall be transformed into the associated mixed mode S-matrix to obtain the S-parameter S_{DD21} from which insertion loss is determined.

Test all conductor pairs and record the results.

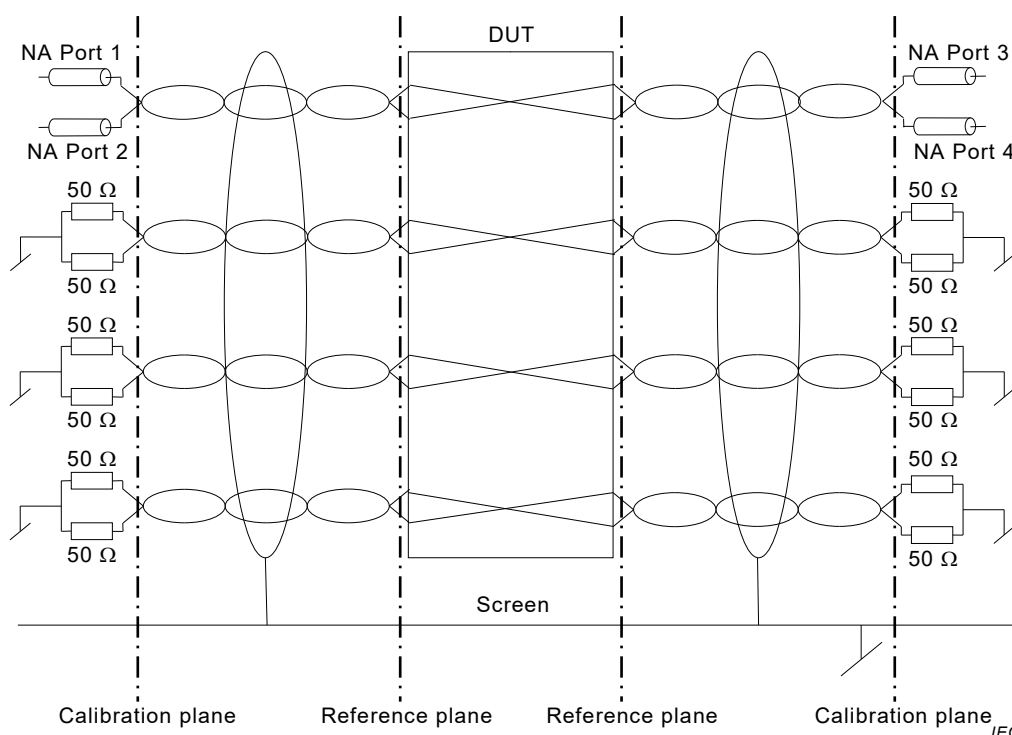


Figure 6 – Insertion loss and TCTL measurement

5.2.6 Test report

The test results shall be reported in graphical or table format with the specification limits shown on the graphs or in the table at the same frequencies as specified in the relevant detail specification. Results for all pairs shall be reported. It shall be explicitly noted if the test results exceed the test limits.

5.2.7 Accuracy

Mate the two interfaces of the test equipment at the calibration plane and perform an IL measurement.

~~The accuracy shall be within $\pm 0,05$ dB.~~ The accuracy shall be better than ± 1 dB at the specification limit.

5.3 Return loss, test 28b

5.3.1 Object

The object of this test is to measure the return loss of a connecting hardware pair at the two reference planes.

5.3.2 Connecting hardware return loss

Connecting hardware shall be tested for return loss in both directions using at least one free connector.

5.3.3 Test method

Return loss is evaluated from the mixed mode parameters S_{DD11} and S_{DD22} for all conductor pairs. The mixed mode S-parameters are derived by transformation of the measured SE S-matrix.

~~NOTE—As a connector is a low-loss device, the return loss of the two sides is nearly equal.~~

5.3.4 Test set-up

The test set-up consists of a network analyzer and two test fixtures as described in Clause 4. An illustration of the test set-up, which also shows the termination principles, is shown in Figure 9. Resistor termination networks in accordance with 4.11 shall be applied for all active and inactive pairs. Interconnects (if used) shall be prepared and controlled per 4.13.2.

Return loss may be measured in a test set-up using only one fixture and a 2-port SE calibration and measurement. In this case, the return loss is measured in only one direction at a time.

5.3.5 Procedure

5.3.5.1 Calibration

A full 4-port SE calibration shall be performed at the calibration planes in accordance with 4.10. Reference loads used for calibration shall be in accordance with 4.11.

5.3.5.2 Measurement

The DUT shall be arranged in a test set-up according to 5.3.4 and Figure 9, including proper termination of the active and inactive pairs. A full SE S-matrix measurement shall be performed. The measured SE S-matrix shall be transformed into the associated mixed mode S-matrix to obtain the S-parameters S_{DD11} and S_{DD22} from which the return loss is determined.

Test all conductor pairs in both adirections and record the results.

5.3.6 Test report

The test results shall be reported in graphical or table format with the specification limits shown on the graphs or in the table at the same frequencies as specified in the relevant clause.

specification. Results for all pairs shall be reported. It shall be explicitly noted if the test results exceed the test limits.

5.3.7 Accuracy

The return loss of the load for calibration is verified to be greater than 46 dB up to 100 MHz and greater than 40 dB at higher frequencies. The uncertainty of the connection between the connector under test and the test fixture is expected to deteriorate the return loss of the set-up (effectively the directional bridge implemented by the test set-up) by 6 dB. The accuracy of the return loss measurements is then equivalent to measurements performed by a directional bridge with a directivity of 40 dB and 34 dB.

5.4 Near-end crosstalk (NEXT), test 28c

5.4.1 Object

The object of this test procedure is to measure the magnitude of the electric and magnetic coupling between the near ends of a disturbing and disturbed pair of a connecting hardware pair combination.

5.4.2 Connecting hardware NEXT

Connecting hardware shall be tested for NEXT in both directions using at least one free connector.

5.4.3 Test method

NEXT is evaluated from the mixed mode parameter S_{DD21} for all conductor pair combinations. The mixed mode S-parameters are derived by transformation of the measured SE S-matrix.

5.4.4 Test set-up

The test set-up consists of a network analyzer and a test fixture as described in Clause 4. An illustration of the test set-up, which also shows the termination principles, is shown in Figure 7. Resistor termination networks in accordance with 4.11 shall be applied for all inactive pairs and for the ends of active pairs not being connected to the network analyzer ports. Interconnects (if used) shall be prepared and controlled per 4.13.2.

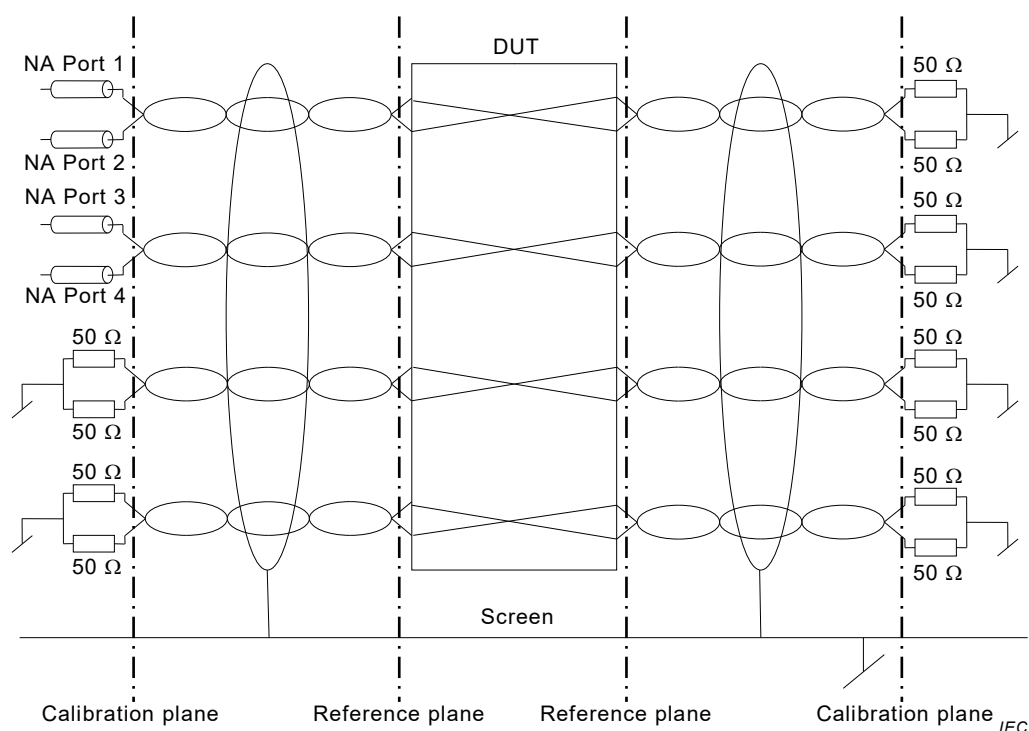


Figure 7 – NEXT measurement

5.4.5 Procedure

5.4.5.1 Calibration

A full 4-port SE calibration shall be performed at the calibration planes in accordance with 4.10. Reference loads used for calibration shall be in accordance with 4.11.

5.4.5.2 Establishment of noise floor

The noise floor of the set-up shall be measured. The level of the noise floor is determined by white noise, which may be reduced by increasing the test power and by reducing the bandwidth of the network analyzer, and by residual crosstalk within the test fixture.

The noise floor shall be measured by terminating the test ports of the test fixture with resistor termination networks and performing a full SE S-matrix measurement. The measured SE S-matrix is transformed into the associated mixed mode S-matrix to obtain the S-parameter S_{DD21} from which the noise floor is established. The noise floor shall be established for all possible conductor pair combinations.

The noise floor shall be 20 dB lower than any specified limit for the crosstalk. If the measured value is closer to the noise floor than 20 dB, this shall be reported.

For high crosstalk values, it may be necessary to screen the terminating resistors.

5.4.5.3 Measurement

The DUT shall be arranged in a test set-up according to 5.4.4 and Figure 7, including proper termination of the active and inactive pairs. A full SE S-matrix measurement shall be performed. The measured SE S-matrix shall be transformed into the associated mixed mode S-matrix to obtain the S-parameter S_{DD21} from which NEXT is determined.

The test has to be performed from both ends of the connecting hardware. Test all conductor pair combinations and record the results.

5.4.5.4 Determining pass and fail

The NEXT of the connecting hardware shall satisfy the requirements of the relevant detail specification for all pair combinations and in both directions.

5.4.6 Test report

The test results shall be reported in graphical or table format with the specification limits shown on the graphs or in the table at the same frequencies as specified in the relevant detail specification. Results for all pairs shall be reported. It shall be explicitly noted if the test results exceed the test limits.

5.4.7 Accuracy

The accuracy shall be better than ± 1 dB at measurements up to 60 dB and ± 2 dB at measurements up to 85 dB.

5.5 Far-end crosstalk (FEXT), test 28d

5.5.1 Object

The object of this test procedure is to measure the magnitude of the electric and magnetic coupling between the near end of a disturbing pair and the far end of disturbed pair of a connecting hardware pair combination.

5.5.2 Connecting hardware FEXT

Connecting hardware shall be tested for FEXT in both directions using at least one free connector.

5.5.3 Test method

FEXT is evaluated from the mixed mode parameter S_{DD21} for all conductor pair combinations. The mixed mode S-parameters are derived by transformation of the measured SE S-matrix.

5.5.4 Test set-up

The test set-up consists of a network analyzer and two test fixtures as described in Clause 4. An illustration of the test set-up, which also shows the termination principles, is shown in Figure 8. Resistor termination networks in accordance with 4.11 shall be applied for all inactive pairs and for the ends of active pairs not being connected to the network analyzer ports. Interconnects (if used) shall be prepared and controlled per 4.13.2.

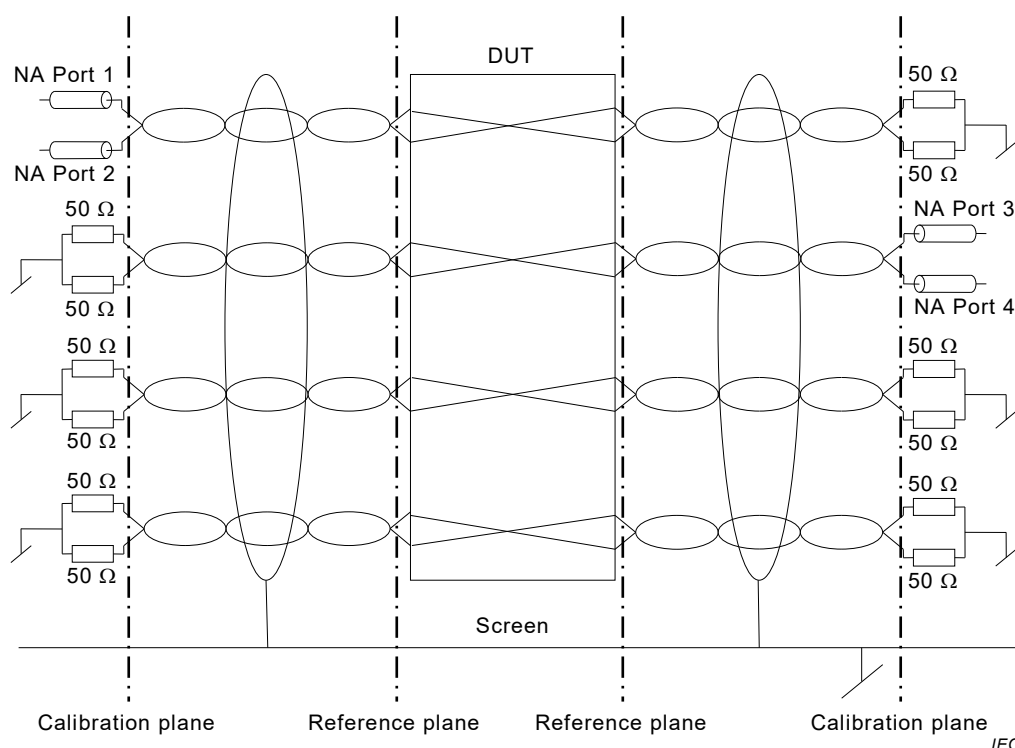


Figure 8 – FEXT measurement

5.5.5 Procedure

5.5.5.1 Calibration

A full 4-port SE calibration shall be performed at the calibration planes in accordance with 4.10. Reference loads used for calibration shall be in accordance with 4.11.

5.5.5.2 Establishment of noise floor

The noise floor of the set-up is established as outlined in 5.4.5.2.

5.5.5.3 Measurement

The DUT shall be arranged in a test set-up according to 5.5.4 and Figure 8, including proper termination of the active and inactive pairs. A full SE S-matrix measurement shall be performed. The measured SE S-matrix shall be transformed into the associated mixed mode S-matrix to obtain the S-parameter S_{DD21} from which FEXT is determined.

Test all conductor pair combinations and record the results.

5.5.6 Test report

The test results shall be reported in graphical or table format with the specification limits shown on the graphs or in the table at the same frequencies as specified in the relevant detail specification. Results for all pair combinations shall be reported. It shall be explicitly noted if the test results exceed the test limits.

5.5.7 Accuracy

The accuracy shall be better than ± 1 dB at measurements up to 60 dB and ± 2 dB at measurements up to 85 dB.

~~5.6 Transfer impedance (Z_T), Test 28e~~~~Refer to test 26e of IEC 60512-26-100.~~**5.6 Transverse conversion loss (TCL), test 28f****5.6.1 Object**

The object of this test is to measure the mode conversion (differential to common mode) of a signal in the conductor pairs of the DUT. This is also called unbalance attenuation or Transverse Conversion Loss, TCL.

5.6.2 Connecting hardware TCL

Connecting hardware shall be tested for TCL from both directions using at least one free connector.

5.6.3 Test method

TCL is evaluated from the mixed mode parameter S_{CD11} for all conductor pairs. The mixed mode S-parameters are derived by transformation of the measured SE S-matrix.

5.6.4 Test set-up

The test set-up consists of a network analyzer and a test fixture as described in Clause 4. An illustration of the test set-up, which also shows the termination principles, is shown in Figure 9. Resistor termination networks in accordance with 4.11 shall be applied for all inactive pairs and for the ends of active pairs not being connected to the network analyzer ports. Interconnects (if used) shall be prepared and controlled per 4.13.2.

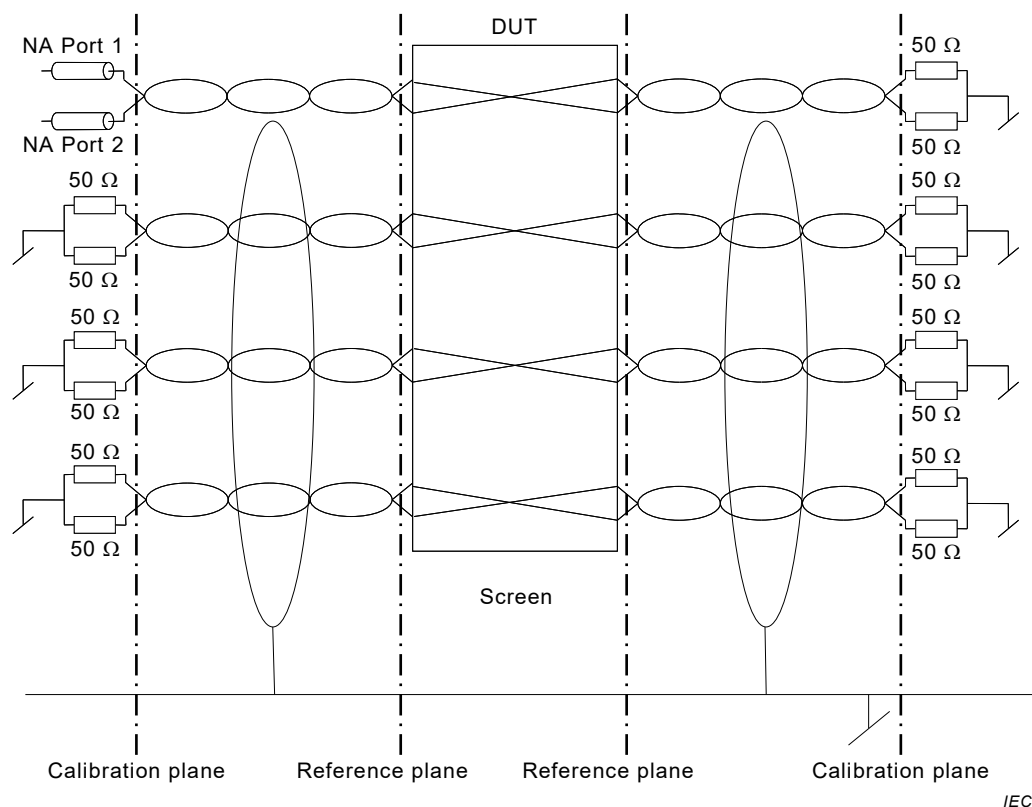


Figure 9 – Return loss and TCL measurement

5.6.5 Procedure

5.6.5.1 Calibration

A full 2-port SE calibration shall be performed at the calibration plane in accordance with 4.10. Reference loads used for calibration shall be in accordance with 4.11.

5.6.5.2 Noise floor

The noise floor of the set-up shall be measured. The level of the noise floor is determined by white noise, which may be reduced by increasing the test power and by reducing the bandwidth of the network analyzer, and by residual intermodal crosstalk within the test fixture.

The noise floor, a_{noise} , shall be measured by terminating the test ports of the test fixture with resistor termination networks and performing a full SE S-matrix measurement. The measured SE S-matrix is transformed into the associated mixed mode S-matrix to obtain the S-parameter $S_{\text{CD}11}$ from which the noise floor is calculated as:

$$a_{\text{noise}} = -20 \log |S_{\text{CD}11}| \quad (2)$$

The noise floor shall be established for all conductor pairs.

The noise floor shall be 20 dB lower than any specified limit for balance. If the measured value is closer to the noise floor than ~~10~~ 20 dB, this shall be reported.

5.6.5.3 Measurement

The DUT shall be arranged in a test set-up according to 5.6.4 and Figure 9, including proper termination of the active and inactive pairs. A full SE S-matrix measurement shall be performed. The measured SE S-matrix shall be transformed into the associated mixed mode S-matrix to obtain the S-parameter $S_{\text{CD}11}$ from which TCL is calculated as:

$$TCL = -20 \log |S_{\text{CD}11}| \quad (3)$$

The test ~~has to~~ shall be performed from ~~the free connector end of~~ both directions on the connecting hardware. Test all conductor pairs and record the results.

5.6.6 Test report

The test results shall be reported in graphical or table format with the specification limits shown on the graphs or in the table at the same frequencies as specified in the relevant detail specification. Results for all pairs shall be reported. It shall be explicitly noted if the test results exceed the test limits.

5.6.7 Accuracy

The accuracy shall be better than ± 1 dB at the specification limit.

5.7 Transverse conversion transfer loss (TCTL), test 28g

5.7.1 Object

The object of this test is to measure the mode conversion (differential to common mode) of a signal in the conductor pairs of the DUT at the far end. This is also called far end unbalance attenuation or Transverse Conversion Transfer Loss, TCTL.

5.7.2 Connecting hardware TCTL

Connecting hardware shall be tested for TCTL from both directions using at least one free connector.

5.7.3 Test method

TCTL is evaluated from the mixed mode parameter S_{CD21} for all conductor pairs. The mixed mode S-parameters are derived by transformation of the measured SE S-matrix.

5.7.4 Test set-up

The test set-up consists of a network analyzer and two test fixtures as described in Clause 4. An illustration of the test set-up, which also shows the termination principles, is shown in Figure 6. Resistor termination networks in accordance with 4.11 shall be applied for all inactive pairs. Interconnects (if used) shall be prepared and controlled per 4.13.2.

5.7.5 Procedure

5.7.5.1 Calibration

A full 4-port SE calibration shall be performed at the calibration planes in accordance with 4.10. Reference loads used for calibration shall be in accordance with 4.11.

5.7.5.2 Noise floor

The noise floor of the test set-up shall be measured using the same approach as outlined in 5.7.5.2 adapted to the 4-port test set-up used for TCTL.

The noise floor a_{noise} is calculated from S_{CD21} as:

$$a_{\text{noise}} = -20 \log |S_{CD21}| \quad (4)$$

The same requirements as described in 5.6.5.2 adapted to TCTL as in the four--ort test set-up used for TCL ~~measurements apply.~~

5.7.5.3 Measurement

The DUT shall be arranged in a test set-up according to 5.7.4 and Figure 6, including proper termination of the active and inactive pairs. A full SE S-matrix measurement shall be performed. The measured (4-port) SE S-matrix shall be transformed into the associated (2-port) mixed mode S-matrix to obtain the S-parameter S_{CD21} from which TCTL is calculated as:

$$\text{TCTL} = -20 \log |S_{CD21}| \quad (5)$$

~~The test has to be performed from the free connector end of the connecting hardware. Test all conductor pairs and record the results.~~

The test shall be performed from both directions on the connecting hardware. Test all conductor pairs and record the results.

5.7.6 Test report

The test results shall be reported in graphical or table format with the specification limits shown on the graphs or in the table at the same frequencies as specified in the relevant detail

specification. Results for all pairs shall be reported. It shall be explicitly noted if the test results exceed the test limits.

5.7.7 Accuracy

The accuracy shall be better than ± 1 dB at the specification limit.

5.8 Shield transfer impedance (Z_T), test 26e

5.8.1 Object

The object of this test is to measure the shield transfer impedance of a common-mode signal transferred through the shield of the DUT. This is specifically required for screened connectors and not applicable for unshielded connectors.

For further information on applicability, see IEC 60512-26-100, refer to test 26e.

5.8.2 Connecting hardware Transfer impedance (Z_T)

Connecting hardware Transfer impedance (Z_T) shall be tested in accordance with IEC 62153-4-12 up to 100 MHz.

5.8.3 Test method

The transfer impedance (Z_T) test method for connectors utilizes the triaxial-tube-in-tube or the line injection apparatus.

5.8.4 Test set-up

The test set-up consists of a network analyzer and test fixtures as described in IEC 62153-4-12. Resistor termination networks in accordance with 4.11 shall be applied for all inactive pairs. Interconnects (if used) shall be prepared and controlled per 4.13.2.

Transfer impedance measurement data may be derived from a common measurement set-up described in IEC 62153-4-12.

5.8.5 Procedure

5.8.5.1 Calibration

A full 4-port SE calibration shall be performed at the calibration planes in accordance with 4.10. Reference loads used for calibration shall be in accordance with 4.11.

5.8.5.2 Noise floor

The noise floor of the test set-up shall be measured using the same approach as outlined in 5.6.5.2 adapted to the 4-port test set-up used for TCTL.

The noise floor a_{noise} is calculated from S_{CD21} as:

$$a_{\text{noise}} = -20 \log |S_{CD21}| \quad (6)$$

5.8.5.3 Measurement

The DUT shall be arranged in a test set-up according to the test fixtures as described in IEC 62153-4-12.

The test should be performed from the free connector end of the connecting hardware. Test all conductor pairs and record the results.

5.8.6 Test report

The test results shall be reported in graphical or table format with the specification limits shown on the graphs or in the table at the same frequencies as specified in the relevant detail specification. Results for all pairs shall be reported. It shall be explicitly noted if the test results exceed the test limits.

5.8.7 Accuracy

The accuracy shall be better than ± 3 dB at the specification limit.

5.9 Coupling attenuation (a_C)

~~Coupling attenuation measurements, when required by the detail specification, apply only to shielded connectors.~~

~~Coupling attenuation shall be performed per IEC 62153-4-12, over the frequency range of 30 MHz to 1 000 MHz.~~

5.9.1 Object

The object of this test is to measure the mode conversion ratio through the shield (common-mode to differential-mode) of a signal in the conductor pairs of the DUT. This is specifically required for screened connectors and optional for unshielded connectors.

5.9.2 Connecting hardware coupling attenuation (a_C)

Connecting hardware coupling attenuation shall be tested in accordance with IEC 62153-4-12 over the frequency range of 30 MHz to 2 000 MHz.

5.9.3 Test method

The coupling attenuation (a_C) test method for connectors is based on coupling attenuation (a_C) measurement methods, which utilize triaxial-tube-in-tube or absorbing-clamp apparatus.

5.9.4 Test set-up

The test set-up consists of a network analyzer and test fixtures as described in IEC 62153-4-12. Resistor termination networks in accordance with 4.11 shall be applied for all inactive pairs. Interconnects (if used) shall be prepared and controlled per 4.13.2.

5.9.5 Procedure

5.9.5.1 Calibration

A full 4-port SE calibration shall be performed at the calibration planes in accordance with 4.10. Reference loads used for calibration shall be in accordance with 4.11.

5.9.5.2 Noise floor

The noise floor of the test set-up shall be measured using the same approach as outlined in 5.6.5.2 adapted to the 4-port test set-up used for TCTL.

The noise floor a_{noise} is calculated from S_{CD21} as:

$$a_{\text{noise}} = -20 \log |S_{\text{CD}21}| \quad (7)$$

5.9.5.3 Measurement

The DUT shall be arranged in a test set-up according to the test fixtures as described in IEC 62153-4-12 including proper termination of the active and inactive pairs. A full SE S-matrix measurement shall be performed. The measured (three-port) SE S-matrix shall be transformed into the associated (two-port) mixed mode S-matrix to obtain the S-parameter $S_{\text{SD}21}$ from which coupling attenuation is calculated as.

$$\text{AC} = -20 \log |S_{\text{CD}21}| \quad (8)$$

The test should be performed from the free connector end of the connecting hardware. Test all conductor pairs and record the results.

5.9.6 Test report

The test results shall be reported in graphical or table format with the specification limits shown on the graphs or in the table at the same frequencies as specified in the relevant detail specification. Results for all pairs shall be reported. It shall be explicitly noted if the test results exceed the test limits.

5.9.7 Accuracy

The accuracy shall be better than ± 3 dB at the specification limit.

Annex A (informative)

Example Derivation of mixed mode parameters using the modal decomposition technique

A.1 General

It is not a requirement of this document to require that a full derivation is produced, and any method of extracting the required S-parameters is acceptable. This may be achieved by the use of network analyzer hardware functions, specific mathematical software, or by circuit simulation tools.

This informative annex presents a summary of how to derive mixed mode parameters from 4-port measurements of S-parameters.

An impedance matrix (Z) of the DUT can be calculated based on Formula A.1.

$$\begin{bmatrix} V_1 \\ V_2 \\ V_3 \\ V_4 \end{bmatrix} = \begin{bmatrix} Z_{11} & Z_{12} & Z_{13} & Z_{14} \\ Z_{21} & Z_{22} & Z_{23} & Z_{24} \\ Z_{31} & Z_{32} & Z_{33} & Z_{34} \\ Z_{41} & Z_{42} & Z_{43} & Z_{44} \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \\ I_3 \\ I_4 \end{bmatrix} \quad (\text{A.1})$$

Where V is the voltage and I is the current, see Figure A.1:

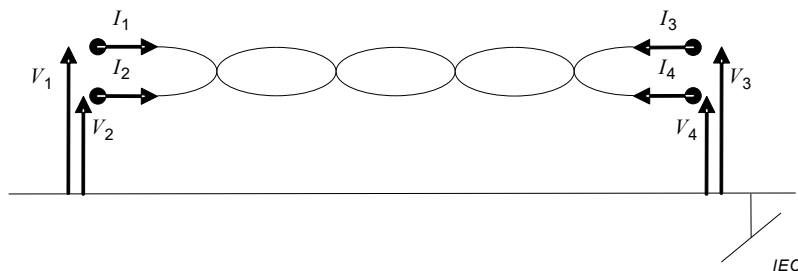


Figure A.1 – Voltage and current on balanced DUT

A.2 Example of a calculation

The modal domain impedance matrix $[Z^m]$ is then calculated from Formula A.2 below, using the conversion matrices given in Formula A.3 and Formula A.4.

$$Z^m = P_e^{-1} Z Q_e \quad (\text{A.2})$$

$$P_e^{-1} = \begin{bmatrix} P^{-1} & 0 \\ 0 & P^{-1} \end{bmatrix} \quad (\text{A.3})$$

$$Q_e = \begin{bmatrix} Q & 0 \\ 0 & Q \end{bmatrix} \quad (\text{A.4})$$

In the case of a 1 pair DUT, the size of the conversion matrices becomes 4x4 with the values given in Formula A.5 and Formula A.6

$$P = \begin{bmatrix} \frac{1}{2} & 1 \\ -\frac{1}{2} & 1 \end{bmatrix} \quad (\text{A.5})$$

$$Q = \begin{bmatrix} 1 & \frac{1}{2} \\ -1 & \frac{1}{2} \end{bmatrix} \quad (\text{A.6})$$

The conversion matrices replace the balun transformers and are referred to as mathematical baluns, producing Formula A.7 and Formula A.8.

$$\begin{bmatrix} V_1 \\ V_2 \\ V_3 \\ V_4 \end{bmatrix} = P_e \begin{bmatrix} V_{D1} \\ V_{C1} \\ V_{D2} \\ V_{C2} \end{bmatrix} \quad (\text{A.7})$$

$$\begin{bmatrix} I_1 \\ I_2 \\ I_3 \\ I_4 \end{bmatrix} = Q_e \begin{bmatrix} I_{D1} \\ I_{C1} \\ I_{D2} \\ I_{C2} \end{bmatrix} \quad (\text{A.8})$$

Substituting Formula A.7 and Formula A.8 into Formula A.1 Formula A.9 is obtained, which is equivalent to a set of hybrid transformers attached at each end of the cable pair as described in Figure A.2.

$$\begin{bmatrix} V_{D1} \\ V_{C1} \\ V_{D2} \\ V_{C2} \end{bmatrix} = \begin{bmatrix} Z_{11}^m & Z_{12}^m & Z_{13}^m & Z_{14}^m \\ Z_{21}^m & Z_{22}^m & Z_{23}^m & Z_{24}^m \\ Z_{31}^m & Z_{32}^m & Z_{33}^m & Z_{34}^m \\ Z_{41}^m & Z_{42}^m & Z_{43}^m & Z_{44}^m \end{bmatrix} \begin{bmatrix} I_{D1} \\ I_{C1} \\ I_{D2} \\ I_{C2} \end{bmatrix} \quad (\text{A.9})$$

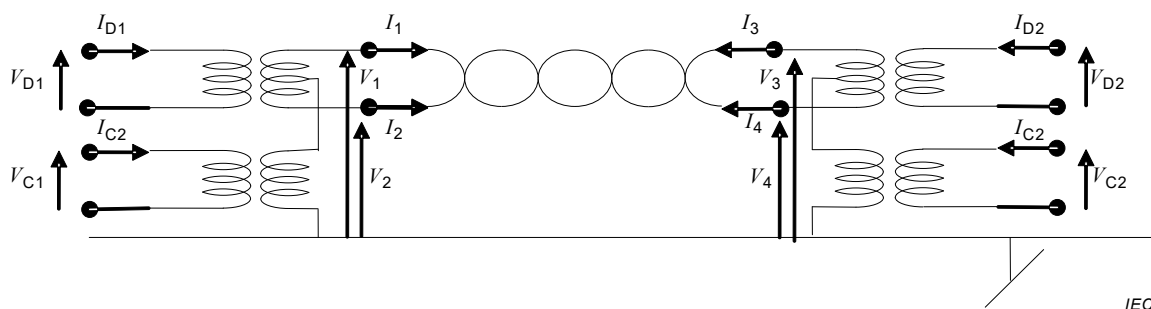


Figure A.2 – Voltage and current on unbalanced DUT

For the measurements concerned in this standard, S-parameters are measured and converted into Z-Parameters. The Z-parameter matrix of a 2n-port circuit can be derived using Formula A.10.

$$Z = R_2^{\frac{1}{2}} [E + S] [E - S]^{-1} R_2^{\frac{1}{2}} \quad (\text{A.10})$$

Where E is a 2n x 2n unit matrix and $R_2^{\frac{1}{2}}$ is given by Formula A.11.

$$R_m^{\frac{1}{2}} = \begin{bmatrix} \sqrt{r_1} & 0 & \dots & 0 \\ 0 & \sqrt{r_2} & & \vdots \\ \vdots & 0 & \ddots & 0 \\ 0 & \dots & 0 & \sqrt{r_{2n}} \end{bmatrix} \quad (\text{A.11})$$

Where r_x is the impedance of the measurement port, typically 50 Ω , giving Formula A.12

$$R_m^{\frac{1}{2}} = \begin{bmatrix} \sqrt{50} & 0 & \dots & 0 \\ 0 & \sqrt{50} & & \vdots \\ \vdots & 0 & \ddots & 0 \\ 0 & \dots & 0 & \sqrt{50} \end{bmatrix} \quad (\text{A.12})$$

The S-parameters in the modal domain are then calculated using Formula A.13, giving Formula A.14.

$$S^m = R_m^{-\frac{1}{2}} [Z^m - R_m] [Z^m + R_m]^{-1} R_m^{\frac{1}{2}} \quad (\text{A.13})$$

$$R_m^{\frac{1}{2}} = \begin{bmatrix} \sqrt{r_{m1}} & 0 & \dots & 0 \\ 0 & \sqrt{r_{m2}} & & \vdots \\ \vdots & 0 & \ddots & 0 \\ 0 & \dots & 0 & \sqrt{r_{m2n}} \end{bmatrix} \quad (\text{A.14})$$

By this method it is possible to convert unbalance network analyzer measurements into mixed mode S-matrices which contain both balanced and unbalanced parameters, as in Formula A.15.

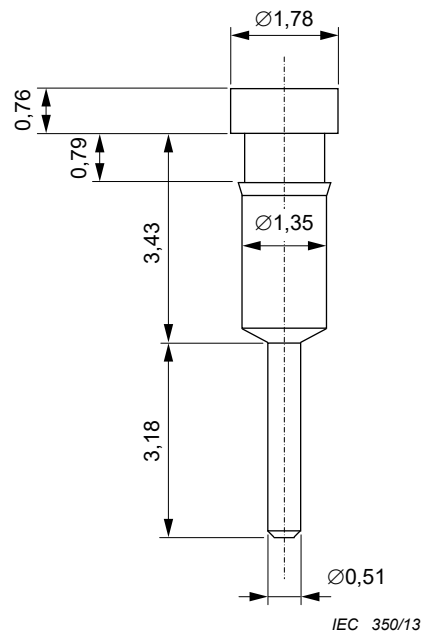
$$\begin{bmatrix} S_{11} & S_{12} & S_{13} & S_{14} \\ S_{21} & S_{22} & S_{23} & S_{24} \\ S_{31} & S_{32} & S_{33} & S_{34} \\ S_{41} & S_{42} & S_{43} & S_{44} \end{bmatrix} \Rightarrow \begin{bmatrix} S_{DD11} & S_{DC11} & S_{DD12} & S_{DC12} \\ S_{CD11} & S_{CC11} & S_{CD12} & S_{CC12} \\ S_{DD21} & S_{DC21} & S_{DD22} & S_{DC22} \\ S_{CD21} & S_{CC21} & S_{CD22} & S_{CC22} \end{bmatrix} \quad (\text{A.15})$$

Annex B **(informative)**

Test pins — Dimensions and references

~~This annex contains dimensions and references of commonly used test interface pins (see Figure B.1). Use of such items is not required by this standard but may allow increased compatibility of sample prepared for test with other test laboratories.~~

Dimension in millimeters



Key

General tolerance:

Linear = 0,127 mm

Diameter = 0,05 mm

Angular = 2°

Figure B.1 — Example of pin and fixed connector dimensions

~~Example of fixed connector description:~~

~~Mill-Max 1001-0-15-15-30-27-04-0~~

~~Material: Brass alloy~~

~~Contact: 30 = Standard 4 finger contact~~

~~Contact material: Beryllium copper~~

~~Shell plating: 15 = 8,5 µm (10 µ") gold over nickel~~

~~Contact plating: 27 = 25,4 µm (30 µ") gold over nickel~~

~~Press-in 1,45 mm (0,057 in) mounting hole~~

Annex B (normative)

Indirect-reference test fixtures

B.1 General

Indirect-reference test fixtures are used for measurement of transmission parameters for connector types e.g. IEC 60603-7 series (8-pole types), IEC 60603-7-2, IEC 60603-7-3, IEC 60603-7-4, IEC 60603-7-5, IEC 60603-7-41, IEC 60603-7-51, IEC 60603-7-81.

The indirect-reference test fixtures and associated test procedures used for measuring IEC 60603-7 series 8-pole connector types transmission parameters shall conform to their respective detail specification test procedures requirements and to the requirements in this annex. In the case that a conflict arises between the requirements in the respective detail specification test procedures and this standard, the respective detail specification test procedures shall take precedence.

The IEC 60603-7 series, 8-pole connector types detail specifications and respective detail test procedures standards for connector transmission parameters measurements are given in Table B.1.

**Table B.1 – IEC 60603-7 series, 8-pole connector types detail specifications
and respective detail connector test procedures standards**

Connector specification	Frequency MHz	Test procedure	Frequency MHz
IEC 60603-7-2 IEC 60603-7-3	100	IEC 60512-26-100	100
IEC 60603-7-4 IEC 60603-7-5	250	IEC 60512-26-100	250
IEC 60603-7-41 IEC 60603-7-51	500	IEC 60512-27-100	500
IEC 60603-7-81	2 000	IEC PAS 60512-27-200	2 000

B.2 Requirements

B.2.1 General requirements

Indirect-reference test fixtures shall meet the overall test fixtures minimum signal integrity requirements listed in 4.14.

NOTE The overall test fixtures minimum signal integrity requirements are additional requirements, applicable to all test fixture types.

B.2.2 Specific requirements

Reference test fixtures shall conform to the test fixtures requirements given in the respective connector standard test procedure standard, given in Table B.1.

The 2 000 MHz reference connector crosstalk (NEXT) vector for testing crosstalk compensation circuits shall conform to the limits or fall within the boundary value ranges described in Table B.2.

The reference test fixtures shall be modified for 2 000 MHz according to the requirements given in the supplemental 2 000 MHz connector standard test procedure, IEC PAS 60512-27-200.

Table B.2 – Reference connector crosstalk (NEXT) vector

Case #	Pair combination	Limit	Limit free connector NEXT loss magnitude dB	Limit free connector NEXT loss phase (degrees) ^{1), 2)}
Case 1	3,6-4,5	Low	$78,1-20\log(f)$	Measured TFC phase
Case 2	3,6-4,5	Central	$78,6-20\log(f)$	Measured TFC phase
Case 3	3,6-4,5	Central	$79,0-20\log(f)$	Measured TFC phase
Case 4	3,6-4,5	High	$79,5-20\log(f)$	Measured TFC phase
Case 5	1,2-3,6	Low	$86,5-20\log(f)$	Measured TFC phase
Case 6	1,2-3,6	High	$89,5-20\log(f)$	Measured TFC phase
Case 7	3,6-7,8	Low	$86,5-20\log(f)$	Measured TFC phase
Case 8	3,6-7,8	High	$89,5-20\log(f)$	Measured TFC phase
Case 9	1,2-4,5	Low	$97-20\log(f)$	+90
Case 10	1,2-4,5	High	$110-20\log(f)$	-90
Case 11	4,5-7,8	Low	$97-20\log(f)$	+90
Case 12	4,5-7,8	High	$110-20\log(f)$	-90
Case 13	1,2-7,8	Low	$106-20\log(f)$	Measured TFC phase
Case 14	1,2-7,8	High	$106-20\log(f)$	Measured TFC phase minus 180°
¹⁾ TFC NEXT loss phase is determined by following the procedure in IEC PAS 60512-27-200.				
²⁾ The reference plane for measuring TFC NEXT loss phase and mated NEXT loss shall be the interface plane as described in IEC PAS 60512-27-200.				