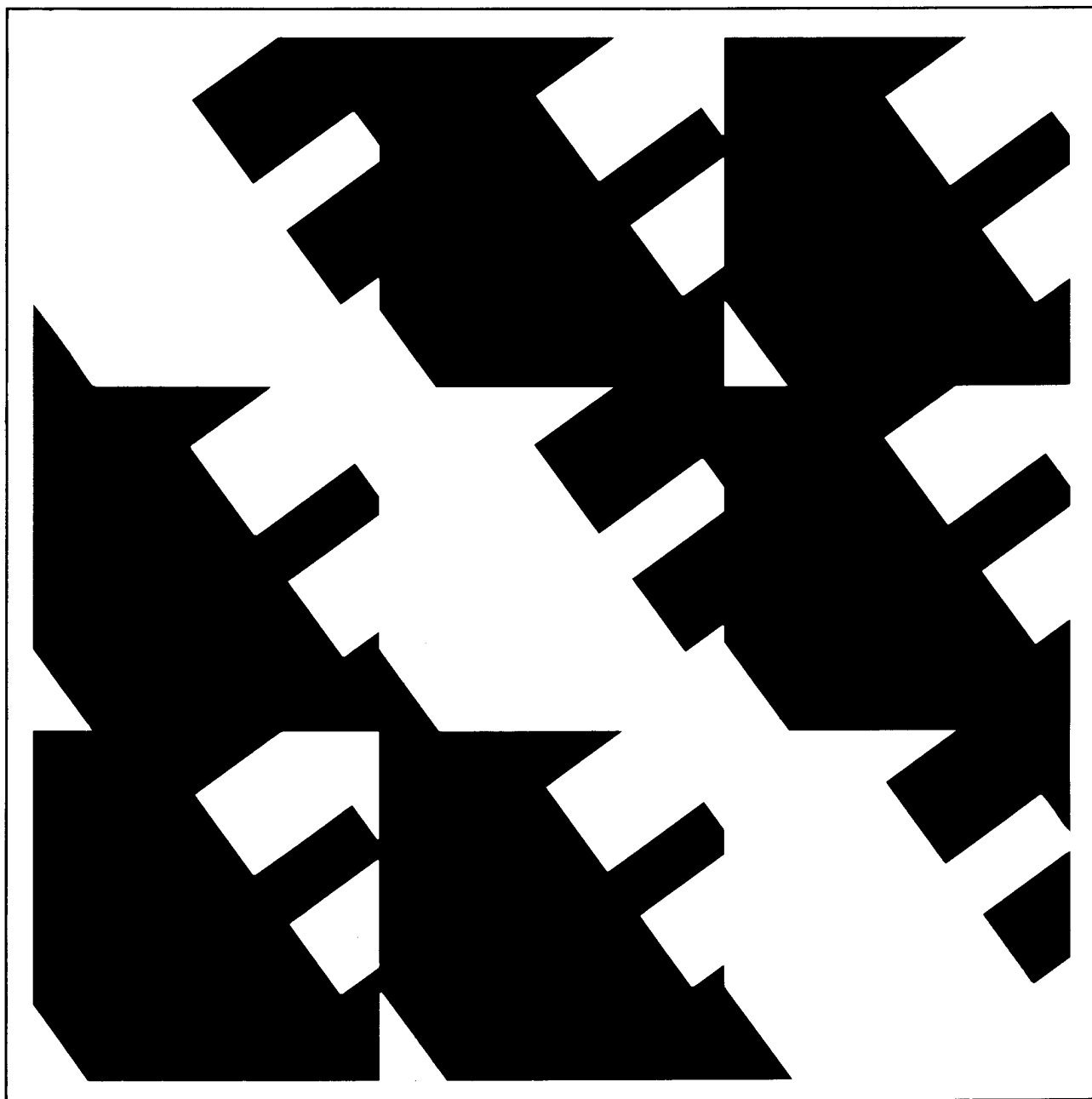


IEEE Standard for Microprocessor Assembly Language

IEEE Std 694-1985



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IEEE Standard for Microprocessor Assembly Language

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of the
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Foreword

(This Foreword is not a part of the IEEE Std 694-1985, IEEE Standard for Microprocessor Assembly Language.)

The microprocessor industry abounds with different assembly language syntaxes and instruction mnemonics. To eliminate confusion, shorten learning time, reduce errors, and promote portability, a standard is clearly needed. This document standardizes existing practice: it specifies a set of instruction mnemonics and descriptions, establishes a standard assembly language syntax, and provides procedures for consistent extension.

The 694 working group for a Microprocessor Assembly Language Standard was formed in 1977 under the auspices of the Microprocessor Standards Committee, which is in turn sponsored by the IEEE Computer Society Technical Committee on Microcomputers and Microprocessors. This standard has evolved over the years until it is now appropriate for almost all general purpose microprocessors, from yesterday's simple 4-bit machines to tomorrow's elaborate architectures.

Suggestions for the improvement of this standard are welcome. They should be addressed to

Secretary
IEEE Standards Board
Institute of Electrical and Electronics Engineers
345 East 47th Street
New York, NY 10017

We wish to thank the many members of the public who have contributed their ideas and efforts toward this standard. Special thanks are due to Wayne Fischer, who chaired 694 until late 1983.

At the time this standard was approved the working group members were as follows:

Geoff Baldwin, *Chairman*

Richard E. James III
Tom Pittman

Stephen Savitzky
Robert G. Stewart

When the IEEE Standards Board approved this standard on March 21, 1985, it had the following membership:

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