
**Information technology —
Telecommunications and information
exchange between systems — Powerline
communication (PLC) — High speed PLC
medium access control (MAC) and
physical layer (PHY) —**

**Part 1:
General requirements**

*Technologies de l'information — Télécommunications et échange
d'information entre systèmes — Courants porteurs en ligne (PLC) —
Contrôle d'accès au support (MAC) et couche physique (PHY) par PLC
à grande vitesse —*

Partie 1: Exigences générales



PDF disclaimer

This PDF file may contain embedded typefaces. In accordance with Adobe's licensing policy, this file may be printed or viewed but shall not be edited unless the typefaces which are embedded are licensed to and installed on the computer performing the editing. In downloading this file, parties accept therein the responsibility of not infringing Adobe's licensing policy. The ISO Central Secretariat accepts no liability in this area.

Adobe is a trademark of Adobe Systems Incorporated.

Details of the software products used to create this PDF file can be found in the General Info relative to the file; the PDF-creation parameters were optimized for printing. Every care has been taken to ensure that the file is suitable for use by ISO member bodies. In the unlikely event that a problem relating to it is found, please inform the Central Secretariat at the address given below.



COPYRIGHT PROTECTED DOCUMENT

© ISO/IEC 2009

All rights reserved. Unless otherwise specified, no part of this publication may be reproduced or utilized in any form or by any means, electronic or mechanical, including photocopying and microfilm, without permission in writing from either ISO at the address below or ISO's member body in the country of the requester.

ISO copyright office
Case postale 56 • CH-1211 Geneva 20
Tel. + 41 22 749 01 11
Fax + 41 22 749 09 47
E-mail copyright@iso.org
Web www.iso.org

Published in Switzerland

Contents

Page

Foreword.....	iv
1 Scope	1
2 Normative References	1
3 Terms and Definitions	2
4 Acronyms and Abbreviations	5
5 Reference Models	8
5.1 PLC Reference Model.....	8
5.2 Interface Protocol Reference Model	8
5.3 PLC Network Topology	9
6 PHY Specification	10
6.1 Overview of PHY	10
6.2 PSDU Format.....	11
6.3 DMT Transmitter	14
6.4 Transmission Mode	21
7 MAC Specification	24
7.1 Structure of MAC	24
7.2 Carrier Sense Multiple Access/Collision Avoidance (CSMA/CA)	27
7.3 PSDU Format.....	38
7.4 Address Resolution.....	50
7.5 Interactive Operation with Link Layer	51
7.6 Priority Classification.....	52
7.7 Proxy Setting Procedure.....	52
7.8 Channel Estimation (CE) Procedure.....	53
7.9 Security.....	56
7.10 Repeater Function by Cell Bridge (CB)	56
7.11 Request To Send (RTS)/Clear To Send (CTS).....	57
7.12 Link Restriction Function for Application of Access Network	62
Annex A (informative) Solution to Hidden-Node Problem	63
A.1 Solutions Other than RTS/CTS	63
A.2 Data Communication Procedure Considering Hidden STAs.....	64

Foreword

ISO (the International Organization for Standardization) and IEC (the International Electrotechnical Commission) form the specialized system for worldwide standardization. National bodies that are members of ISO or IEC participate in the development of International Standards through technical committees established by the respective organization to deal with particular fields of technical activity. ISO and IEC technical committees collaborate in fields of mutual interest. Other international organizations, governmental and non-governmental, in liaison with ISO and IEC, also take part in the work. In the field of information technology, ISO and IEC have established a joint technical committee, ISO/IEC JTC 1.

International Standards are drafted in accordance with the rules given in the ISO/IEC Directives, Part 2.

The main task of the joint technical committee is to prepare International Standards. Draft International Standards adopted by the joint technical committee are circulated to national bodies for voting. Publication as an International Standard requires approval by at least 75 % of the national bodies casting a vote.

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. ISO and IEC shall not be held responsible for identifying any or all such patent rights.

ISO/IEC 12139-1 was prepared by Korean Agency for Technology and Standards (as KS X 4600-1) and was adopted, under a special “fast-track procedure”, by Joint Technical Committee ISO/IEC JTC 1, *Information technology*, in parallel with its approval by the national bodies of ISO and IEC.

ISO/IEC 12139 consists of the following parts, under the general title *Information technology — Telecommunications and information exchange between systems — Powerline communication (PLC) — High speed PLC medium access control (MAC) and physical layer (PHY)*:

— *Part 1: General requirements*

Advanced MAC and PHY requirements will form the subject of a future Part 2.

Part 1 covers MAC and PHY technology for In-home/Access data networks via powerline communications (PLC), the system of which is operating below 30MHz. The coexistence schemes will be considered in developing Part 2, which will apply to data and high quality multimedia networks requiring advanced MAC and PHY technology. The used or forbidden band of this standard will be subject to national regulations.

Information technology — Telecommunications and information exchange between systems — Powerline communication (PLC) — High speed PLC medium access control (MAC) and physical layer (PHY) —

Part 1: General requirements

1 Scope

The scope of this standard is a physical and medium access control layer specification with respect to the connectivity for 'In-home' and 'Access' network high speed powerline communication stations.

This standard provides functional requirements and specification of the physical and medium access control layer for high speed powerline communication devices, and does not include specific implementation methods.

2 Normative References

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

ISO/IEC 8802-11:2005, Information technology — Telecommunications and information exchange between systems — Local and metropolitan area networks — Specific requirements — Part 11: Wireless LAN Medium Access Control (MAC) and Physical Layer (PHY) specifications

ITU-T G.992.1: Asymmetric Digital Subscriber Line (ADSL) Transceivers

ITU-T G.994.1: Handshake Procedure for Digital Subscriber Line (DSL) Transceivers

IEEE Std 802.3:2000, Information technology — Local and metropolitan area networks — Part 3: Carrier sense multiple access with collision detection (CSMA/CD) access method and physical layer specifications

FCC Rules, 47 CFR (10-1-98 Edition), Part 15: Radio Frequency Devices

Federal Information Processing Standards: Publication 46-3 Data Encryption Standard (DES)

T1E1.4 Trial-Use Standard: Very-High-Bit-Rate Digital Subscriber Lines (VDSL) Metallic Interface Part 1: Functional Requirements and Common Specification

T1E1.4 Trial-Use Standard: Very-High-Bit-Rate Digital Subscriber Lines (VDSL) Metallic Interface Part 3: Technical Specification for a Multi-Carrier Modulation (MCM) Transceiver

3 Terms and Definitions

3.1 Ad-hoc network

A network consisting of only stations within the boundary of communication through powerlines
The ad-hoc network is typically generated in a voluntary manner.

3.2 Backbone

A facility or a collection of facilities for connecting LAN to WAN

3.3 Backoff Period

A period during which stations contend for the medium access

3.4 Backoff Procedure

A procedure to disperse the times at which stations with queued frames attempt transmission

3.5 Backoff Value

The number of time slots that a station shall wait for initiating a transaction

3.6 Bit

The basic unit of the binary system

In binary system, every number is expressed in '0' or '1', each of which is a bit.

3.7 Byte

A unit comprised of a set of bits, the basic unit of data representing '0' or '1'
8 bits constitute 1 byte.

3.8 Carrier Sense

A station's standard for determining whether the medium is currently occupied

3.9 Cell

A synonym for logical network

3.10 Cell Bridge (CB)

A station connecting two different cells

Cell Bridge provides the repeater functionality.

3.11 Ciphertext

Encrypted data

3.12 Cleartext

Unencrypted data

3.13 Collision

An event of two or more frames colliding in the medium, caused by simultaneous transmission of the frames

3.14 Contention Window

A slotted range in which each station can select a time slot to initiate a transaction

3.15 Delimiter

A combination of preamble and control frame

3.16 Differential Modulation

A modulation that encodes information by the 'phase difference' between two consecutive symbols

3.17 Discrete Multi-Tone (DMT)

A modulation technique in which a channel with a certain bandwidth is divided into subchannels (or tones) of narrower bandwidths
Each subchannel is modulated by a different subcarrier.

3.18 Flip-flop

A circuit that has two stable states
It maintains its state until the input decides on one stable state and another input approves of it by deciding on the other state.
It can memorize a bit by corresponding two stable states to '1' and '0'.

3.19 Frame

A synonym for PSDU

3.20 Home Networking

Sharing digital data and constructing an environment with availability of broadband communication by forming a network between information devices at home

3.21 InterFrame Space

A time interval between frames on the medium

3.22 Link Timer

A value that increases at each symbol after the link between two stations is established

3.23 Logical Network

A network classified by Group Identifier (GID)
A single physical network can be divided into more than one logical network.
Logical Network is a synonym for cell.

3.24 MAC Management Information (MMI)

Management information for MAC generated by MAC Management Entity (MME)

3.25 MAC Protocol Data Unit (MPDU)

A frame unit that consists of frame header and frame body
Frame body contains either MSDU(s) or MMI(s), each in FBB format.

3.26 MAC Service Data Unit (MSDU)

A frame unit used in the MAC layer
It contains data from link layer.

3.27 Medium BUSY

A medium state indicating that a station has occupied the medium
To determine this state, Physical Carrier Sense (PCS) and Virtual Carrier Sense (VCS) are used.

3.28 Medium CONTENTION

A medium state indicating that stations are contending for the medium access
It starts after Short Contention InterFrame Space (SCIFS) or Long Contention InterFrame Space (LCIFS) from the end of the last previous transaction.

3.29 Medium IDLE

A medium state indicating that no station has occupied the medium
It starts after SCIFS or LCIFS plus maximum Contention Window Size (CWS) from the end of the last previous transaction.

3.30 MAC Interface (MI)

The logical interface between the upper link layer and the MAC layer of the station

3.31 Network

A collection of interconnected elements that provides connection services to users

3.32 PHY Interface (PI)

The physical interface between the station and powerline

3.33 Privacy

The service to prevent the content of messages from being read by others beside the intended recipients

3.34 Proxy Station

The representative of all stations within a logical network
It renders partial Automatic Repeat reQuest (ARQ) possible.

3.35 PHY Service Data Unit (PSDU)

A frame unit used in physical layer

3.36 Reassembly

The reverse process of segmentation

3.37 Repeater

A station that relays frames from one station to another station for which direct communication is impeded

3.38 Routing Table

A table that maps MAC addresses to Station Identifier (SID) and Tone Map Index (TMI)

3.39 Scrambler

A circuit that converts the input data into random signal series in order to repress the single frequency component by repeating regular data patterns among the successive input data

3.40 Segmentation

A process of partitioning a service block into multiple segments

3.41 Serial Interface

The interface in which all the data are transmitted through the same communication line, bit after bit

3.42 Service Block

A synonym for Frame Body Block (FBB)

3.43 Station

A synonym for PLC Transceiver Unit (PTU)

3.44 Sub-frame

A group of symbols existing in a frame
Sub-frame includes the control frame and the data frame.

3.45 Symbol

A bit or a defined sequence of bits

3.46 Symbol Block

A group of symbols processed as one unit in physical layer
Its length is always 16 symbols.

3.47 Time Slot

A time unit used in backoff procedure

3.48 Transaction

A minimal set of interactively transmitted/received frames between two stations

3.49 Transaction Combo

A combination of consecutive transactions

Except for the first transaction, all transactions in a transaction combo do not contend for the medium.

The interframe space between two contiguous transactions in a transaction combo is always Short Response InterFrame Space (SRIFS).

3.50 Transmission Mode

Frame-based transmission scheme

Transmission mode is classified into Diversity (DV), Extended DV (EDV), and NORMAL modes.

3.51 VLAN Tag

The field within the layer-2 frame header defined in 802.1Q

4 Acronyms and Abbreviations

ACK	ACKnowledgement
AES	Advanced Encryption Standard
AG	AGC Gain
AGC	Automatic Gain Control
ARQ	Automatic Repeat reQuest
A/V	Audio/Video
BF	Broadcast Flag
BPS	Bits Per Symbol
BPSK	Binary Phase Shift Keying
CB	Cell Bridge
CE	Channel Estimation
CF	Control Frame
CFCS	Control Frame Check Sequence
CP	Cyclic Prefix
CRC	Cyclic Redundancy Check
CRD	Collision Recovery Duration
CSMA/CA	Carrier Sense Multiple Access/Collision Avoidance
CTS	Clear To Send
CWS	Contention Window Size
D8PSK	Differential 8-ary Phase Shift Keying
DBPSK	Differential Binary Phase Shift Keying
DES	Data Encryption Standard
DF	Data Frame
DFCS	Data Frame Check Sequence

DMT	Discrete Multi-Tone
DQPSK	Differential Quadrature Phase Shift Keying
DSID	Destination Station ID
DT	Delimiter Type
DV	DiVersity
DVF	DiVersity Flag
EDV	Extended DiVersity
FBB	Frame Body Block
FBBL	Frame Body Block Length
FBBP	Frame Body Block Payload
FBBPAD	Frame Body Block PADding
FBBSSID	Frame Body Block Source Station ID
FBBT	Frame Body Block Type
FBBTTL	Frame Body Block Time To Live
FBBV	Frame Body Block Version
FEC	Forward Error Correction
FFT	Fast Fourier Transform
FPV	Frame Protocol Version
GF	Galois Field
GID	Group ID
ID	IDentifier
IFFT	Inverse Fast Fourier Transform
IFS	InterFrame Space
ITR	Inverse TRaining
LCIFS	Long Contention InterFrame Space
LRIFS	Long Response InterFrame Space
LSB	Least Significant Bit
LSF	Last Segment Flag
MAC	Medium Access Control
MII	Media Independent Interface
MME	MAC Management Entity
MMI	MAC Management Information
MPDU	MAC Protocol Data Unit
MSB	Most Significant Bit
MSDU	MAC Service Data Unit
MTMI	My Tone Map Index

N/A	Not Applicable
NFBB	Number of Frame Body Block
NSB	Number of Symbol Block
NMS	Network Management System
PCS	Physical Carrier Sense
PHY	PHYsical (layer)
PLC	PowerLine Communication
PRS	Pseudo-Random Sequence
PSDU	PHY Service Data Unit
PSK	Phase Shift Keying
PTMI	Partner's Tone Map Index
PUNCI	PUNCturing Indicator
QoS	Quality of Service
QPSK	Quadrature Phase Shift Keying
RET	REsponse Type
RF	Response Flag
RS	Reed-Solomon
RT	Routing Table
RTS	Request To Send
SB	Service Block
SC	Segment Count
SCIFS	Short Contention InterFrame Space
SEG	SEGmentation
SID	Station ID
SN	Sequence Number
SNR	Signal-to-Noise Ratio
SRB	Slot Reservation Bit
SRIFS	Short Response InterFrame Space
SSID	Source Station ID
STA	STAtion
TM	Tone Map
TMI	Tone Map Index
TR	TRaining
TS	Training Sequence
TSD	Time Slot Duration
TSF	Training Sequence Flag

TSR	Training Sequence Request
UART	Universal Asynchronous Receiver Transmitter
VC	Version Control
VCS	Virtual Carrier Sense
VF	Variant Field
VLAN	Virtual Local Area Network

5 Reference Models

High speed PLC refers to interactive communication between more than two PLC devices in in-home or access network by using low and medium voltage powerline. Each PLC device can communicate with other PLC devices with the same group identifier as itself, and can communicate in various manners by forming a single logical network with them.

5.1 PLC Reference Model

Reference model of a high speed PLC system is constituted as in Fig. 1.

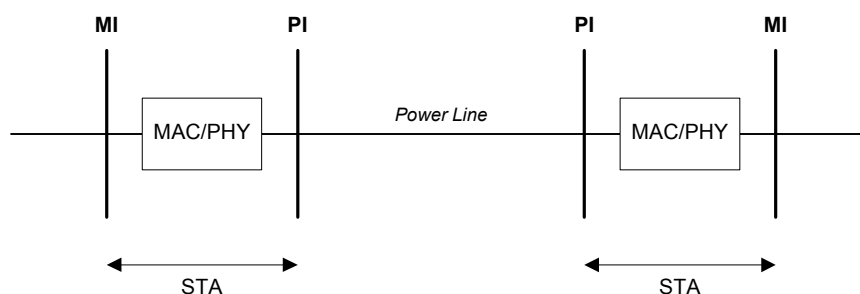


Fig. 1 - PLC Reference Model

PHY Interface (PI) is the physical interface between STA and the powerline. MAC Interface (MI) is the logical interface defining the relationship between the upper link layer and MAC layer of STA. The device including MAC and PHY, which are defined in this standard, is referred to as STA.

5.2 Interface Protocol Reference Model

Interface protocol reference model, illustrated in Fig. 2, is another expression of reference model and emphasizing the layer structure. The "upper layer" in this standard refers to the link layer above MI.

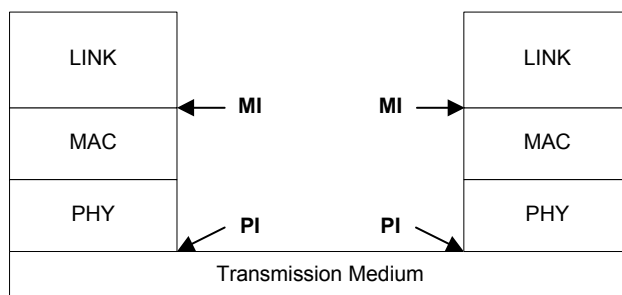


Fig. 2 - PLC Protocol Reference Model

5.3 PLC Network Topology

5.3.1 Home Networking

Fig. 3 illustrates the network topology in case of STA being applied to in-home for home networking only. STAs of each home shall form a cell (logical network) through GID. Each cell co-exists on the same physical network, but they shall be logically separated. Privacy shall be guaranteed by encoding data using different encryption keys. STAs of each cell render ad-hoc network and access network services possible through one-to-many communication.

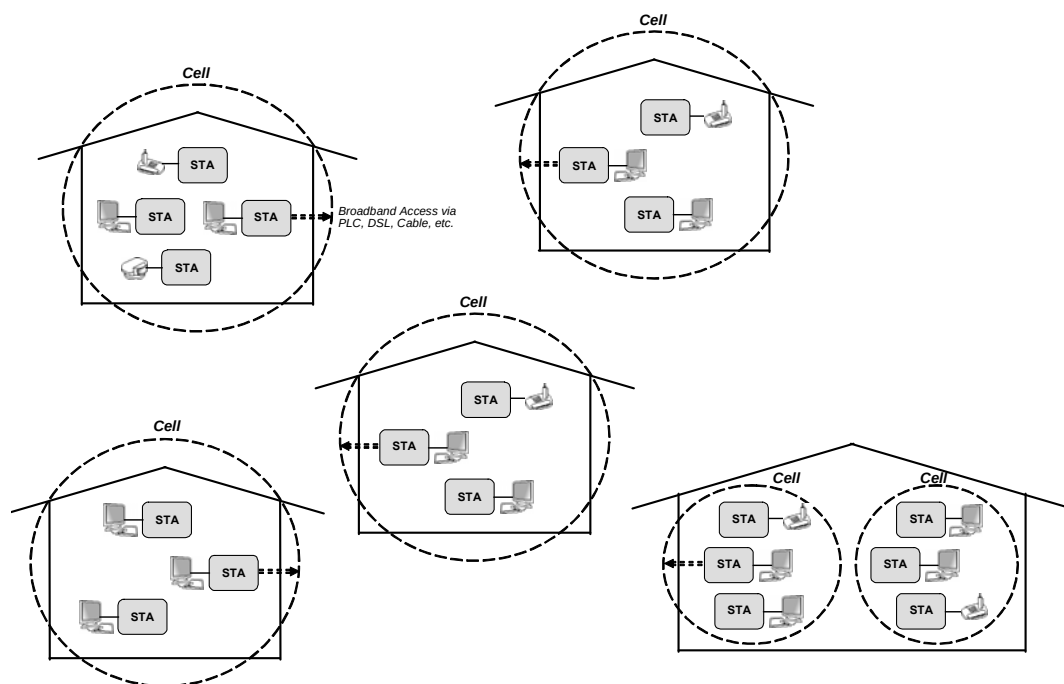


Fig. 3 - PLC Network Topology for Home Networking

5.3.2 Access Network

Fig. 4 illustrates the network topology in case of STA being applied to the access network. The entire PLC network is roughly divided into in-home network and access network.

STA functioning as the connection point with the backbone network in the access network is located on neighborhood transformer and thereby renders it possible for STAs existing in the same physical network to form a high speed access network.

Cell Bridge (CB), which is STA connecting two separate cells (two logical networks), makes it possible for the STAs at home to access a high speed access network. CB also functions as the repeater extending the communication coverage.

STAs composing in-home network can access a high speed access network through home networking and CB.

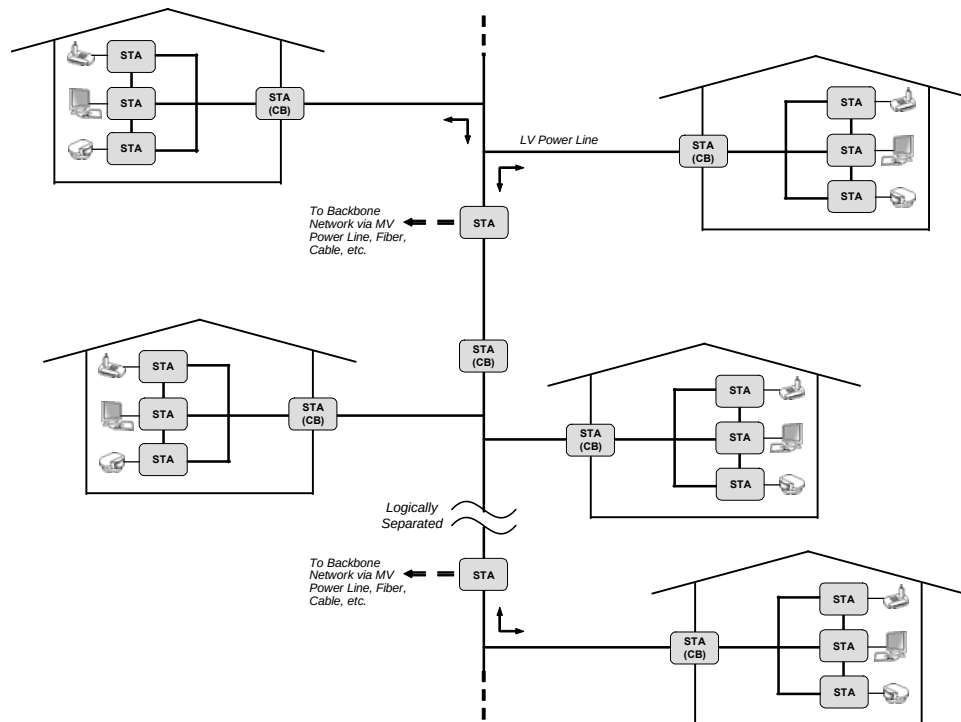


Fig. 4 - PLC Network Topology for Access Network

6 PHY Specification

The PHY specification presented in this chapter is for data network using high speed PLC.

6.1 Overview of PHY

The modulation-demodulation adopts DMT method. Table 1 shows the basic specification of PHY. Fig. 5 illustrates a DMT symbol to which cyclic prefix and pulse shaping are applied.

Table 1 - Specification of PHY

Item	Value
Bandwidth used	2.15 ~ 23.15 MHz
Forbidden band	*
Tone space (= 25MHz / 256)	97.65625 kHz
Sampling frequency	50 MHz
IFFT interval [T _{fft}]	512 sample
Cyclic prefix interval [T _{cp}]	128 sample

Rolloff interval [βT]	16 sample
Symbol interval [$T_s = T_{fft} + T_{cp} - \beta T$]	624 sample
Symbol rate	80.1282 kHz
Symbol period without CP	10.24 μ s
Symbol period with CP	12.48 μ s
Tone (or sub-channel) modulation	DBPSK, DQPSK, D8PSK
* This shall be subject to national regulations.	

DMT system presented in this standard shall adopt a bandwidth of 2.15~23.15MHz and each tone has a bandwidth of 97.65625kHz (theoretical) with the exception of the bandwidth designated as the guard band in accordance with each national regulation. The cyclic prefix of 128 samples shall be used in order to remove interference between DMT symbols in powerline channels. In addition, in order to reduce the probability of a packet error caused by the noise generated in the powerline channel, Forward Error Correction (FEC) comprising the convolutional codes and Reed-Solomon codes shall be used. For the modulation method used in each tone, DBPSK, DQPSK and D8PSK shall be adopted according to the channel condition.

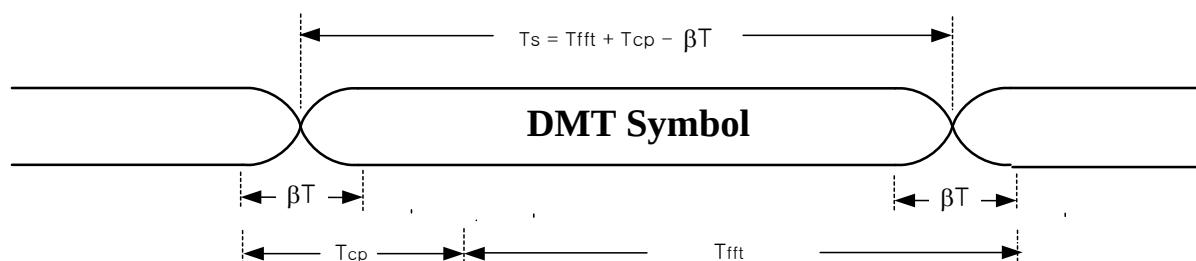


Fig. 5 - DMT Symbol

6.2 PSDU Format

The structure of PHY Service Data Unit (PSDU) used in this standard is shown in Fig. 6 and comprises delimiter and Data Frame (DF). The delimiter is composed of preamble and Control Frame (CF). DF may or may not exist according to the characteristics of the frame. The frame that has no DF is referred to as short PSDU and the frame that has DF is referred to as long PSDU. CF and DF are sub-frames existing in PSDU.

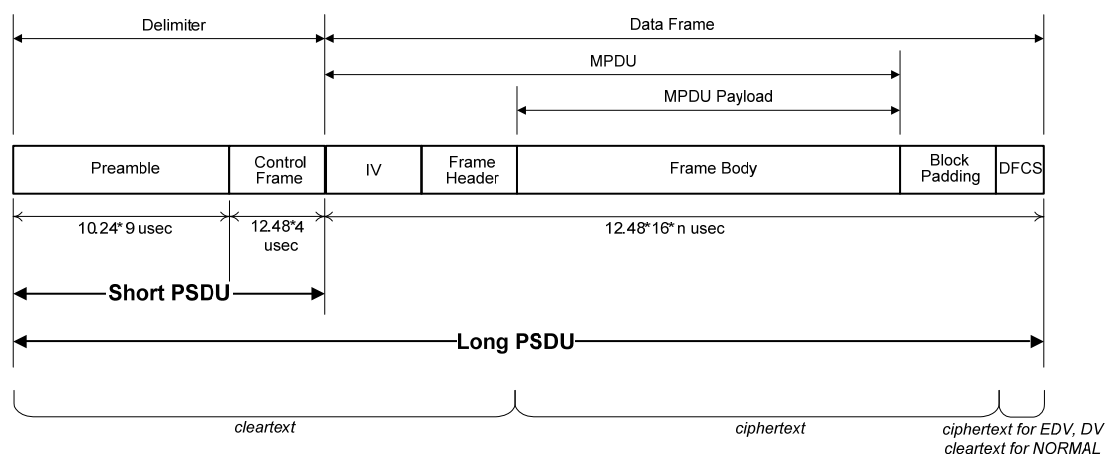


Fig. 6 - PSDU Format

6.2.1 Preamble

Preamble shall consist of 7 TRs followed by 2 ITRs without cyclic prefix. Using this, the reception end

performs Physical Carrier Sense (PCS), Automatic Gain Control (AGC) and synchronization. With the 256-tone of TR, the signals modulated into PSK shall be transmitted. The phase of each tone shall be determined as follows.

$$\begin{aligned}
 PRS[n] &= 1 & \text{for } 0 \leq n \leq 9 \\
 PRS[n] &= PRS[n-10] \oplus PRS[n-8] \oplus PRS[n-6] & \text{for } 10 \leq n \leq 1022 \\
 &\quad \oplus PRS[n-4] \oplus PRS[n-2] \oplus PRS[n-1] \\
 PRS[n] &= 1 & \text{for } n = 1023
 \end{aligned}$$

Generated 1024 Pseudo-Random Sequence (PRS)s shall be grouped by 4 bits and mapped into 1 tone. The phase of each tone shall have a value among $0, 1 \times (\pi/8), 2 \times (\pi/8), \dots, 15 \times (\pi/8)$ according to the 4-bit value of the tone. Table 2 shows the mapping relationship of constellation point with generated PRS and Table 3 shows the phase value of each tone.

Table 2 - Mapping Relationship of bit PRS and Constellation Point

[PRS(4n) PRS(4n+1) PRS(4n+2) PRS(4n+3)]	Tone Phase	[PRS(4n) PRS(4n+1) PRS(4n+2) PRS(4n+3)]	Tone Phase
0000	0	1000	$8 \times (\pi/8)$
0001	$1 \times (\pi/8)$	1001	$9 \times (\pi/8)$
0010	$2 \times (\pi/8)$	1010	$10 \times (\pi/8)$
0011	$3 \times (\pi/8)$	1011	$11 \times (\pi/8)$
0100	$4 \times (\pi/8)$	1100	$12 \times (\pi/8)$
0101	$5 \times (\pi/8)$	1101	$13 \times (\pi/8)$
0110	$6 \times (\pi/8)$	1110	$14 \times (\pi/8)$
0111	$7 \times (\pi/8)$	1111	$15 \times (\pi/8)$

Table 3 - Standard Phase Value of Each Tone

Tone No.	Phase Value (x $\pi/8$)	Tone No.	Phase Value (x $\pi/8$)	Tone No.	Phase Value (x $\pi/8$)	Tone No.	Phase Value (x $\pi/8$)	Tone No.	Phase Value (x $\pi/8$)	Tone No.	Phase Value (x $\pi/8$)	Tone No.	Phase Value (x $\pi/8$)	Tone No.	Phase Value (x $\pi/8$)
0	15	1	15	2	13	3	8	4	11	5	2	6	9	7	1
8	14	9	6	10	8	11	10	12	3	13	4	14	3	15	8
16	7	17	6	18	7	19	1	20	9	21	8	22	1	23	2
24	9	25	12	26	13	27	12	28	7	29	12	30	7	31	1
32	4	33	11	34	11	35	13	36	15	37	8	38	8	39	11
40	4	41	0	42	12	43	9	44	13	45	10	46	8	47	8
48	13	49	13	50	13	51	11	52	2	53	15	54	8	55	3
56	1	57	3	58	8	59	1	60	15	61	10	62	6	63	2
64	10	65	3	66	9	67	0	68	2	69	8	70	0	71	3
72	4	73	14	74	11	75	13	76	9	77	1	78	5	79	12
80	15	81	2	82	14	83	2	84	4	85	4	86	13	87	4
88	9	89	5	90	4	91	1	92	11	93	13	94	2	95	11
96	2	97	4	98	2	99	4	100	9	101	14	102	14	103	6
104	3	105	0	106	4	107	12	108	14	109	14	110	0	111	10
112	13	113	3	114	7	115	12	116	12	117	11	118	3	119	3
120	6	121	11	122	8	123	4	124	2	125	9	126	10	127	4
128	1	129	0	130	7	131	5	132	3	133	15	134	2	135	5

136	8	137	3	138	12	139	0	140	2	141	14	142	9	143	14
144	3	145	5	146	9	147	15	148	2	149	8	150	11	151	9
152	3	153	6	154	6	155	11	156	14	157	13	158	15	159	14
160	1	161	6	162	3	163	11	164	14	165	11	166	6	167	3
168	6	169	13	170	1	171	9	172	5	173	2	174	8	175	6
176	10	177	9	178	9	179	0	180	15	181	11	182	10	183	12
184	2	185	10	186	14	187	10	188	10	189	13	190	14	191	4
192	6	193	3	194	13	195	7	196	6	197	1	198	8	199	4
200	15	201	10	202	0	203	11	204	7	205	4	206	2	207	2
208	0	209	3	210	9	211	13	212	1	213	2	214	15	215	5
216	0	217	11	218	12	219	14	220	5	221	10	222	13	223	5
224	14	225	1	226	11	227	0	228	1	229	1	230	13	231	2
232	6	233	1	234	14	235	13	236	2	237	13	238	11	239	9
240	5	241	15	242	11	243	12	244	5	245	15	246	13	247	5
248	8	249	8	250	6	251	7	252	10	253	3	254	15	255	9

The forbidden band designated in each national regulation shall not be used. The corresponding tone of the forbidden band refers to Table 6.

ITR shall use the signal of which is inverted 180 degrees from that of TR. The first 16-sample and last 16-sample of the preamble shall be shaped by using the first 16-sample and next 16-sample of 32-tap window function respectively. The 32-tap window function shall be used as follows.

$$w[n] = \sin^2\left(\frac{\pi}{2}(0.5 + (n-8)/16)\right) \quad \text{for } 0 \leq n \leq 15$$

$$w[n] = w[31-n] \quad \text{for } 16 \leq n \leq 31$$

6.2.2 Control Frame (CF)

The control frame comprises 4 DMT symbols with a cyclic prefix and each DMT symbol shall carry out differential BPSK encoding on the basis of standard phase value of each tone in Table 3. Each DMT symbol shall carry out shaping by using the 32-tap window function (refer to 6.2.1).

The control frame shall use 124 tones (refer to 6.3.5) and the tone numbers are shown in Table 4.

Table 4 - Tone Numbers for Control Frame

Tone Sequence	Tone Number									
1 st ~ 10 th	47	48	49	50	51	52	53	54	55	56
11 th ~ 20 th	57	58	59	60	61	62	63	64	78	79
21 th ~ 30 th	80	81	82	83	95	96	97	98	99	107
31 th ~ 40 th	108	109	110	111	112	113	114	115	116	117
41 th ~ 50 th	118	119	120	121	122	123	124	125	126	127
51 th ~ 60 th	128	129	130	131	132	133	150	151	152	153
61 th ~ 70 th	154	155	156	157	158	159	160	161	162	163
71 th ~ 80 th	164	165	166	167	168	169	170	171	172	173
81 th ~ 90 th	174	175	176	177	178	179	180	189	190	191
91 th ~ 100 th	192	193	194	195	196	197	198	199	200	201
101 th ~ 110 th	202	203	204	205	206	207	208	209	210	211

111 th ~ 120 th	212	223	224	225	226	227	228	229	230	231
121 th ~ 124 th	232	233	234	235	-	-	-	-	-	-

The tones other than the 124 tones used in the control frame shall be transmitted after generating the same value as the standard phase value in Table 3 or a 180 degree inverted phase value.

6.2.3 Data Frame (DF)

A DMT symbol block is composed of 16 DMT symbols with a cyclic prefix, and up to 15 DMT symbol blocks can be transmitted within one data frame. For each DMT symbol within a data frame, differential PSK encoding shall be carried out in reference to the previous DMT symbol, and for each tone, DBPSK, DQPSK and D8PSK shall be applied according to the channel condition. For each DMT symbol, shaping shall be carried out using the 32-tap window function (refer to 6.2.1).

6.3 DMT Transmitter

Fig. 7 illustrates the block diagram of a DMT transmitter.

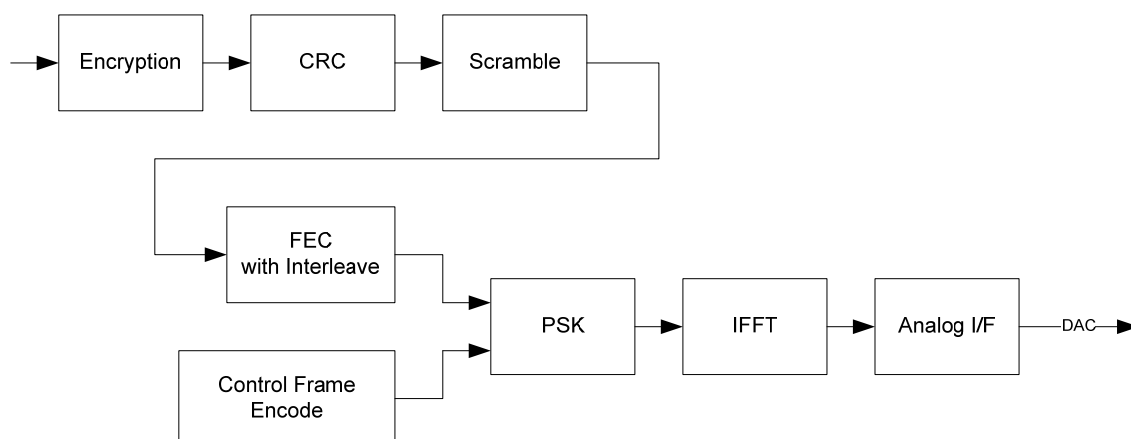


Fig. 7 - Block Diagram of a DMT Transmitter

6.3.1 Encryption

56-bit DES is used for encryption. Refer to Federal Information Processing Standards Publication 46-3 "Data Encryption Standard" for the specific implementation.

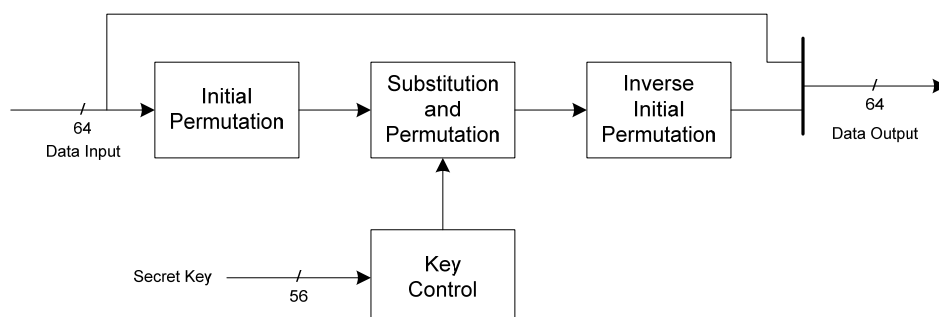


Fig. 8 - 56-Bit DES Block Diagram

128-bit AES is an optional encryption.

6.3.2 Cyclic Redundancy Check (CRC)

Data frame CRC encoder generates a 16-bit CRC of the frame header, the frame body, and the block

padding. It is generated through the following formula.

$$CRC_1(x) = x^k * (x^{15} + x^{14} + x^{13} + \dots + x^2 + x + 1) \bmod G(x)$$

$$CRC_2(x) = M(x) * x^{16} \bmod G(x)$$

CRC is obtained by taking the one's complement of the modulo-2 sum of CRC1 and CRC2. Every CRC register shall be first set to '1'.

The message polynomial is:

$$M(x) = m_{k-1}x^{k-1} + m_{k-2}x^{k-2} + \dots + m_1x + m_0$$

The generator polynomial is:

$$G(x) = x^{16} + x^{12} + x^5 + 1$$

The examination polynomial is:

$$DFCS(x) = c_{15}x^{15} + c_{14}x^{14} + c_{13}x^{13} + c_{12}x^{12} + \dots + c_3x^3 + c_2x^2 + c_1x + c_0$$

Every message shall be processed with MSB first. The total number of messages for CRC is expressed as k in the message polynomial. Here, m_{k-1} is MSB of the first byte of the entire message and m_0 is LSB of the last byte.

CRC shall be generated and transmitted at every frame. CRC calculation shall start after CRC is initialized at the first symbol of each frame. At the last symbol, the result shall be attached at the end and transmitted.

6.3.3 Scrambler

Of data bits, MSB shall be transmitted first and LSB later. The frame synchronous scrambler shall use the following generator polynomial $S(x)$.

$$S(x) = x^7 + x^4 + 1$$

When the transmission end carries out scrambling or when the reception end carries out inverse scrambling, the same scrambler shall be used. The scrambler shall be initialized as binary number '1' for each symbol block unit.

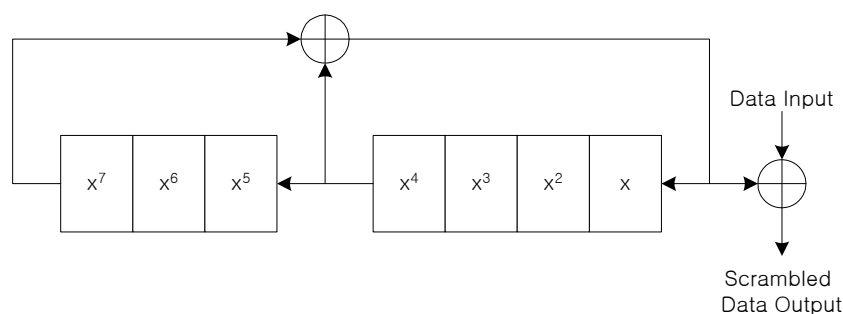


Fig. 9 - Scrambler

6.3.4 FEC and Interleaver

FEC and interleaver comprise Reed-Solomon coder, convolutional coder, puncturing and interleaver.

6.3.4.1 Reed-Solomon Encoding

The finite field of the Reed-Solomon codes shall be GF(256) and the generator polynomial shall be as follows. t of the following polynomial is the value expressing the ability to correct errors.

Code generator polynomial: $g(x) = \prod_{i=1}^n (x + \alpha^i)$, where $n = 2 * t$

Field generator polynomial: $p(x) = x^8 + x^4 + x^3 + x^2 + 1$ (435octal)

The actually used Reed-Solomon codes shall be the shortened codes of the original (255, 255-2*t) codes.

For more stable communication in a powerline channel environment with a lot of impulse noises, $t=8$ shall be used in NORMAL mode transmission.

6.3.4.2 Convolutional Encoding and Puncturing

The convolutional coder shall use 1/2 code rate and 7 constraint length (K). Each tap connection shall be A=0b1111001 and B=0b1011011 as shown in Fig. 10.

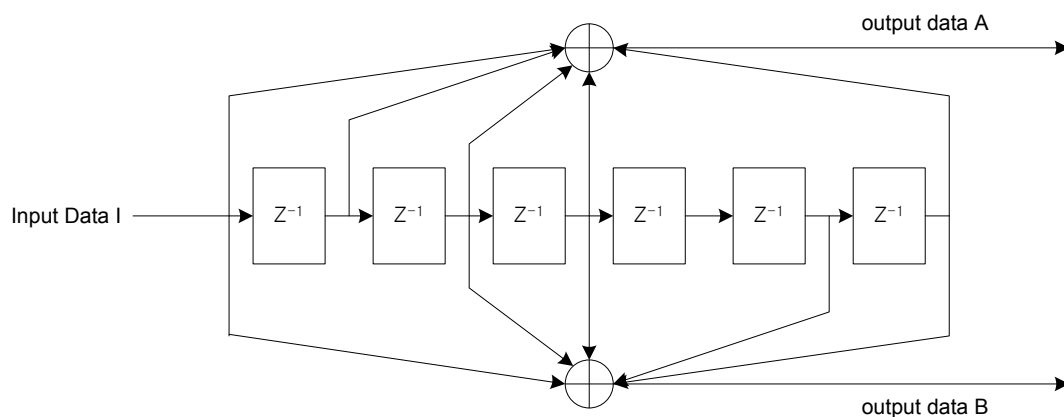


Fig. 10 - Convolutional Coder (code rate = 1/2, K=7)

The processing shall be carried out by each symbol block unit and if a symbol block is encoded, 6 tail bits (tail bit: all binary number '0') shall be added and then the symbol block is transmitted. During this process, all the flip-flops within the coder shall be initialized as binary number '0'.

The code rate of a convolutional coder is 1/2 and puncturing is used to adjust it to 3/4. With respect to puncturing, refer to Fig. 11.

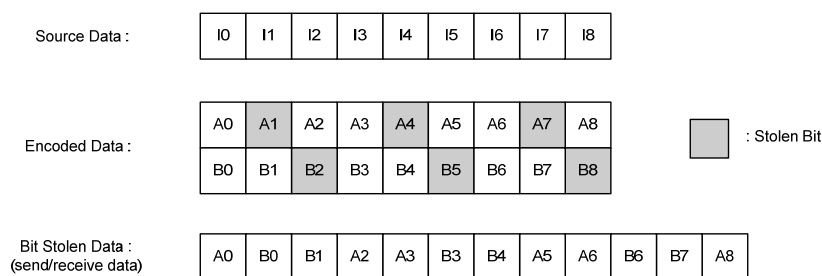


Fig. 11 - Puncturing Codes (code rate = 3/4)

6.3.4.3 Interleaver

Interleaving takes place in the form of a block interleaver, and shall vary as follows according to the size of the symbol block and the number of bits per symbol. If the size of the symbol block is $N_{SB}(=16)$ and the number of bits per symbol is N_{BPS} , interleaver bit output $D_{OUT}(k)$ shall have the following relationship with interleaver bit input $D_{IN}(k)$. Interleaver is shown in Fig. 12. $X(k)$ is the data before offset parameter N_O is applied, and the value N_O is 8.

$$X(k) = D_{IN}(N_C \times k - (N_C \times N_R - 1) \times \text{floor}(\frac{k}{N_R}))$$

$$D_{OUT}(k) = X(\text{mod}((k + N_O \times \text{floor}(\frac{k}{N_R})), N_R) + N_R \times \text{floor}(\frac{k}{N_R}))$$

where,

$$k = 0, 1, 2, \dots, N_{SB} \times N_{BPS} - 1$$

$$N_C = N_{SB} \text{ (when code rate is 1/2), or } 2 \times N_{SB} \text{ (when code rate is 3/4)}$$

$$N_R = N_{BPS} \text{ (when code rate is 1/2), or } N_{BPS} / 2 \text{ (when code rate is 3/4)}$$

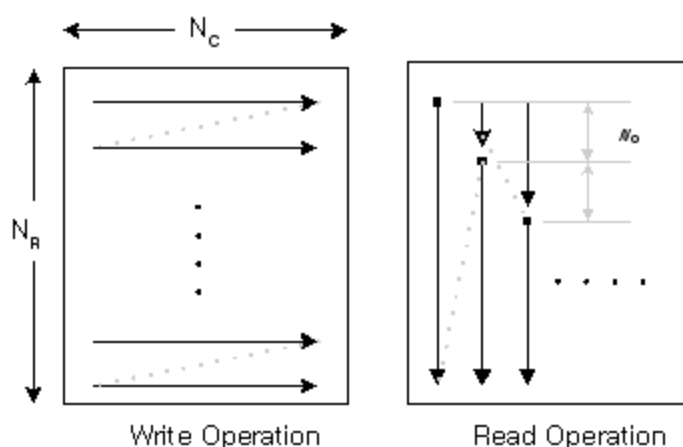


Fig. 12 - Reading/Writing Operation of Data Interleaver

6.3.5 Control Frame Encoding

Encoding of the control frame uses Reed-Solomon code and diversity mapping, and is constituted as Fig. 13.