



INTERNATIONAL STANDARD ISO/IEC 14496-4:2004/Amd.1:2005
TECHNICAL CORRIGENDUM 1

Published 2005-09-01

INTERNATIONAL ORGANIZATION FOR STANDARDIZATION • МЕЖДУНАРОДНАЯ ОРГАНИЗАЦИЯ ПО СТАНДАРТИЗАЦИИ • ORGANISATION INTERNATIONALE DE NORMALISATION
INTERNATIONAL ELECTROTECHNICAL COMMISSION • МЕЖДУНАРОДНАЯ ЭЛЕКТРОТЕХНИЧЕСКАЯ КОМИССИЯ • COMMISSION ÉLECTROTECHNIQUE INTERNATIONALE

Information technology — Coding of audio-visual objects —
Part 4:
Conformance testing

AMENDMENT 1: Conformance testing for MPEG-4

TECHNICAL CORRIGENDUM 1

Technologies de l'information — Codage des objets audiovisuels —

Partie 4: Essai de conformité

AMENDEMENT 1: Essai de conformité pour MPEG-4

RECTIFICATIF TECHNIQUE 1

Technical Corrigendum 1 to ISO/IEC 14496-4:2004/Amd.1:2005 was prepared by Joint Technical Committee ISO/IEC JTC 1, *Information technology*, Subcommittee SC 29, *Coding of audio, picture, multimedia and hypermedia information*.

In subclause 5.8.2, replace the following table for the missing or invalid conformance bitstreams:

Categories	Bitstream	Donated by	Bitstream Name	Profile Name												
				Simple	Advanced Simple					FGS						
				L0	L0	L1	L2	L3	L4	L5	L0	L1	L2	L3	L4	L5
General	GE-3	GI	vcon-ge3.cmp						S							
	GE-13	Siemens	vcon-ge13-L0.bits	S												
	GE-13	Mitsubishi	vcon-ge13-L0.bits		S											
	GE-13	Mitsubishi	vcon-ge13-L1.bits			S										
	GE-13	Mitsubishi	vcon-ge13-L2.bits				S									
	GE-13	Mitsubishi	vcon-ge13-L3.bits					S								
	GE-13	Mitsubishi	vcon-ge13-L4.bits						S							
	GE-13	Mitsubishi	vcon-ge13-L5.bits							S						
	GE-16	Siemens	vcon-ge16-L0.bits	S												
	GE-16	Mitsubishi	vcon-ge16-L0.bits		S											
	GE-16	Mitsubishi	vcon-ge16-L1.bits			S										
	GE-16	Mitsubishi	vcon-ge16-L2.bits				S									
	GE-16	Mitsubishi	vcon-ge16-L3.bits					S								
	GE-16	Mitsubishi	vcon-ge16-L4.bits						S							
	GE-16	Mitsubishi	vcon-ge16-L5.bits							S						
	GE-18	GI	vcon-ge18.cmp					S								
	GE-24	GI	vcon-ge24.cmp					S								
	GE-25	GI	vcon-ge25.cmp					S								
	er-2-1	Siemens	Vcon-er2-1.cmp	S												
	er-2-1	Toshiba	Vcon-er2-1.cmp		S											
	er-3-1	Siemens	Vcon-er3-1.cmp	S												
	er-3-1	Toshiba	Vcon-er3-1.cmp		S											
	A1GE-1	Bosch	vcon_a1ge_1_ace_l4.bits						X							
	A1GE-1	Bosch	vcon_a1ge_1_ace_l5.bits							X						
	A1GE-2	Univ. Hann.	a1ge-2_ace_l4.bits						X							
	A1GE-2	Univ. Hann.	a1ge-2_ace_l5.bits							X						
	A1GE-3	Hitachi	vcon-asp3L4.bits						X							
	A1GE-3	Hitachi	vcon-asp3L5.bits							X						
	A1GE-4	Univ. Hann.	a1ge-4_ace.bits		S											
	A1GE-5	Hitachi	vcon-asp5L0.bits		S											
	A1GE-6	Hitachi	vcon-asp6L0.bits		S											
	A1GE-7	Hitachi	vcon-asp7L0.bits		S											
	A1GE-8	Hitachi	vcon-asp8L0.bits		S											
	A1GE-9	Univ. Hann.	a1ge-9_ace_l0.bits		X											
	A1GE-9	Univ. Hann.	a1ge-9_ace_l1.bits			X										
	A1GE-9	Univ. Hann.	a1ge-9_ace_l2.bits				X									
	A1GE-9	Univ. Hann.	a1ge-9_ace_l3.bits					X								
	A1GE-9	Univ. Hann.	a1ge-9_ace_l4.bits						X							
	A1GE-9	Univ. Hann.	a1ge-9_ace_l5.bits							X						
	A1GE-10	Bosch	vcon_a1ge_10_ace_l0.bits		X											
	A1GE-10	Bosch	vcon_a1ge_10_ace_l1.bits			X										
	A1GE-10	Bosch	vcon_a1ge_10_ace_l2.bits				X									
	A1GE-10	Bosch	vcon_a1ge_10_ace_l3.bits					X								