



INTERNATIONAL STANDARD ISO/IEC 14496-4:2004 TECHNICAL CORRIGENDUM 1

Published 2005-09-01

INTERNATIONAL ORGANIZATION FOR STANDARDIZATION • МЕЖДУНАРОДНАЯ ОРГАНИЗАЦИЯ ПО СТАНДАРТИЗАЦИИ • ORGANISATION INTERNATIONALE DE NORMALISATION
INTERNATIONAL ELECTROTECHNICAL COMMISSION • МЕЖДУНАРОДНАЯ ЭЛЕКТРОТЕХНИЧЕСКАЯ КОМИССИЯ • COMMISSION ÉLECTROTECHNIQUE INTERNATIONALE

Information technology — Coding of audio-visual objects — Part 4: Conformance testing

TECHNICAL CORRIGENDUM 1

Technologies de l'information — Codage des objets audiovisuels —

Partie 4: Essai de conformité

RECTIFICATIF TECHNIQUE 1

Technical Corrigendum 1 to ISO/IEC 14496-4:2004 was prepared by Joint Technical Committee ISO/IEC JTC 1, *Information technology*, Subcommittee SC 29, *Coding of audio, picture, multimedia and hypermedia information*.

In Table 10, subclause 5.5.7, replace the following:

	SH-7-1	Toshiba	vcon-sh7-1.cmp							S							
	SH-7-2	Toshiba	vcon-sh7-2.cmp							S							
	SH-8-1	Toshiba	vcon-sh8-1.cmp							S							
	SH-8-2	Toshiba	vcon-sh8-2.cmp							S							

with:

	SH-7-1	Toshiba	vcon-sh7-1_reva.cmp							S							
	SH-7-2	Toshiba	vcon-sh7-2_reva.cmp							S							
	SH-8-1	Toshiba	vcon-sh8-1_reva.cmp							S							
	SH-8-2	Toshiba	vcon-sh8-2_reva.cmp							S							

ICS 35.040

Ref. No. ISO/IEC 14496-4:2004/Cor.1:2005(E)

© ISO/IEC 2005 – All rights reserved

Published in Switzerland

AVA Documents
Document Sharing Hub

In Table 10, replace:

Scalability	SCS-1	Sony	vcon-scs1.bits	S	S	S											
	SCS-1_e	Sony	vcon-scs1_e.bits				S	S									
	SCS-2	Sony	vcon-scs2.bits	S	S	S											
	SCS-2_e	Sony	vcon-scs2_e.bits				S	S									
	SCS-3	Sony	vcon-scs3.bits	S	S	S											
	SCS-3_e	Sony	vcon-scs3_e.bits				S	S									
	SCS-8	Sony	vcon-scs8.bits	D													
	SCS-8_e	Sony	vcon-scs8_e.bits				D										
	SCS-9	Sony	vcon-scs9.bits		D												
	SCS-9_e	Sony	vcon-scs9_e.bits					D									

with:

Scalability	SCS-1	Sony	vcon-scs1_reva.bits	S	S	S											
	SCS-1_e	Sony	vcon-scs1_e_reva.bits				S	S									
	SCS-2	Sony	vcon-scs2_reva.bits	S	S	S											
	SCS-2_e	Sony	vcon-scs2_e_reva.bits				S	S									
	SCS-3	Sony	vcon-scs3_reva.bits	S	S	S											
	SCS-3_e	Sony	vcon-scs3_e_reva.bits				S	S									
	SCS-8	Sony	vcon-scs8_reva.bits	D													
	SCS-8_e	Sony	vcon-scs8_e_reva.bits				D										
	SCS-9	Sony	vcon-scs9_reva.bits		D												
	SCS-9_e	Sony	vcon-scs9_e_reva.bits					D									

In Table 10, replace:

Error Resilience	er-1	Toshiba	Vcon-er1.cmp			S											
	er-2-1	Toshiba	Vcon-er2-1.cmp	S													
	er-2-2	Toshiba	Vcon-er2-2.cmp		S												
	er-2-3	Toshiba	Vcon-er2-3.cmp			S											
	er-3-1	Toshiba	Vcon-er3-1.cmp	S													
	er-3-2	Toshiba	Vcon-er3-2.cmp		S												
	er-3-3	Toshiba	Vcon-er3-3.cmp			S											

with:

Error Resilience	er-1	Toshiba	Vcon-er1_reva.cmp			S											
	er-2-1	Toshiba	Vcon-er2-1_reva.cmp	S													
	er-2-2	Toshiba	Vcon-er2-2_reva.cmp		S												
	er-2-3	Toshiba	Vcon-er2-3_reva.cmp			S											
	er-3-1	Toshiba	Vcon-er3-1_reva.cmp	S													
	er-3-2	Toshiba	Vcon-er3-2_reva.cmp		S												
	er-3-3	Toshiba	Vcon-er3-3_reva.cmp			S											

In Table 12, subclause 5.5.8.1, replace:

mit007.m4v	Simple@L1	Talk	10.000	64	176	144	150	basic
------------	-----------	------	--------	----	-----	-----	-----	-------

with:

mit007_reva.m4v	Simple@L1	Talk	10.000	64	176	144	150	basic
-----------------	-----------	------	--------	----	-----	-----	-----	-------

In Table 14, subclause 5.5.8.1, replace:

Scalable Still Texture	ss-1	Sharp	vcon-ss1.bits									S	S	S		S	S	
	ss-2	Sharp	vcon-ss2.bits									S	S	S		S	S	
	ss-3	Sharp	vcon-ss3.bits									S	S	S		S	S	
	ss-4	Sharp	vcon-ss4.bits									S	S	S		S	S	
	ss-5	Sharp	vcon-ss5.bits									S	S	S		S	S	
	ss-6	Sharp	vcon-ss6.bits									S	S	S		S	S	
	ss-7	Sharp	vcon-ss7.bits									S	S	S		S	S	
	ss-8	Sarnoff	vcon-ss8.bits									S	S	S		S	S	
	ss-9	Sarnoff	vcon-ss9.bits									S	S	S		S	S	
	ss-10	Sarnoff	vcon-ss10.bits									S	S	S		S	S	
	ss-11	Sarnoff	vcon-ss11.bits									S	S	S		S	S	
	ss-12	TI	vcon-ss12.bits									S	S	S		S	S	
	ss-13	TI	vcon-ss13.bits									S	S	S		S	S	

with:

Scalable Still Texture	ss-1	Sharp	v1_vcon-ss1.bits									S	S	S		S	S	
	ss-2	Sharp	v1_vcon-ss2.bits									S	S	S		S	S	
	ss-3	Sharp	v1_vcon-ss3.bits									S	S	S		S	S	
	ss-4	Sharp	v1_vcon-ss4.bits									S	S	S		S	S	
	ss-5	Sharp	v1_vcon-ss5.bits									S	S	S		S	S	
	ss-6	Sharp	v1_vcon-ss6.bits									S	S	S		S	S	
	ss-7	Sharp	v1_vcon-ss7.bits									S	S	S		S	S	
	ss-8	Sarnoff	v1_vcon-ss8.bits									S	S	S		S	S	
	ss-9	Sarnoff	v1_vcon-ss9.bits									S	S	S		S	S	
	ss-10	Sarnoff	v1_vcon-ss10.bits									S	S	S		S	S	
	ss-11	Sarnoff	v1_vcon-ss11.bits									S	S	S		S	S	
	ss-12	TI	v1_vcon-ss12.bits									S	S	S		S	S	
	ss-13	TI	v1_vcon-ss13.bits									S	S	S		S	S	

In Table 14, replace:

San021.m4v	Simple@L2	Aki1	10.000	128	352	288	99	basic
San022.m4v	Simple@L1	Aki1	10.000	64	176	144	100	VBV(L1)
San023.m4v	Simple@L2	Aki1	10.000	128	352	288	49	VBV(L2)
San024.m4v	Simple@L3	Aki1	10.000	384	352	288	127	VBV(L3)

with:

San021_reva.m4v	Simple@L2	Aki1	10.000	128	352	288	99	basic
San022_reva.m4v	Simple@L1	Aki1	10.000	64	176	144	100	VBV(L1)
San023_reva.m4v	Simple@L2	Aki1	10.000	128	352	288	97	VBV(L2)
San024_reva.m4v	Simple@L3	Aki1	10.000	384	352	288	239	VBV(L3)

In Table 16, subclause 5.5.8.2, replace:

mat000.m4v	Core@L1	own synthetic	66.600	116	16	16	999	IVOP IDCT bitstream1
mat001.m4v	Simple@L1	own synthetic	66.600	30	16	16	999	IVOP IDCT bitstream2

with:

mat000_reva.m4v	Core@L1	own synthetic	66.600	116	16	16	998	IVOP IDCT bitstream1
mat001_reva.m4v	Simple@L1	own synthetic	66.600	30	16	16	998	IVOP IDCT bitstream2

In Table 19, replace:

mat045.m4v	Simple@L1	own synthetic	0.118	64	16	16	2	AC/DC Saturation
------------	-----------	---------------	-------	----	----	----	---	------------------

with:

mat045_reva.m4v	Simple@L1	own synthetic	0.118	64	16	16	2	AC/DC Saturation
-----------------	-----------	---------------	-------	----	----	----	---	------------------

In Table 20, replace:

Pio002.m4v	Core@L1	Friends	10.000	384	176	144	300	Video Packet + Variable Q
Pio003.m4v	Core@L1	Drive	10.000	384	176	144	300	Data partitioning + Variable Q
Pio004.m4v	Core@L1	Octopus	10.000	384	176	144	300	RVLC + Variable Q

with:

Pio002_reva.m4v	Core@L1	friends	10.000	384	176	144	300	Video Packet + Variable Q
Pio003_reva.m4v	Core@L1	drive	10.000	384	176	144	300	Data partitioning + Variable Q
Pio004_reva.m4v	Core@L1	octopus	10.000	384	176	144	300	RVLC + Variable Q

In Table 22, replace:

Pio005.m4v	Core@L1	aki2	10.000	192	176	144	300	Bianry Shape (Variable Q + intra dc vlc thr)
------------	---------	------	--------	-----	-----	-----	-----	--

with:

Pio005.m4v	Core@L1	aki2	10.000	192	176	144	300	Binary Shape (Variable Q + intra_dc_vlc_thr)
------------	---------	------	--------	-----	-----	-----	-----	--

In Table 22, replace:

pio006.m4v	Core@L1	bike	10.000	384	176	144	300	Binary Shape (Video Packet + Variable Q)
pio007.m4v	Core@L1	goldfish	10.000	384	176	144	300	Binary Shape (Data partitioning + Variable Q)
pio008.m4v	Core@L1	goldfish	10.000	384	176	144	300	Binary Shape (RVLC + Variable Q)

with:

pio006_reva.m4v	Core@L1	bike	10.000	384	176	144	300	Binary Shape (Video Packet + Variable Q)
pio007_reva.m4v	Core@L1	goldfish	10.000	384	176	144	300	Binary Shape (Data partitioning + Variable Q)
pio008_reva.m4v	Core@L1	goldfish	10.000	384	176	144	300	Binary Shape (RVLC + Variable Q)

In Table 23, replace:

nec008.m4v	Core@L1	drive	10.000	384	176	144	299	Short Header
------------	---------	-------	--------	-----	-----	-----	-----	--------------

with:

nec008_reva.m4v	Core@L1	drive	10.000	384	176	144	300	Short Header
-----------------	---------	-------	--------	-----	-----	-----	-----	--------------

In Table 27, subclause 5.6.2, replace:

	GE-3	UH	ge-3_ace_l1.bits												X										
		UH	ge-3_ace_l2.bits													X									
		UH	ge-3_ace_l3.bits														X								
		UH	ge-3_ace_l4.bits															X							

with:

[illegible]