

INTERNATIONAL  
STANDARD

ISO/IEC/  
IEEE  
8802-1AS

Second edition  
2021-11

---

---

**Information technology —  
Telecommunications and information  
exchange between systems — Local  
and metropolitan area networks —**

**Part 1AS:**

**Timing and synchronization for time-  
sensitive applications in bridged local  
area networks**

*Technologies de l'information — Télécommunications et échange  
d'information entre systèmes — Réseaux locaux et métropolitains —*

*Partie 1AS: Temporisation et synchronisation pour les applications  
sensibles au temps des réseaux locaux pontés*



Reference number  
ISO/IEC/IEEE 8802-1AS:2021(E)  
**Documents**  
Document Sharing Hub  
© IEEE 2020



**COPYRIGHT PROTECTED DOCUMENT**

© IEEE 2020

All rights reserved. Unless otherwise specified, or required in the context of its implementation, no part of this publication may be reproduced or utilized otherwise in any form or by any means, electronic or mechanical, including photocopying, or posting on the internet or an intranet, without prior written permission. Permission can be requested from IEEE at the address below.

Institute of Electrical and Electronics Engineers, Inc  
3 Park Avenue, New York  
NY 10016-5997, USA

Email: [stds.ipr@ieee.org](mailto:stds.ipr@ieee.org)  
Website: [www.ieee.org](http://www.ieee.org)

Published in Switzerland

## Foreword

ISO (the International Organization for Standardization) and IEC (the International Electrotechnical Commission) form the specialized system for worldwide standardization. National bodies that are members of ISO or IEC participate in the development of International Standards through technical committees established by the respective organization to deal with particular fields of technical activity. ISO and IEC technical committees collaborate in fields of mutual interest. Other international organizations, governmental and non-governmental, in liaison with ISO and IEC, also take part in the work.

The procedures used to develop this document and those intended for its further maintenance are described in the ISO/IEC Directives, Part 1. In particular, the different approval criteria needed for the different types of ISO/IEC documents should be noted (see [www.iso.org/directives](http://www.iso.org/directives) or [www.iec.ch/members\\_experts/refdocs](http://www.iec.ch/members_experts/refdocs)).

IEEE Standards documents are developed within the IEEE Societies and the Standards Coordinating Committees of the IEEE Standards Association (IEEE-SA) Standards Board. The IEEE develops its standards through a consensus development process, approved by the American National Standards Institute, which brings together volunteers representing varied viewpoints and interests to achieve the final product. Volunteers are not necessarily members of the Institute and serve without compensation. While the IEEE administers the process and establishes rules to promote fairness in the consensus development process, the IEEE does not independently evaluate, test, or verify the accuracy of any of the information contained in its standards.

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. ISO and IEC shall not be held responsible for identifying any or all such patent rights. Details of any patent rights identified during the development of the document will be in the Introduction and/or on the ISO list of patent declarations received (see [www.iso.org/patents](http://www.iso.org/patents)) or the IEC list of patent declarations received (see [patents.iec.ch](http://patents.iec.ch)).

Any trade name used in this document is information given for the convenience of users and does not constitute an endorsement.

For an explanation of the voluntary nature of standards, the meaning of ISO specific terms and expressions related to conformity assessment, as well as information about ISO's adherence to the World Trade Organization (WTO) principles in the Technical Barriers to Trade (TBT), see [www.iso.org/iso/foreword.html](http://www.iso.org/iso/foreword.html). In the IEC, see [www.iec.ch/understanding-standards](http://www.iec.ch/understanding-standards).

ISO/IEC/IEEE 8802-1AS was prepared by the LAN/MAN of the IEEE Computer Society (as IEEE Std 802.1AS-2020) and drafted in accordance with its editorial rules. It was adopted, under the “fast-track procedure” defined in the Partner Standards Development Organization cooperation agreement between ISO and IEEE, by Joint Technical Committee ISO/IEC JTC 1, *Information technology*, Subcommittee SC 6, *Telecommunications and information exchange between systems*.

A list of all parts in the ISO/IEC/IEEE 8802 series can be found on the ISO and IEC websites.

Any feedback or questions on this document should be directed to the user's national standards body. A complete listing of these bodies can be found at [www.iso.org/members.html](http://www.iso.org/members.html) and [www.iec.ch/national-committees](http://www.iec.ch/national-committees).

**Abstract:** Protocols, procedures, and managed objects for the transport of timing over local area networks are defined in this standard. It includes the transport of synchronized time, the selection of the timing source (i.e., best master), and the indication of the occurrence and magnitude of timing impairments (i.e., phase and frequency discontinuities).

**Keywords:** best master, frequency offset, Grandmaster Clock, Grandmaster PTP Instance, PTP End Instance, PTP Relay Instance, IEEE 802.1AS™, phase offset, synchronization, syntonization, time-aware system

---

The Institute of Electrical and Electronics Engineers, Inc.  
3 Park Avenue, New York, NY 10016-5997, USA

Copyright © 2020 by The Institute of Electrical and Electronics Engineers, Inc.  
All rights reserved. Published 19 June 2020. Printed in the United States of America.

MoCA is a registered trademark of the Multimedia over Coax Alliance.

POSIX is a registered trademark of The Institute of Electrical and Electronics Engineers, Incorporated.

IEEE and IEEE 802 are registered trademarks in the U.S. Patent & Trademark Office, owned by The Institute of Electrical and Electronics Engineers, Incorporated.

|        |                        |            |
|--------|------------------------|------------|
| PDF:   | ISBN 978-1-5044-6430-7 | STD24046   |
| Print: | ISBN 978-1-5044-6431-4 | STDPD24046 |

*IEEE prohibits discrimination, harassment and bullying.*

*For more information, visit <https://www.ieee.org/about/corporate/governance/p9-26.html>.*

*No part of this publication may be reproduced in any form, in an electronic retrieval system or otherwise, without the prior written permission of the publisher.*

## Important Notices and Disclaimers Concerning IEEE Standards Documents

IEEE documents are made available for use subject to important notices and legal disclaimers. These notices and disclaimers, or a reference to this page, appear in all standards and may be found under the heading “Important Notices and Disclaimers Concerning IEEE Standards Documents.” They can also be obtained on request from IEEE or viewed at <https://standards.ieee.org/ipr/disclaimers.html>.

## Notice and Disclaimer of Liability Concerning the Use of IEEE Standards Documents

IEEE Standards documents (standards, recommended practices, and guides), both full-use and trial-use, are developed within IEEE Societies and the Standards Coordinating Committees of the IEEE Standards Association (“IEEE SA”) Standards Board. IEEE (“the Institute”) develops its standards through a consensus development process, approved by the American National Standards Institute (“ANSI”), which brings together volunteers representing varied viewpoints and interests to achieve the final product. IEEE Standards are documents developed through scientific, academic, and industry-based technical working groups. Volunteers in IEEE working groups are not necessarily members of the Institute and participate without compensation from IEEE. While IEEE administers the process and establishes rules to promote fairness in the consensus development process, IEEE does not independently evaluate, test, or verify the accuracy of any of the information or the soundness of any judgments contained in its standards.

IEEE Standards do not guarantee or ensure safety, security, health, or environmental protection, or ensure against interference with or from other devices or networks. Implementers and users of IEEE Standards documents are responsible for determining and complying with all appropriate safety, security, environmental, health, and interference protection practices and all applicable laws and regulations.

IEEE does not warrant or represent the accuracy or content of the material contained in its standards, and expressly disclaims all warranties (express, implied and statutory) not included in this or any other document relating to the standard, including, but not limited to, the warranties of: merchantability; fitness for a particular purpose; non-infringement; and quality, accuracy, effectiveness, currency, or completeness of material. In addition, IEEE disclaims any and all conditions relating to: results; and workmanlike effort. IEEE standards documents are supplied “AS IS” and “WITH ALL FAULTS.”

Use of an IEEE standard is wholly voluntary. The existence of an IEEE standard does not imply that there are no other ways to produce, test, measure, purchase, market, or provide other goods and services related to the scope of the IEEE standard. Furthermore, the viewpoint expressed at the time a standard is approved and issued is subject to change brought about through developments in the state of the art and comments received from users of the standard.

In publishing and making its standards available, IEEE is not suggesting or rendering professional or other services for, or on behalf of, any person or entity nor is IEEE undertaking to perform any duty owed by any other person or entity to another. Any person utilizing any IEEE Standards document, should rely upon his or her own independent judgment in the exercise of reasonable care in any given circumstances or, as appropriate, seek the advice of a competent professional in determining the appropriateness of a given IEEE standard.

IN NO EVENT SHALL IEEE BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO: PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE PUBLICATION, USE OF, OR RELIANCE UPON ANY STANDARD, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE AND REGARDLESS OF WHETHER SUCH DAMAGE WAS FORESEEABLE.

## Translations

The IEEE consensus development process involves the review of documents in English only. In the event that an IEEE standard is translated, only the English version published by IEEE should be considered the approved IEEE standard.

## Official statements

A statement, written or oral, that is not processed in accordance with the IEEE SA Standards Board Operations Manual shall not be considered or inferred to be the official position of IEEE or any of its committees and shall not be considered to be, or be relied upon as, a formal position of IEEE. At lectures, symposia, seminars, or educational courses, an individual presenting information on IEEE standards shall make it clear that his or her views should be considered the personal views of that individual rather than the formal position of IEEE.

## Comments on standards

Comments for revision of IEEE Standards documents are welcome from any interested party, regardless of membership affiliation with IEEE. However, IEEE does not provide consulting information or advice pertaining to IEEE Standards documents. Suggestions for changes in documents should be in the form of a proposed change of text, together with appropriate supporting comments. Since IEEE standards represent a consensus of concerned interests, it is important that any responses to comments and questions also receive the concurrence of a balance of interests. For this reason, IEEE and the members of its societies and Standards Coordinating Committees are not able to provide an instant response to comments or questions except in those cases where the matter has previously been addressed. For the same reason, IEEE does not respond to interpretation requests. Any person who would like to participate in revisions to an IEEE standard is welcome to join the relevant IEEE working group.

Comments on standards should be submitted to the following address:

Secretary, IEEE SA Standards Board  
445 Hoes Lane  
Piscataway, NJ 08854 USA

## Laws and regulations

Users of IEEE Standards documents should consult all applicable laws and regulations. Compliance with the provisions of any IEEE Standards document does not imply compliance to any applicable regulatory requirements. Implementers of the standard are responsible for observing or referring to the applicable regulatory requirements. IEEE does not, by the publication of its standards, intend to urge action that is not in compliance with applicable laws, and these documents may not be construed as doing so.

## Copyrights

IEEE draft and approved standards are copyrighted by IEEE under US and international copyright laws. They are made available by IEEE and are adopted for a wide variety of both public and private uses. These include both use, by reference, in laws and regulations, and use in private self-regulation, standardization, and the promotion of engineering practices and methods. By making these documents available for use and adoption by public authorities and private users, IEEE does not waive any rights in copyright to the documents.

## Photocopies

Subject to payment of the appropriate fee, IEEE will grant users a limited, non-exclusive license to photocopy portions of any individual standard for company or organizational internal use or individual, non-commercial use only. To arrange for payment of licensing fees, please contact Copyright Clearance Center, Customer Service, 222 Rosewood Drive, Danvers, MA 01923 USA; +1 978 750 8400. Permission to photocopy portions of any individual standard for educational classroom use can also be obtained through the Copyright Clearance Center.

## Updating of IEEE Standards documents

Users of IEEE Standards documents should be aware that these documents may be superseded at any time by the issuance of new editions or may be amended from time to time through the issuance of amendments, corrigenda, or errata. An official IEEE document at any point in time consists of the current edition of the document together with any amendments, corrigenda, or errata then in effect.

Every IEEE standard is subjected to review at least every 10 years. When a document is more than 10 years old and has not undergone a revision process, it is reasonable to conclude that its contents, although still of some value, do not wholly reflect the present state of the art. Users are cautioned to check to determine that they have the latest edition of any IEEE standard.

In order to determine whether a given document is the current edition and whether it has been amended through the issuance of amendments, corrigenda, or errata, visit IEEE Xplore at <https://ieeexplore.ieee.org/> or contact IEEE at the address listed previously. For more information about the IEEE SA or IEEE's standards development process, visit the IEEE SA Website at <https://standards.ieee.org>.

## Errata

Errata, if any, for IEEE standards can be accessed via <https://standards.ieee.org/standard/index.html>. Search for standard number and year of approval to access the web page of the published standard. Errata links are located under the Additional Resources Details section. Errata are also available in IEEE Xplore: <https://ieeexplore.ieee.org/browse/standards/collection/ieee/>. Users are encouraged to periodically check for errata.

## Patents

Attention is called to the possibility that implementation of this standard may require use of subject matter covered by patent rights. By publication of this standard, no position is taken by the IEEE with respect to the existence or validity of any patent rights in connection therewith. If a patent holder or patent applicant has filed a statement of assurance via an Accepted Letter of Assurance, then the statement is listed on the IEEE SA Website at <https://standards.ieee.org/about/sasb/patcom/patents.html>. Letters of Assurance may indicate whether the Submitter is willing or unwilling to grant licenses under patent rights without compensation or under reasonable rates, with reasonable terms and conditions that are demonstrably free of any unfair discrimination to applicants desiring to obtain such licenses.

Essential Patent Claims may exist for which a Letter of Assurance has not been received. The IEEE is not responsible for identifying Essential Patent Claims for which a license may be required, for conducting inquiries into the legal validity or scope of Patents Claims, or determining whether any licensing terms or conditions provided in connection with submission of a Letter of Assurance, if any, or in any licensing agreements are reasonable or non-discriminatory. Users of this standard are expressly advised that determination of the validity of any patent rights, and the risk of infringement of such rights, is entirely their own responsibility. Further information may be obtained from the IEEE Standards Association.

## Participants

At the time this revision was submitted to the IEEE SA Standards Board for approval, the IEEE 802.1 Working Group had the following membership:

**Glenn Parsons, *Chair***  
**John Messenger, *Vice Chair***  
**János Farkas, *Chair, Time-Sensitive Networking Task Group***  
**Craig Gunther, *Vice Chair, Time-Sensitive Networking Task Group***  
**Geoffrey Garner, *Editor***  
**Stephan Kehrer, *MIB Editor***

|                     |                   |                  |
|---------------------|-------------------|------------------|
| Astrit Ademaj       | Michael Karl      | Jessy Rouyer     |
| Ralf Assmann        | Randy Kelsey      | Atsushi Sato     |
| Jens Bierschenk     | Hajime Koto       | Frank Schewe     |
| Christian Boiger    | James Lawlis      | Michael Seaman   |
| Paul Bottorff       | Christophe Mangin | Maik Seewald     |
| Radhakrishna Canchi | Scott Mansfield   | Johannes Specht  |
| Feng Chen           | Kenichi Maruhashi | Marius Stanica   |
| Weiyang Cheng       | David McCall      | Gunter Steindl   |
| Paul Congdon        | Larry McMillan    | Karim Traore     |
| Rodney Cummings     | Tero Mustala      | Xinyuan Wang     |
| Josef Dorr          | Roy Myers         | Tongtong Wang    |
| Hesham Elbakoury    | Hiroki Nakano     | Hao Wang         |
| Thomas Enzinger     | Bob Noseworthy    | Karl Weber       |
| Donald Fedyk        | Tomoki Ohsawa     | Brian Weis       |
| Norman Finn         | Hiroshi Ohue      | Ludwig Winkel    |
| Marina Gutierrez    | Donald Pannell    | Jordon Woods     |
| Stephen Haddock     | Michael Potts     | Takahiro Yamaura |
| Mark Hantel         | Dieter Proell     | Nader Zein       |
| Marc Holness        | Wei Qiu           | William Zhao     |
| Satoko Itaya        | Karen Randall     | Helge Zinner     |
| Yoshihiro Ito       | Maximilian Riegel | Harald Zweck     |

The following members of the balloting committee voted on this standard. Balloters may have voted for approval, disapproval, or abstention.

|                     |                   |                  |
|---------------------|-------------------|------------------|
| Robert Aiello       | Charles Cook      | Marina Gutierrez |
| Thomas Alexander    | Rodney Cummings   | Stephen Haddock  |
| Amelia Andersdotter | William Dickerson | Mark Hantel      |
| Carol Ansley        | Michael Dood      | Marco Hernandez  |
| Butch Anton         | János Farkas      | Werner Hoelzl    |
| Stefan Aust         | Norman Finn       | Woojung Huh      |
| Philip Beaumont     | Michael Fischer   | C. Huntley       |
| Gordon Bechtel      | John Fletcher     | Noriyuki Ikeuchi |
| Harry Bims          | Avraham Freedman  | Atsushi Ito      |
| Christian Boiger    | Matthias Fritsche | Raj Jain         |
| Kenneth Bow         | Yukihiro Fujimoto | SangKwon Jeong   |
| Nancy Bravin        | Geoffrey Garner   | Lokesh Kabra     |
| Vern Brethour       | Devon Gayle       | Manabu Kagami    |
| Dietmar Bruckner    | Mietek Glinkowski | Innocent Kamwa   |
| Demetrio Bucaneg    | Zhigang Gong      | Piotr Karocki    |
| Ashley Butterworth  | David Goodall     | Stephan Kehrer   |
| William Byrd        | Ethan Grossman    | Stuart Kerry     |
| Paul Cardinal       | Randall Groves    | Evgeny Khorov    |
| Juan Carreon        | Stephen Guendert  | Yongbum Kim      |
| David Chalupsky     | Michael Gundlach  | Patrick Kinney   |
| David Chen          | Craig Gunther     | Thomas Kurihara  |

Chung-Yiu Lam  
Robert Landman  
Hyeong Ho Lee  
John Lemon  
James Lepp  
Daniel Levesque  
Jon Lewis  
Michael Lynch  
John Mackay  
Bruce Mackie  
Nicolai Malykh  
Vinayagam Mariappan  
Roger Marks  
Arthur Marris  
Jeffery Masters  
Stephen McCann  
Brett McClellan  
Sven Meier  
Richard Mellitz  
Michael Montemurro  
Matthew Mora  
Bruce Muschlitz

Michael Newman  
Nick S. A. Nikjoo  
Paul Nikolich  
Satoshi Obara  
Robert O'Hara  
David Olsen  
Glenn Parsons  
Bansi Patel  
Arumugam Paventhan  
Walter Pieniac  
Clinton Powell  
R. K. Rannow  
Alon Regev  
Denis Reilly  
Maximilian Riegel  
Robert Robinson  
Silvana Rodrigues  
Jessy Rouyer  
Richard Roy  
Peter Saunderson  
Frank Schewe  
Michael Seaman

Veselin Skendzic  
Manikantan Srinivasan  
Kevin Stanton  
Thomas Starai  
Walter Struppler  
David Thompson  
Payam Torab  
Mark-Rene Uchida  
James Van De Ligt  
Dmitri Varsanofiev  
George Vlantis  
Khurram Waheed  
Lisa Ward  
Stephen Webb  
Karl Weber  
Hung-Yu Wei  
Scott Willy  
Ludwig Winkel  
Andreas Wolf  
Yu Yuan  
Oren Yuen  
Nader Zein

When the IEEE SA Standards Board approved this standard on 30 January 2020, it had the following membership:

**Gary Hoffman, Chair**  
**Vacant Position, Vice Chair**  
**Jean-Philippe Faure, Past Chair**  
**Konstantinos Karachalios, Secretary**

Ted Burse  
J. Travis Griffith  
Grace Gu  
Guido R. Hiertz  
Joseph L. Koepfinger\*  
John D. Kulick  
David J. Law

Howard Li  
Dong Liu  
Kevin Lu  
Paul Nikolich  
Damir Novosel  
Jon Walter Rosdahl

Dorothy Stanley  
Mehmet Ulema  
Lei Wang  
Sha Wei  
Philip B. Winston  
Daidi Zhong  
Jingyi Zhou

\*Member Emeritus

## Introduction

This introduction is not part of IEEE Std 802.1AS-2020, IEEE Standard for Local and Metropolitan Area Networks—Timing and Synchronization for Time-Sensitive Applications.

This standard contains state-of-the-art material. The area covered by this standard is undergoing evolution. Revisions are anticipated within the next few years to clarify existing material, to correct possible errors, and to incorporate new related material. Information on the current revision state of this and other IEEE 802<sup>®</sup> standards may be obtained from

Secretary, IEEE SA Standards Board  
445 Hoes Lane  
Piscataway, NJ 08854 USA

## Contents

|     |                                                                                           |    |
|-----|-------------------------------------------------------------------------------------------|----|
| 1.  | Overview.....                                                                             | 17 |
| 1.1 | Scope.....                                                                                | 17 |
| 1.2 | Purpose.....                                                                              | 17 |
| 2.  | Normative references .....                                                                | 18 |
| 3.  | Definitions .....                                                                         | 20 |
| 4.  | Acronyms and abbreviations .....                                                          | 23 |
| 5.  | Conformance.....                                                                          | 26 |
| 5.1 | Requirements terminology.....                                                             | 26 |
| 5.2 | Protocol Implementation Conformance Statement (PICS).....                                 | 26 |
| 5.3 | Time-aware system requirements .....                                                      | 26 |
| 5.4 | PTP Instance requirements and options.....                                                | 26 |
| 5.5 | MAC-specific timing and synchronization methods for full-duplex IEEE 802.3 links .....    | 28 |
| 5.6 | MAC-specific timing and synchronization methods for IEEE Std 802.11-2016 .....            | 29 |
| 5.7 | MAC-specific timing and synchronization methods for IEEE 802.3 EPON .....                 | 29 |
| 5.8 | MAC-specific timing and synchronization methods for coordinated shared network (CSN)..... | 29 |
| 6.  | Conventions .....                                                                         | 30 |
| 6.1 | General.....                                                                              | 30 |
| 6.2 | Service specification method and notation .....                                           | 30 |
| 6.3 | Lexical form syntax .....                                                                 | 30 |
| 6.4 | Data types and on-the-wire formats.....                                                   | 30 |
| 7.  | Time-synchronization model for a packet network .....                                     | 35 |
| 7.1 | General.....                                                                              | 35 |
| 7.2 | Architecture of a time-aware network .....                                                | 35 |
| 7.3 | Time synchronization .....                                                                | 42 |
| 7.4 | PTP Instance architecture .....                                                           | 45 |
| 7.5 | Differences between gPTP (IEEE Std 802.1AS) and PTP (IEEE Std 1588-2019) .....            | 46 |
| 8.  | IEEE 802.1AS concepts and terminology .....                                               | 48 |
| 8.1 | gPTP domain.....                                                                          | 48 |
| 8.2 | Timescale .....                                                                           | 48 |
| 8.3 | Link asymmetry .....                                                                      | 50 |
| 8.4 | Messages .....                                                                            | 51 |
| 8.5 | Ports .....                                                                               | 53 |
| 8.6 | PTP Instance characterization.....                                                        | 54 |
| 9.  | Application interfaces .....                                                              | 59 |
| 9.1 | Overview of the interfaces .....                                                          | 59 |
| 9.2 | ClockSourceTime interface .....                                                           | 60 |
| 9.3 | ClockTargetEventCapture interface .....                                                   | 60 |
| 9.4 | ClockTargetTriggerGenerate interface .....                                                | 61 |
| 9.5 | ClockTargetClockGenerator interface .....                                                 | 63 |

|      |                                                                                                                 |     |
|------|-----------------------------------------------------------------------------------------------------------------|-----|
| 9.6  | ClockTargetPhaseDiscontinuity interface .....                                                                   | 64  |
| 10.  | Media-independent layer specification .....                                                                     | 65  |
| 10.1 | Overview .....                                                                                                  | 65  |
| 10.2 | Time-synchronization state machines .....                                                                       | 67  |
| 10.3 | Best master clock selection, external port configuration, and announce interval setting<br>state machines ..... | 98  |
| 10.4 | State machines related to signaling gPTP capability .....                                                       | 135 |
| 10.5 | Message attributes .....                                                                                        | 142 |
| 10.6 | Message formats .....                                                                                           | 143 |
| 10.7 | Protocol timing characterization .....                                                                          | 156 |
| 11.  | Media-dependent layer specification for full-duplex point-to-point links .....                                  | 160 |
| 11.1 | Overview .....                                                                                                  | 160 |
| 11.2 | State machines for MD entity specific to full-duplex point-to-point links .....                                 | 166 |
| 11.3 | Message attributes .....                                                                                        | 195 |
| 11.4 | Message formats .....                                                                                           | 197 |
| 11.5 | Protocol timing characterization .....                                                                          | 205 |
| 11.6 | Control of computation of neighborRateRatio .....                                                               | 206 |
| 11.7 | Control of computation of meanLinkDelay .....                                                                   | 207 |
| 12.  | Media-dependent layer specification for IEEE 802.11 links .....                                                 | 208 |
| 12.1 | Overview .....                                                                                                  | 208 |
| 12.2 | Messages .....                                                                                                  | 212 |
| 12.3 | Determination of Timing Measurement and Fine Timing Measurement capability .....                                | 214 |
| 12.4 | Determination of asCapable .....                                                                                | 214 |
| 12.5 | State machines .....                                                                                            | 215 |
| 12.6 | FTM parameters .....                                                                                            | 227 |
| 12.7 | Format of VendorSpecific information element .....                                                              | 228 |
| 12.8 | Synchronization message interval .....                                                                          | 229 |
| 13.  | Media-dependent layer specification for interface to IEEE 802.3 Ethernet passive optical<br>network link .....  | 230 |
| 13.1 | Overview .....                                                                                                  | 230 |
| 13.2 | Message attributes .....                                                                                        | 234 |
| 13.3 | Message format .....                                                                                            | 234 |
| 13.4 | Determination of asCapable .....                                                                                | 237 |
| 13.5 | Layering for IEEE 802.3 EPON links .....                                                                        | 237 |
| 13.6 | Service interface definitions .....                                                                             | 238 |
| 13.7 | MD entity global variables .....                                                                                | 240 |
| 13.8 | State machines .....                                                                                            | 241 |
| 13.9 | Message transmission intervals .....                                                                            | 244 |
| 14.  | Timing and synchronization management .....                                                                     | 245 |
| 14.1 | General .....                                                                                                   | 245 |
| 14.2 | Default Parameter Data Set (defaultDS) .....                                                                    | 248 |
| 14.3 | Current Parameter Data Set (currentDS) .....                                                                    | 251 |
| 14.4 | Parent Parameter Data Set (parentDS) .....                                                                      | 253 |
| 14.5 | Time Properties Parameter Data Set (timePropertiesDS) .....                                                     | 255 |
| 14.6 | Path Trace Parameter Data Set (pathTraceDS) .....                                                               | 256 |
| 14.7 | Acceptable Master Table Parameter Data Set (acceptableMasterTableDS) .....                                      | 257 |

|         |                                                                                                                        |     |
|---------|------------------------------------------------------------------------------------------------------------------------|-----|
| 14.8    | Port Parameter Data Set (portDS).....                                                                                  | 258 |
| 14.9    | Description Port Parameter Data Set (descriptionPortDS) .....                                                          | 269 |
| 14.10   | Port Parameter Statistics Data Set (portStatisticsDS).....                                                             | 269 |
| 14.11   | Acceptable Master Port Parameter Data Set (acceptableMasterPortDS) .....                                               | 273 |
| 14.12   | External Port Configuration Port Parameter Data Set<br>(externalPortConfigurationPortDS).....                          | 273 |
| 14.13   | Asymmetry Measurement Mode Parameter Data Set<br>(asymmetryMeasurementModeDS).....                                     | 274 |
| 14.14   | Common Services Port Parameter Data Set (commonServicesPortDS) .....                                                   | 275 |
| 14.15   | Common Mean Link Delay Service Default Parameter Data Set (cmldsDefaultDS) .....                                       | 275 |
| 14.16   | Common Mean Link Delay Service Link Port Parameter Data Set (cmldsLinkPortDS)....                                      | 276 |
| 14.17   | Common Mean Link Delay Service Link Port Parameter Statistics Data Set<br>(cmldsLinkPortStatisticsDS) .....            | 281 |
| 14.18   | Common Mean Link Delay Service Asymmetry Measurement Mode Parameter Data<br>Set (cmldsAsymmetryMeasurementModeDS)..... | 283 |
| 15.     | Managed object definitions .....                                                                                       | 284 |
| 15.1    | Internet Standard Management Framework .....                                                                           | 284 |
| 15.2    | Structure of the MIB .....                                                                                             | 284 |
| 15.3    | Relationship to MIB in IEEE Std 802.1AS-2011 .....                                                                     | 291 |
| 15.4    | Security considerations .....                                                                                          | 291 |
| 15.5    | Textual conventions defined in this MIB .....                                                                          | 293 |
| 15.6    | IEEE 802.1AS MIB module .....                                                                                          | 293 |
| 16.     | Media-dependent layer specification for CSN.....                                                                       | 368 |
| 16.1    | Overview.....                                                                                                          | 368 |
| 16.2    | Coordinated Shared Network characteristics.....                                                                        | 368 |
| 16.3    | Layering for CSN links.....                                                                                            | 369 |
| 16.4    | Path delay measurement over a CSN backbone .....                                                                       | 370 |
| 16.5    | Synchronization messages .....                                                                                         | 373 |
| 16.6    | Specific CSN requirements.....                                                                                         | 377 |
| 16.7    | Grandmaster PTP Instance capability .....                                                                              | 378 |
| 16.8    | CSN clock and node performance requirements .....                                                                      | 378 |
| Annex A | (normative) Protocol Implementation Conformance Statement (PICS) proforma .....                                        | 379 |
| A.1     | Introduction.....                                                                                                      | 379 |
| A.2     | Abbreviations and special symbols.....                                                                                 | 379 |
| A.3     | Instructions for completing the PICS proforma.....                                                                     | 380 |
| A.4     | PICS proforma for IEEE Std 802.1AS-2020 .....                                                                          | 382 |
| A.5     | Major capabilities .....                                                                                               | 383 |
| A.6     | Media access control methods .....                                                                                     | 384 |
| A.7     | Minimal time-aware system.....                                                                                         | 384 |
| A.8     | Signaling .....                                                                                                        | 386 |
| A.9     | Best master clock .....                                                                                                | 387 |
| A.11    | Media-independent master.....                                                                                          | 389 |
| A.10    | Grandmaster-capable PTP Instance .....                                                                                 | 389 |
| A.12    | Media-independent performance requirements .....                                                                       | 390 |
| A.13    | Media-dependent, full-duplex point-to-point link .....                                                                 | 391 |
| A.15    | Media-dependent IEEE 802.3 EPON link .....                                                                             | 394 |
| A.14    | Media-dependent IEEE 802.11 link.....                                                                                  | 394 |
| A.16    | Media-dependent CSN link .....                                                                                         | 395 |
| A.17    | Media-dependent MoCA link .....                                                                                        | 395 |

|                                                                                               |                                                           |     |
|-----------------------------------------------------------------------------------------------|-----------------------------------------------------------|-----|
| A.19                                                                                          | Remote management.....                                    | 396 |
| A.20                                                                                          | Application interfaces .....                              | 396 |
| A.21                                                                                          | External port configuration.....                          | 396 |
| A.18                                                                                          | Media-dependent ITU-T G.hn link .....                     | 396 |
| Annex B (normative) Performance requirements .....                                            |                                                           | 397 |
| B.1                                                                                           | LocalClock requirements.....                              | 397 |
| B.2                                                                                           | PTP Instance requirements .....                           | 402 |
| B.3                                                                                           | End-to-end time-synchronization performance .....         | 403 |
| B.4                                                                                           | End-to-end jitter and wander performance .....            | 403 |
| Annex C (informative) Timescales and epochs .....                                             |                                                           | 405 |
| C.1                                                                                           | Overview .....                                            | 405 |
| C.2                                                                                           | TAI and UTC .....                                         | 405 |
| C.3                                                                                           | NTP and GPS.....                                          | 407 |
| C.4                                                                                           | Timescale conversions.....                                | 407 |
| C.5                                                                                           | Time zones and GMT .....                                  | 408 |
| Annex D Reserved for future use .....                                                         |                                                           | 409 |
| Annex E Reserved for future use .....                                                         |                                                           | 410 |
| Annex F (informative) PTP profile included in this standard .....                             |                                                           | 411 |
| F.1                                                                                           | General.....                                              | 411 |
| F.2                                                                                           | Identification.....                                       | 411 |
| F.3                                                                                           | PTP attribute values .....                                | 412 |
| F.4                                                                                           | PTP options.....                                          | 412 |
| F.5                                                                                           | LocalClock and PTP Instance performance requirements..... | 413 |
| Annex G (informative) The asymmetry compensation measurement procedure based on line-swapping |                                                           | 414 |
| G.1                                                                                           | Introduction.....                                         | 414 |
| G.2                                                                                           | Pre-conditions for measurement .....                      | 414 |
| G.3                                                                                           | Measurement procedure.....                                | 414 |
| Annex H (informative) Bibliography .....                                                      |                                                           | 417 |

## List of figures

|                                                                                                                                                                                                                                                 |     |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|
| Figure 7-1—Time-aware network example.....                                                                                                                                                                                                      | 36  |
| Figure 7-2—Time-aware network of Figure 7-1 after an access network link failure .....                                                                                                                                                          | 37  |
| Figure 7-3—Time-aware network example for multiple gPTP domains .....                                                                                                                                                                           | 38  |
| Figure 7-4—Time-aware network example for synchronization path redundancy, with one clock source providing time to two domains .....                                                                                                            | 40  |
| Figure 7-5—Time-aware network example for GM redundancy with one primary GM and one hot-standby GM, which are separated in two gPTP domains .....                                                                                               | 41  |
| Figure 7-6—Time-aware network example for GM+synchronziation path redundancy, with one primary and one hot-standby GM. Each GM establishes two sync trees, resulting in a total of four Sync trees that are separated in four gPTP domains..... | 42  |
| Figure 7-7—Conceptual medium delay measurement .....                                                                                                                                                                                            | 43  |
| Figure 7-8—PTP Instance model .....                                                                                                                                                                                                             | 45  |
| Figure 8-1—Propagation asymmetry.....                                                                                                                                                                                                           | 50  |
| Figure 8-2—Definition of message timestamp point, reference plane, timestamp measurement plane, and latency constants .....                                                                                                                     | 52  |
| Figure 9-1—Application interfaces .....                                                                                                                                                                                                         | 59  |
| Figure 10-1—Model for media-independent layer of PTP Instance .....                                                                                                                                                                             | 66  |
| Figure 10-2—Time-synchronization state machines—overview and interrelationships.....                                                                                                                                                            | 68  |
| Figure 10-3—SiteSyncSync state machine.....                                                                                                                                                                                                     | 84  |
| Figure 10-4—PortSyncSyncReceive state machine .....                                                                                                                                                                                             | 86  |
| Figure 10-5—ClockMasterSyncSend state machine .....                                                                                                                                                                                             | 88  |
| Figure 10-6—ClockMasterSyncOffset state machine .....                                                                                                                                                                                           | 90  |
| Figure 10-7—ClockMasterSyncReceive state machine .....                                                                                                                                                                                          | 92  |
| Figure 10-8—PortSyncSyncSend state machine .....                                                                                                                                                                                                | 95  |
| Figure 10-9—ClockSlaveSync state machine .....                                                                                                                                                                                                  | 97  |
| Figure 10-10—Example master/slave hierarchy of PTP Instances .....                                                                                                                                                                              | 99  |
| Figure 10-11—Best master clock selection state machines—overview and interrelationships .....                                                                                                                                                   | 105 |
| Figure 10-12—External port configuration state machines—overview and interrelationships .....                                                                                                                                                   | 106 |
| Figure 10-13—PortAnnounceReceive state machine.....                                                                                                                                                                                             | 116 |
| Figure 10-14—PortAnnounceInformation state machine.....                                                                                                                                                                                         | 118 |
| Figure 10-15—PortStateSelection state machine .....                                                                                                                                                                                             | 121 |
| Figure 10-16—PortAnnounceInformationExt state machine .....                                                                                                                                                                                     | 123 |
| Figure 10-17—PortStateSettingExt state machine .....                                                                                                                                                                                            | 126 |
| Figure 10-18—PortAnnounceTransmit state machine .....                                                                                                                                                                                           | 128 |
| Figure 10-19—AnnounceIntervalSetting state machine.....                                                                                                                                                                                         | 131 |
| Figure 10-20—SyncIntervalSetting state machine .....                                                                                                                                                                                            | 134 |
| Figure 10-21—GptpCapableTransmit state machine .....                                                                                                                                                                                            | 136 |
| Figure 10-22—GptpCapableReceive state machine.....                                                                                                                                                                                              | 138 |
| Figure 10-23—GptpCapableIntervalSetting state machine.....                                                                                                                                                                                      | 141 |
| Figure 11-1—Propagation delay measurement using peer-to-peer delay mechanism .....                                                                                                                                                              | 161 |
| Figure 11-2—Transport of time-synchronization information.....                                                                                                                                                                                  | 163 |
| Figure 11-3—Model for a PTP Instance of a time-aware system with full-duplex point-to-point links ...                                                                                                                                           | 166 |
| Figure 11-4—Detail of MD entity time-synchronization state machines for full-duplex point-to-point links .....                                                                                                                                  | 167 |
| Figure 11-5—Peer-to-peer delay mechanism state machines—overview and interrelationships .....                                                                                                                                                   | 167 |
| Figure 11-6—MDSyncReceiveSM state machine.....                                                                                                                                                                                                  | 175 |
| Figure 11-7—MDSyncSendSM state machine.....                                                                                                                                                                                                     | 179 |
| Figure 11-8—OneStepTxOperSetting state machine .....                                                                                                                                                                                            | 181 |
| Figure 11-9—MDPdelayReq state machine .....                                                                                                                                                                                                     | 188 |
| Figure 11-10—MDPdelayResp state machine .....                                                                                                                                                                                                   | 191 |
| Figure 11-11—LinkDelayIntervalSetting state machine .....                                                                                                                                                                                       | 194 |

|                                                                                                                                                                                                        |     |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|
| Figure 12-1—Timing measurement procedure for IEEE 802.11 links .....                                                                                                                                   | 209 |
| Figure 12-2—Fine Timing Measurement procedure for IEEE 802.11 links .....                                                                                                                              | 209 |
| Figure 12-3—Illustration of Fine Timing Measurement burst .....                                                                                                                                        | 211 |
| Figure 12-4—Media-dependent and lower entities in stations with IEEE 802.11 links .....                                                                                                                | 213 |
| Figure 12-5—Master state machine A: (a) For TM, receives information from the PortSync entity and sends to slave, and (b) for FTM, receives and stores information from the PortSync entity .....      | 216 |
| Figure 12-6—Master state machine B: (a) For TM, not invoked and (b) for FTM, receives initial FTM request from slave and sends information received from upstream to slave in successive FTM frames... | 217 |
| Figure 12-7—Slave state machine .....                                                                                                                                                                  | 223 |
| Figure 12-8—Format of VendorSpecific information element when Type = 0 .....                                                                                                                           | 228 |
| Figure 13-1—IEEE 802.3 EPON time-synchronization interfaces .....                                                                                                                                      | 233 |
| Figure 13-2—IEEE 802.3 EPON interface model.....                                                                                                                                                       | 238 |
| Figure 13-3—State machine for IEEE 802.3 EPON requester.....                                                                                                                                           | 242 |
| Figure 13-4—State machine for IEEE 802.3 EPON responder.....                                                                                                                                           | 244 |
| Figure 16-1—Example of CSN backbone in a TSN LAN .....                                                                                                                                                 | 368 |
| Figure 16-2—Media-dependent and lower entities in CSN nodes .....                                                                                                                                      | 369 |
| Figure 16-3—Path types over CSN as IEEE 802.1AS backbone.....                                                                                                                                          | 370 |
| Figure 16-4—Propagation delay and residence time over a CSN backbone.....                                                                                                                              | 370 |
| Figure 16-5—CSN node-to-node path delay measurement.....                                                                                                                                               | 371 |
| Figure 16-6—IEEE 802.1AS Sync message propagation over the CSN backbone .....                                                                                                                          | 373 |
| Figure B-1—Wander generation (TDEV) requirement for LocalClock entity .....                                                                                                                            | 399 |
| Figure B-2—ADEV limit corresponding to wander generation requirement of Figure B-1 .....                                                                                                               | 400 |
| Figure B-3—PTPDEV limit corresponding to wander generation requirement of Figure B-1.....                                                                                                              | 401 |
| Figure B-4—MTIE masks met for maximum endpoint filter bandwidths of Table B-4.....                                                                                                                     | 404 |
| Figure G-1—Asymmetry compensation measurement procedure .....                                                                                                                                          | 415 |

## List of tables

|                                                                                                                                                                                                 |     |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|
| Table 6-1—Primitive data types .....                                                                                                                                                            | 31  |
| Table 8-1—Default values for priority1, for the respective media.....                                                                                                                           | 55  |
| Table 8-2—TimeSource enumeration .....                                                                                                                                                          | 57  |
| Table 10-1—Summary of scope of global variables used by time synchronization state machines<br>(see 10.2.4 and 10.2.5).....                                                                     | 76  |
| Table 10-2—PTP Port state definitions .....                                                                                                                                                     | 98  |
| Table 10-3—Summary of scope of global variables used by best master clock selection, external port<br>configuration, and announce interval setting state machines (see 10.3.9 and 10.3.10)..... | 107 |
| Table 10-4—Destination address for Announce and Signaling messages .....                                                                                                                        | 142 |
| Table 10-5—EtherType for Announce and Signaling messages .....                                                                                                                                  | 142 |
| Table 10-6—Propagate TLVs of IEEE Std 1588-2019 .....                                                                                                                                           | 144 |
| Table 10-7—PTP message header .....                                                                                                                                                             | 144 |
| Table 10-8—Values for messageType field .....                                                                                                                                                   | 145 |
| Table 10-9—Values of flag bits.....                                                                                                                                                             | 146 |
| Table 10-10—messageTypeSpecific semantics .....                                                                                                                                                 | 147 |
| Table 10-11—Announce message fields .....                                                                                                                                                       | 148 |
| Table 10-12—Path trace TLV .....                                                                                                                                                                | 149 |
| Table 10-13—Signaling message fields .....                                                                                                                                                      | 150 |
| Table 10-14—Message interval request TLV .....                                                                                                                                                  | 151 |
| Table 10-15—Interpretation of special values of logLinkDelayInterval.....                                                                                                                       | 152 |
| Table 10-16—Interpretation of special values of logTimeSyncInterval .....                                                                                                                       | 152 |
| Table 10-17—Interpretation of special values of logAnnounceInterval .....                                                                                                                       | 153 |
| Table 10-18—Definitions of bits of flags field of message interval request TLV .....                                                                                                            | 153 |
| Table 10-19—gPTP-capable TLV .....                                                                                                                                                              | 154 |
| Table 10-20—gPTP-capable message interval request TLV.....                                                                                                                                      | 155 |
| Table 10-21—Interpretation of special values of logGtpCapableMessageInterval.....                                                                                                               | 156 |
| Table 11-1—Value of meanLinkDelayThresh for various links .....                                                                                                                                 | 169 |
| Table 11-2—Summary of scope of global variables used by<br>time synchronization state machines (see 10.2.4 and 10.2.5).....                                                                     | 171 |
| Table 11-3—Destination address for Sync, Follow_Up, Pdelay_Req, Pdelay_Resp, and<br>Pdelay_Resp_Follow_Up messages .....                                                                        | 196 |
| Table 11-4—EtherType for Sync, Follow_Up, Pdelay_Req, Pdelay_Resp, and<br>Pdelay_Resp_Follow_Up messages .....                                                                                  | 196 |
| Table 11-5—Values for messageType field .....                                                                                                                                                   | 198 |
| Table 11-6—Value of correctionField.....                                                                                                                                                        | 199 |
| Table 11-7—References for sequenceId value exceptions.....                                                                                                                                      | 199 |
| Table 11-8—Sync message fields if twoStep flag is TRUE.....                                                                                                                                     | 200 |
| Table 11-9—Sync message fields if twoStep flag is FALSE.....                                                                                                                                    | 200 |
| Table 11-10—Follow_Up message fields .....                                                                                                                                                      | 201 |
| Table 11-11—Follow_Up information TLV .....                                                                                                                                                     | 202 |
| Table 11-12—Pdelay_Req message fields .....                                                                                                                                                     | 203 |
| Table 11-13—Pdelay_Resp message fields.....                                                                                                                                                     | 204 |
| Table 11-14—Pdelay_Resp_Follow_Up message fields .....                                                                                                                                          | 204 |
| Table 12-1—Values of bits of tmFtmSupport .....                                                                                                                                                 | 214 |
| Table 12-2—FTM parameters relevant to time-synchronization transport .....                                                                                                                      | 227 |
| Table 12-3—Values of Burst Duration and Min Delta FTM, for each value of currentLogSyncInterval ..                                                                                              | 228 |
| Table 12-4—Values of the Type field in the VendorSpecific information element.....                                                                                                              | 228 |
| Table 13-1—TIMESYNC message fields .....                                                                                                                                                        | 235 |
| Table 14-1—defaultDS table .....                                                                                                                                                                | 251 |
| Table 14-2—currentDS table .....                                                                                                                                                                | 253 |
| Table 14-3—parentDS table .....                                                                                                                                                                 | 255 |

|                                                                                                                           |     |
|---------------------------------------------------------------------------------------------------------------------------|-----|
| Table 14-4—timePropertiesDS table.....                                                                                    | 256 |
| Table 14-5—pathTraceDS table .....                                                                                        | 257 |
| Table 14-6—acceptableMasterTableDS table .....                                                                            | 258 |
| Table 14-7—portState enumeration.....                                                                                     | 258 |
| Table 14-8—delayMechanism enumeration.....                                                                                | 259 |
| Table 14-9—Description of pdelayTruncatedTimestampsArray .....                                                            | 266 |
| Table 14-10—portDS table.....                                                                                             | 267 |
| Table 14-11—descriptionPortDS table.....                                                                                  | 269 |
| Table 14-12—portStatisticsDS table .....                                                                                  | 272 |
| Table 14-13—acceptableMasterPortDS table .....                                                                            | 273 |
| Table 14-14—externalPortConfigurationPortDS table .....                                                                   | 274 |
| Table 14-15—asymmetryMeasurementModeDS table .....                                                                        | 274 |
| Table 14-16—commonServicesPortDS table .....                                                                              | 275 |
| Table 14-17—cmldsDefaultDS table.....                                                                                     | 276 |
| Table 14-18—cmldsLinkPortDS table .....                                                                                   | 280 |
| Table 14-19—cmldsLinkPortStatisticsDS table.....                                                                          | 282 |
| Table 14-20—cmldsAsymmetryMeasurementModeDS table.....                                                                    | 283 |
| Table 15-1—IEEE8021-AS-V2 MIB structure and object cross reference.....                                                   | 285 |
| Table 16-1—CSN TLV .....                                                                                                  | 375 |
| Table 16-2—Definitions and option selections per link technology .....                                                    | 377 |
| Table B-1—Wander generation TDEV requirement for LocalClock entity .....                                                  | 399 |
| Table B-2—ADEV limit corresponding to wander generation requirement of Table B-1 .....                                    | 400 |
| Table B-3—PTPDEV limit corresponding to wander generation requirement of Table B-1 .....                                  | 401 |
| Table B-4—Maximum endpoint filter bandwidths needed to meet respective MTIE masks and<br>peak-to-peak jitter limits ..... | 403 |
| Table B-5—Breakpoints for Mask 1 .....                                                                                    | 404 |
| Table B-6—Breakpoints for Mask 2 .....                                                                                    | 404 |
| Table C-1—Timescale parameters .....                                                                                      | 405 |
| Table C-2—Timescale conversions.....                                                                                      | 408 |

# IEEE Standard for Local and Metropolitan Area Networks— Timing and Synchronization for Time-Sensitive Applications

## 1. Overview

### 1.1 Scope

This standard specifies protocols, procedures, and managed objects used to ensure that the synchronization requirements are met for time-sensitive applications, such as audio, video, and time-sensitive control, across networks, for example, IEEE 802 and similar media. This includes the maintenance of synchronized time during normal operation and following addition, removal, or failure of network components and network reconfiguration. It specifies the use of IEEE 1588™ specifications where applicable in the context of IEEE Std 802.1Q™-2018.<sup>1</sup> Synchronization to an externally provided timing signal [e.g., a recognized timing standard such as Coordinated Universal Time (UTC) or International Atomic Time (TAI)] is not part of this standard but is not precluded.

### 1.2 Purpose

This standard enables systems to meet the respective jitter, wander, and time-synchronization requirements for time-sensitive applications, including those that involve multiple streams delivered to multiple end stations. To facilitate the widespread use of packet networks for these applications, synchronization information is one of the components needed at each network element where time-sensitive application data are mapped or demapped or a time-sensitive function is performed. This standard leverages the work of the IEEE 1588 Working Group by developing the additional specifications needed to address these requirements.

---

<sup>1</sup> Information on references can be found in Clause 2.

## 2. Normative references

The following referenced documents are indispensable for the application of this standard (i.e., they must be understood and used; therefore, each referenced document is cited in text, and its relationship to this document is explained). For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments or corrigenda) applies.

IEEE Std 754™-2008, IEEE Standard for Floating-Point Arithmetic.<sup>2, 3</sup>

IEEE Std 802®-2014, IEEE Standard for Local and Metropolitan Area Networks—Overview and Architecture.

IEEE Std 802c™-2017, IEEE Standard for Local and Metropolitan Area Networks—Overview and Architecture—Amendment 2: Local Medium Access Control (MAC) Address Usage.

IEEE Std 802.1AC™-2016, IEEE Standard for Local and metropolitan area networks—Media Access Control (MAC) Service Definition.

IEEE Std 802.1AX™-2014, IEEE Standard for Local and metropolitan area networks—Link Aggregation.

IEEE Std 802.1Q™-2018, IEEE Standard for Local and Metropolitan Area Networks—Bridges and Bridged Networks.

IEEE Std 802.3™-2018, IEEE Standard for Ethernet.

IEEE Std 802.11™-2016, IEEE Standard for Information technology—Telecommunications and information exchange between systems—Local and metropolitan area networks—Specific requirements, Part 11: Wireless LAN Medium Access Control (MAC) and Physical Layer (PHY) Specifications.

IEEE Std 1588™-2019, IEEE Standard for a Precision Clock Synchronization Protocol for Networked Measurement and Control Systems.

IERS Bulletin C (see <https://www.iers.org/IERS/EN/Publications/Bulletins/bulletins.html>).

IETF RFC 2863 (June 2000), The Interfaces Group MIB, K. McCloghrie and F. Kastenholz.<sup>4</sup>

IETF RFC 3410 (Dec. 2002), Introduction and Applicability Statements for Internet Standard Management Framework, J. Case, R. Mundy, D. Partain, and B. Stewart.

IETF RFC 3418 (Dec. 2002), Management Information Base (MIB) for the Simple Network Management Protocol (SNMP), R. Presuhn, ed.

ISO 80000-3:2006, Quantities and units — Part 3: Space and time.<sup>5</sup>

<sup>2</sup> IEEE publications are available from The Institute of Electrical and Electronics Engineers (<https://standards.ieee.org>).

<sup>3</sup> The IEEE standards or products referenced in this clause are trademarks owned by The Institute of Electrical and Electronics Engineers, Incorporated.

<sup>4</sup> IETF Requests for Comments (RFCs) are available from the Internet Engineering Task Force (<https://www.rfc-editor.org>).

<sup>5</sup> ISO publications are available from the International Organization for Standardization (<https://www.iso.org>) and the American National Standards Institute (<https://www.ansi.org>).

ITU-T Recommendation G.984.3, Amendment 2, Gigabit-capable Passive Optical Networks (G-PON): Transmission convergence layer specification—Time-of-day distribution and maintenance updates and clarifications.<sup>6</sup>

ITU-T Recommendation G.9960, Unified high-speed wire-line based home networking transceivers—System architecture and physical layer specification [with ITU-T G.9961, commonly referred to as “G.hn”].

ITU-T Recommendation G.9961, Data link layer (DLL) for unified high-speed wire-line based home networking transceivers [with ITU-T G.9960, commonly referred to as “G.hn”].

MoCA<sup>®</sup> MAC/PHY Specification v2.0, MoCA-M/P-SPEC-V2.0-20100507, Multimedia over Coax Alliance (MoCA).<sup>7</sup>

---

<sup>6</sup> ITU-T publications are available from the International Telecommunications Union (<https://www.itu.int>).

<sup>7</sup> MoCA specifications are available from the Multimedia over Coax Alliance (<http://www.mocalliance.org/specs>).

### 3. Definitions

For the purposes of this standard, the following terms and definitions apply. The *IEEE Standards Dictionary Online* should be consulted for terms not defined in this clause.<sup>8</sup>

**3.1 accuracy:** The mean of the time or frequency error between the clock under test and a reference clock over an ensemble of measurements (see IEEE Std 1588-2019).

**3.2 Bridge:** Either a MAC Bridge or a VLAN-aware Bridge, as specified in Clause 5 of IEEE Std 802.1Q-2018.

**3.3 clock:** A physical device that is capable of providing a measurement of the passage of time since a defined epoch.

**3.4 device:** An entity implementing some functionality, e.g., a clock, a time-aware system, a port.

**3.5 direct communication:** A communication of IEEE 802.1AS information between two PTP Instances with no intervening PTP Instance.

**3.6 end station:** A device attached to a local area network (LAN) or metropolitan area network (MAN) that acts as a source of, and/or destination for, traffic carried on the LAN or MAN.

**3.7 event message:** A message that is timestamped on egress from a PTP Instance and ingress to a PTP Instance.

NOTE—See 8.4.3.<sup>9</sup>

**3.8 fractional frequency offset:** The fractional offset,  $y$ , between a measured clock and a reference clock as defined by the following:

$$y = \frac{f_m - f_r}{f_r}$$

where  $f_m$  is the frequency of the measured clock and  $f_r$  is the frequency of the reference clock. The measurement units of  $f_m$  and  $f_r$  are the same.

**3.9 frequency offset:** The offset between a measured frequency and a reference frequency as defined by  $f_m - f_r$ , where  $f_m$  is the frequency of the measured clock and  $f_r$  is the frequency of the reference clock. The measurement units of  $f_m$  and  $f_r$  are the same.

**3.10 general message:** A message that is not timestamped.

**3.11 gPTP communication path:** A segment of a generalized precision time protocol (gPTP) domain that enables direct communication between two PTP Instances.

NOTE—See 8.1.

**3.12 grandmaster-capable PTP Instance:** A PTP Instance that is capable of being a Grandmaster PTP Instance.

<sup>8</sup> *IEEE Standards Dictionary Online* is available at <https://dictionary.ieee.org/>.

<sup>9</sup> Notes in text, tables, and figures of a standard are given for information only and do not contain requirements needed to implement the standard.

**3.13 Grandmaster Clock:** In the context of a single PTP domain, the synchronized time of a PTP Instance that is the source of time to which all other PTP Instances in the domain are synchronized.

**3.14 Grandmaster PTP Instance:** A PTP Instance containing the Grandmaster Clock.

**3.15 local area network (LAN):** A network of devices, whether indoors or outdoors, covering a limited geographic area, e.g., a building or campus.

**3.16 local clock:** A free-running clock, embedded in a respective entity (e.g., PTP Instance, CSN node), that provides a common time to that entity relative to an arbitrary epoch.

**3.17 message timestamp point:** A point within an event message serving as a reference point for when a timestamp is taken.

**3.18 message type:** The message type of a message is the name of the respective message, e.g., Sync, Announce, Timing Measurement Frame.

**3.19 precision:** A measure of the deviation from the mean of the time or frequency error between the clock under test and a reference clock (see IEEE Std 1588-2019).

**3.20 primary reference:** A source of time and/or frequency that is traceable to international standards. *See also: traceability.*

**3.21 PTP End Instance:** A PTP Instance that has exactly one PTP Port.

**3.22 PTP Instance:** An instance of the IEEE 802.1AS protocol, operating in a single time-aware system within exactly one domain. A PTP Instance implements the portions of IEEE Std 802.1AS indicated as applicable to either a PTP Relay Instance or a PTP End Instance.

NOTE—As used in IEEE Std 802.1AS, the term *PTP Instance* refers to an IEEE 1588 PTP Instance that conforms to the requirements of IEEE Std 802.1AS.

**3.23 PTP Link:** Within a domain, a network segment between two PTP Ports using the peer-to-peer delay mechanism of IEEE Std 802.1AS. The peer-to-peer delay mechanism is designed to measure the propagation time over such a link.

NOTE—A PTP Link between PTP Ports of PTP Instances is also a gPTP Communication Path (see 3.11).

**3.24 PTP Relay Instance:** A PTP Instance that is capable of communicating synchronized time received on one PTP Port to other PTP Ports, using the IEEE 802.1AS protocol.

NOTE—A PTP Relay Instance could, for example, be contained in a bridge, a router, or a multi-port end station.

**3.25 recognized timing standard:** A recognized standard time source that is a source external to IEEE 1588 precision time protocol (PTP) and provides time that is traceable to the international standards laboratories maintaining clocks that form the basis for the International Atomic Time (TAI) and Coordinated Universal Time (UTC) timescales. Examples of these sources are National Institute of Standards and Technology (NIST) timeservers and global navigation satellite systems (GNSSs).

**3.26 reference plane:** The boundary between a PTP Port of a PTP Instance and the network physical medium. Timestamp events occur as frames cross this interface.

**3.27 residence time:** The duration of the time interval between the receipt of a time-synchronization event message by a PTP Instance and the sending of the next subsequent time-synchronization event message on another PTP Port of that PTP Instance. Residence time can be different for different PTP Ports. The term *residence time* applies only to the case where *syncLocked* is TRUE.

NOTE 1—See 10.2.5.15 for the definition of the variable `syncLocked`.

NOTE 2—If a PTP Port of a PTP Instance sends a time-synchronization event message without having received a time-synchronization event message, e.g., if `syncLocked` is FALSE or if sync receipt timeout occurs (see 10.7.3.1), the duration of the interval between the most recently received time-synchronization event message and the sent time-synchronization event message is mathematically equivalent to residence time; however, this interval is not normally called a *residence time*.

**3.28 stability (of a clock or clock signal):** A measure of the variations over time of the frequency error (of the clock or clock signal). The frequency error typically varies with time due to aging and various environmental effects, e.g., temperature.

**3.29 synchronized time:** The time of an event relative to the Grandmaster Clock.

NOTE—If there is a change in the Grandmaster PTP Instance or its time base, the *synchronized time* can experience a phase and/or frequency step.

**3.30 synchronized clocks:** Absent relativistic effects, two clocks are synchronized to a specified uncertainty if they have the same epoch and their measurements of the time of a single event at an arbitrary time differ by no more than that uncertainty.

NOTE—See 8.2.2.

**3.31 syntonized clocks:** Absent relativistic effects, two clocks are syntonized to a specified uncertainty if the duration of a second is the same on both, which means the time as measured by each advances at the same rate within the specified uncertainty. The two clocks might or might not share the same epoch.

**3.32 time-aware system:** A device that contains one or more PTP Instances and/or PTP services (e.g., Common Mean Link Delay Service).

NOTE 1—See 11.2.17 for a description of the Common Mean Link Delay Service.

NOTE 2—A time-aware system can contain more than one PTP Instance in the same domain and/or different domains.

**3.33 timestamp measurement plane:** The plane at which timestamps are captured. If the timestamp measurement plane is different from the reference plane, the timestamp is corrected for ingressLatency and/or egressLatency. *See: reference plane.*

NOTE—For timestamping on egress and ingress, see 8.4.3.

**3.34 traceability:** See 3.1.81 in IEEE Std 1588-2019.

## 4. Acronyms and abbreviations

|        |                                                                                   |
|--------|-----------------------------------------------------------------------------------|
| Ack    | acknowledgment                                                                    |
| ADEV   | Allan deviation                                                                   |
| ARB    | arbitrary                                                                         |
| BC     | Boundary Clock                                                                    |
| BMC    | best master clock                                                                 |
| BMCA   | best master clock algorithm                                                       |
| CID    | Company identification (allocated by the IEEE)                                    |
| CMLDS  | Common Mean Link Delay Service                                                    |
| CSN    | coordinated shared network                                                        |
| CTC    | channel time clock                                                                |
| EPON   | IEEE 802.3™ Ethernet Passive Optical Network, as specified in IEEE Std 802.3-2018 |
| FTM    | Fine Timing Measurement                                                           |
| G.hn   | ITU-T G.9960 and ITU-T G.9961                                                     |
| GM     | Grandmaster                                                                       |
| GMT    | Greenwich mean time                                                               |
| GNSS   | global navigation satellite system                                                |
| GPS    | global positioning (satellite) system                                             |
| gPTP   | generalized precision time protocol (IEEE Std 802.1AS)                            |
| IERS   | International Earth Rotation and Reference Systems Service                        |
| IP     | Internet Protocol                                                                 |
| ISS    | Internal Sublayer Service                                                         |
| LAN    | local area network                                                                |
| LCI    | location configuration information                                                |
| LLC    | logical link control                                                              |
| MAC    | media access control                                                              |
| MACsec | media access control security                                                     |

|        |                                                                                  |
|--------|----------------------------------------------------------------------------------|
| MIB    | Management Information Base                                                      |
| MLME   | IEEE 802.11™ MAC layer management entity                                         |
| MPCP   | IEEE 802.3 multipoint control protocol                                           |
| MPCPDU | IEEE 802.3 MPCP data unit                                                        |
| MD     | media-dependent                                                                  |
| NMS    | Network Management System                                                        |
| NTP    | network time protocol <sup>10</sup>                                              |
| OLT    | IEEE 802.3 optical line terminal                                                 |
| ONU    | IEEE 802.3 optical network unit                                                  |
| OSSP   | organization-specific slow protocol                                              |
| OUI    | Organizationally Unique Identifier                                               |
| P2P    | peer-to-peer                                                                     |
| PICS   | Protocol Implementation Conformance Statement                                    |
| POSIX® | portable operating system interface (see ISO/IEC 9945:2003 [B17] <sup>11</sup> ) |
| PTP    | IEEE 1588 precision time protocol                                                |
| PTPDEV | PTP deviation                                                                    |
| RTT    | round-trip time                                                                  |
| SI     | international system of units                                                    |
| SMI    | Structure of Management Information                                              |
| SMIv2  | Structure of Management Information version 2                                    |
| SNMP   | Simple Network Management Protocol                                               |
| STA    | station                                                                          |
| TAI    | International Atomic Time                                                        |
| TC     | Transparent Clock                                                                |
| TDEV   | time deviation                                                                   |

<sup>10</sup> Information available at <https://www.ietf.org/rfc/rfc1305.txt>.

<sup>11</sup> The numbers in brackets correspond to the numbers in the bibliography in Annex G.

|      |                               |
|------|-------------------------------|
| TDM  | time division multiplexing    |
| TDMA | time division multiple access |
| TLV  | type, length, value           |
| TM   | Timing Measurement            |
| UCT  | unconditional transfer        |
| UTC  | Coordinated Universal Time    |
| VLAN | virtual local area network    |
| WLAN | wireless local area network   |

## 5. Conformance

This clause specifies the mandatory and optional capabilities provided by conformant implementations of this standard.

### 5.1 Requirements terminology

For consistency with existing IEEE and IEEE 802.1 standards, requirements placed upon conformant implementations of this standard are expressed using the following terminology:

- a) **shall** is used for mandatory requirements.
- b) **may** is used to describe implementation or administrative choices (“may” means “is permitted to,” and hence, “may” and “may not” mean precisely the same thing).
- c) **should** is used for recommended choices (the behaviors described by “should” and “should not” are both permissible but not equally desirable choices).

The Protocol Implementation Conformance Statement (PICS) proforma (see Annex A) reflects the occurrences of the words shall, may, and should within the standard. The words shall, may, and should, as used in Annex A itself, reflect the use of the PICs and not conformance to the standard.

The standard avoids needless repetition and apparent duplication of its formal requirements by using **is**, **is not**, **are**, and **are not** for definitions and the logical consequences of conformant behavior. Behavior that is permitted but is neither always required nor directly controlled by an implementer or administrator, or whose conformance requirement is detailed elsewhere, is described by **can**. Behavior that never occurs in a conformant implementation or system of conformant implementations is described by **cannot**. The word **allow** is used as a replacement for the phrase “support the ability for,” and the word **capability** means “can be configured to.”

### 5.2 Protocol Implementation Conformance Statement (PICS)

The supplier of an implementation that is claimed to conform to this standard shall complete a copy of the PICS proforma provided in Annex A and shall provide the information necessary to identify both the supplier and the implementation.

### 5.3 Time-aware system requirements

An implementation of a time-aware system shall support at least one IEEE 1588 precision time protocol (PTP) Instance.

### 5.4 PTP Instance requirements and options

#### 5.4.1 Summary of requirements

An implementation of a PTP Instance shall:

- a) Implement the generalized precision time protocol (gPTP) requirements specified in Clause 8.
- b) Support the requirements for time-synchronization state machines (10.1.2, 10.2.1, 10.2.2, 10.2.3, 10.2.4, 10.2.5, and 10.2.6).
- c) Support at least one PTP Port.
- d) On each supported PTP Port, implement the PortSyncSyncReceive state machine (10.2.8).

- e) Implement the ClockSlaveSync state machine (10.2.13).
- f) Support the following best master clock algorithm (BMCA) requirements:
  - 1) Implement the BMCA (10.3.2, 10.3.3, 10.3.4, 10.3.5, 10.3.6, 10.3.8, and 10.3.10).
  - 2) For domain 0, implement specifications for externalPortConfigurationEnabled value of FALSE (10.3.1).
  - 3) Implement the PortAnnounceReceive state machine (10.3.11).
  - 4) Implement the PortAnnounceInformation state machine (10.3.12).
  - 5) Implement the PortStateSelection state machine (10.3.13).
  - 6) Have the BMCA as the default mode of operation, with externalPortConfiguration FALSE, on domain 0.
  - 7) Implement at least one of the possibilities for externalPortConfigurationEnabled (i.e., FALSE, meaning the BMCA is used, and TRUE, meaning external port configuration is used) on domains other than domain 0.
- g) Implement the SiteSyncSync state machine (10.2.7).
- h) Implement the state machines related to signaling gPTP capability (10.4).
- i) For receipt of all messages and for transmission of all messages except Announce (see 10.6.3) and Signaling (see 10.6.4), support the message requirements as specified in 10.5, 10.6, and 10.7.
- j) Support the performance requirements in B.1 and B.2.4.

#### 5.4.2 PTP Instance options

An implementation of a PTP Instance should:

- a) Support the performance requirements in B.2.2 and B.2.3.

An implementation of a PTP Instance may:

- b) Support the following media-independent master capability on at least one PTP Port:
  - 1) Implement the PortSyncSyncSend state machine (10.2.12).
  - 2) Implement the PortAnnounceTransmit state machine (10.3.16).
  - 3) Implement the AnnounceIntervalSetting state machine (10.3.17).
  - 4) For transmit of the Announce message, support the message requirements as specified in 10.5, 10.6, and 10.7.
- c) Support the following for Grandmaster PTP Instance capability:
  - 1) Support the media-independent master capability specified in item b) of 5.4.2.
  - 2) Support the requirements for a grandmaster-capable PTP Instance (10.1.3).
  - 3) Implement the ClockMasterSyncSend state machine (10.2.9).
  - 4) Implement the ClockMasterSyncOffset state machine (10.2.10).
  - 5) Implement the ClockMasterReceive state machine (10.2.11).
- d) Support more than one PTP Port as a PTP Relay Instance (5.4.3).
- e) Support transmit of the Signaling message according to the message requirements as specified in 10.5 and 10.6.
- f) Support more than one PTP Instance; such support allows for more than one domain (7.2.3).

- g) Support the following external port configuration capability on at least one port:
  - 1) Implement specifications for externalPortConfigurationEnabled value of true (10.3.1).
  - 2) Implement the PortAnnounceInformationExt state machine (10.3.14).
  - 3) Implement the PortStateSettingExt state machine (10.3.15).
- h) Implement the SyncIntervalSetting state machine (10.3.18).
- i) Implement one or more of the application interfaces specified in Clause 9; A PTP Instance that claims to support application interfaces shall state which application interfaces are supported.
- j) Support timing and synchronization management as specified in Clause 14.
- k) Support the use of a remote management protocol. A PTP Instance that claims to support remote management shall:
  - 1) State which remote management protocol standard(s) or specification(s) are supported (see A.19).
  - 2) State which standard(s) or specification(s) for managed object definitions and encodings are supported for use by the remote management protocol (see A.19).
  - 3) If the Simple Network Management Protocol (SNMP) is supported as a remote management protocol, support the managed object definitions specified as Structure of Management Information version 2 (SMIv2) Management Information Base (MIB) modules in Clause 15.
- l) Implement both BMCA and external port configuration on domains other than domain 0; if both possibilities are implemented on domains other than domain 0, the default value of externalPortConfigurationEnabled shall be FALSE.

## 5.4.3 PTP Relay Instance requirements

An implementation of a PTP Relay Instance shall:

- a) Support more than one PTP Port.
- b) Support the PTP Instance requirements specified in 5.4.
- c) Support the media-independent master capability specified in item b) of 5.4.2.

## 5.5 MAC-specific timing and synchronization methods for full-duplex IEEE 802.3 links

An implementation of a time-aware system with IEEE 802.3 media access control (MAC) services to physical ports shall:

- a) Support full-duplex operation, as specified in 4.2 and Annex 4A of IEEE Std 802.3-2018.
- b) Support the requirements as specified in Clause 11.
- c) Implement the SyncIntervalSetting state machine (10.3.18).

An implementation of a PTP Instance with IEEE 802.3 MAC services to physical ports may:

- d) Support asymmetry measurement mode as specified in 10.3.12, 10.3.13, 10.3.16, 11.2.14, 11.2.15, 11.2.19, and 14.8.45.
- e) Support one-step capability on receive as specified in 11.2.14.
- f) Support one-step capability on transmit as specified in 11.2.15.
- g) Support the OneStepTxOperSetting state machine specified in 11.2.16.
- h) Support propagation delay averaging, as specified in 11.2.19.3.4.

## 5.6 MAC-specific timing and synchronization methods for IEEE Std 802.11-2016

An implementation of a time-aware system with IEEE 802.11 MAC services to physical ports shall:

- a) Support the requirements as specified in Clause 12.
- b) Support at least one of
  - 1) the media-dependent master state machines (12.5.1), or
  - 2) the media-dependent slave state machine (12.5.2).

An implementation of a PTP End Instance with IEEE 802.11 MAC services to physical ports shall:

- c) Support at least one of TIMINGMSMT as specified in IEEE Std 802.11-2016 or FINETIMINGMSMT as specified in IEEE Std 802.11-2016.

An implementation of a PTP Relay Instance with IEEE 802.11 MAC services to physical ports shall:

- d) Support the requirements of TIMINGMSMT as specified in IEEE Std 802.11-2016.

An implementation of a PTP Relay Instance with IEEE 802.11 MAC services to physical ports should:

- e) Support FINETIMINGMSMT as specified in IEEE Std 802.11-2016.

NOTE—In order to maintain backward compatibility with existing TM-based PTP End Instances that support only Timing Measurement (TM), the PTP Relay Instance is required to support TM. PTP End Instances are allowed to support TM or Fine Timing Measurement (FTM), or both to permit PTP End Instances compliant with this standard to implement only the FTM standard, which requires a PTP Relay Instance that supports FTM.

## 5.7 MAC-specific timing and synchronization methods for IEEE 802.3 EPON

An implementation of a time-aware system with IEEE 802.3 Ethernet Passive Optical Network (EPON) MAC services to physical ports shall:

- a) Support the requirements as specified in IEEE Std 802.3-2018 for multipoint MAC Control (64.2 and 64.3) and multipoint physical coding sublayer (PCS) and physical medium attachment (PMA) extensions (Clause 65).
- b) Support the requirements as specified in Clause 13.

## 5.8 MAC-specific timing and synchronization methods for coordinated shared network (CSN)

An implementation of a time-aware system with CSN MAC services to physical ports shall:

- a) Support the requirements as specified in Clause 16.
- b) Support at least one MoCA port (16.6.2) or ITU-T G.hn port (16.6.3).

## 6. Conventions

### 6.1 General

This clause defines various conventions and notation used in the standard, i.e., naming conventions, service specification method and notation, and data type definitions.

### 6.2 Service specification method and notation

The method and notation for specifying service interfaces is described in Clause 7 of IEEE Std 802.1AC-2016.

### 6.3 Lexical form syntax

A lexical form refers to the following:

- A name
- A data type

The conventions illustrated in the following list regarding lexical forms are used in this standard:

- a) Type names: e.g., ClockQuality (no word separation, initial letter of each word capitalized).
- b) Enumeration members and global constants: e.g., ATOMIC\_CLOCK (word separation underscored, all letters capitalized).
- c) Fields within PTP messages, instances of structures, and variables: e.g., secondsField, clockQuality, clockIdentity (two-word field names at a minimum, no word separation, initial word not capitalized, initial letter capitalization on subsequent words).
- d) Members of a structure: e.g., clockQuality.clockClass (no word separation, structure name followed by a period followed by the member name).
- e) Data set names: e.g., defaultDS, parentDS, portDS, currentDS, timePropertiesDS (no word separation, initial word not capitalized, initial letter capitalization on subsequent words, end marked by the letters DS).
- f) Data set members: e.g., defaultDS.clockQuality.clockClass (data set name followed by a period followed by a member name followed by a period followed by a member name).
- g) PTP message names: e.g., Sync, Pdelay\_Req (word separation underscored, initial letter of each word capitalized).

When a lexical form appears in text, as opposed to in a type, or a format definition, the form is to be interpreted as singular, plural, or possessive as appropriate to the context of the text.

### 6.4 Data types and on-the-wire formats

#### 6.4.1 General

The data types specified for the various variables and message fields define logical properties that are necessary for correct operation of the protocol or interpretation of IEEE 1588 precision time protocol (PTP) or IEEE 802.11 message content.

NOTE—Implementations are free to use any internal representation of data types if the internal representation does not change the semantics of any quantity visible via communications using the IEEE 802.1AS protocol or in the specified operations of the protocol.

### 6.4.2 Primitive data types specifications

All non-primitive data types are derived from the primitive types in Table 6-1. Signed integers are represented in two's complement form.

**Table 6-1—Primitive data types**

| Data type                                                                                                                          | Definition                                                         |
|------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------|
| Boolean                                                                                                                            | TRUE or FALSE                                                      |
| EnumerationN                                                                                                                       | N-bit enumerated value                                             |
| UIntegerN                                                                                                                          | N-bit unsigned integer                                             |
| IntegerN                                                                                                                           | N-bit signed integer                                               |
| Nibble                                                                                                                             | 4-bit field not interpreted as a number                            |
| Octet                                                                                                                              | 8-bit field not interpreted as a number                            |
| OctetN                                                                                                                             | N-octet field not interpreted as a number, with $N > 1$            |
| Float64 (see NOTE)                                                                                                                 | IEEE 754™ binary64 (64-bit double-precision floating-point format) |
| NOTE—The Float64 data type was called Double in the 2011 edition of this standard. The semantics of the data type has not changed. |                                                                    |

### 6.4.3 Derived data type specifications

#### 6.4.3.1 ScaledNs

The ScaledNs type represents signed values of time and time interval in units of  $2^{-16}$  ns.

typedef Integer96 ScaledNs;

For example:  $-2.5$  ns is expressed as:

0xFFFF FFFF FFFF FFFF FFFD 8000

Positive or negative values of time or time interval outside the maximum range of this data type are encoded as the largest positive or negative value of the data type, respectively.

#### 6.4.3.2 UScaledNs

The UScaledNs type represents unsigned values of time and time interval in units of  $2^{-16}$  ns.

typedef UInteger96 UScaledNs;

For example:  $2.5$  ns is expressed as:

0x0000 0000 0000 0000 0002 8000

Values of time or time interval greater than the maximum value of this data type are encoded as the largest positive value of the data type.

#### 6.4.3.3 TimeInterval

The TimeInterval type represents time intervals, in units of  $2^{-16}$  ns.

```
typedef Integer64 TimeInterval;
```

For example: 2.5 ns is expressed as:

```
0x0000 0000 0002 8000
```

Positive or negative time intervals outside the maximum range of this data type are encoded as the largest positive and negative values of the data type, respectively.

#### 6.4.3.4 Timestamp

The Timestamp type represents a positive time with respect to the epoch.

```
struct Timestamp
{
    UInteger48 seconds;
    UInteger32 nanoseconds;
};
```

The seconds member is the integer portion of the timestamp in units of seconds.

The nanoseconds member is the fractional portion of the timestamp in units of nanoseconds.

The nanoseconds member is always less than  $10^9$ .

For example:

+2.000000001 seconds is represented by seconds = 0x0000 0000 0002 and nanoseconds= 0x0000 0001

#### 6.4.3.5 ExtendedTimestamp

The ExtendedTimestamp type represents a positive time with respect to the epoch.

```
struct ExtendedTimestamp
{
    UInteger48 seconds;
    UInteger48 fractionalNanoseconds;
};
```

The seconds member is the integer portion of the timestamp in units of seconds.

The fractionalNanoseconds member is the fractional portion of the timestamp in units of  $2^{-16}$  ns.

The fractionalNanoseconds member is always less than  $(2^{16})(10^9)$ .

For example:

+2.000000001 seconds is represented by seconds = 0x0000 0000 0002 and fractionalNanoseconds = 0x0000 0001 0000

**6.4.3.6 ClockIdentity**

The ClockIdentity type identifies a PTP Instance.

```
typedef Octet8 ClockIdentity;
```

**6.4.3.7 PortIdentity**

The PortIdentity type identifies a port of a PTP Instance.

```
struct PortIdentity
{
    ClockIdentity clockIdentity;
    UInteger16 portNumber;
};
```

**6.4.3.8 ClockQuality**

The ClockQuality represents the quality of a clock.

```
struct ClockQuality
{
    UInteger8 clockClass;
    Enumeration8 clockAccuracy;
    UInteger16 offsetScaledLogVariance;
};
```

**6.4.4 Protocol data unit (PDU) formats****6.4.4.1 General**

The data types defined in 6.4.2 and 6.4.3 shall be mapped onto the wire according to the mapping rules for the respective medium, e.g., IEEE Std 802.3-2018 and IEEE Std 802.11-2016, and the terms of 6.4.4.

IEEE 802.1AS PDUs consist of the messages defined or referenced in Clause 10, Clause 11, Clause 12, and Clause 13, based on the data types defined in 6.4.2 and 6.4.3. The internal ordering of the fields of the IEEE 802.1AS PDUs is specified in 6.4.4.3 to 6.4.4.5.

**6.4.4.2 Numbering of bits within an octet**

Bits are numbered with the most significant bit being 7 and the least significant bit being 0.

NOTE—The numbering and ordering of bits within an octet of a PDU, described here, is independent of and unrelated to the order of transmission of the bits on the underlying physical layer.

**6.4.4.3 Primitive data types**

Numeric primitive data types defined in 6.4.2 shall be formatted with the most significant octet nearest to the beginning of the PDU followed in order by octets of decreasing significance.

The Boolean data type TRUE shall be formatted as a single bit equal to 1 and FALSE as a single bit equal to 0.

Enumerations of whatever length shall be formatted as though the assigned values are unsigned integers of the same length, e.g., Enumeration16 shall be formatted as though the value had type UInteger16.

#### 6.4.4.4 Arrays of primitive types

All arrays shall be formatted with the member having the lowest numerical index nearest to the beginning of the PDU followed by successively higher numbered members, without any padding. In octet arrays, the octet with the lowest numerical index is termed the most significant octet.

When a field containing more than one octet is used to represent a numeric value, the most significant octet shall be nearest to the beginning of the PDU, followed by successively less significant octets.

When a single octet contains multiple fields of primitive data types, the bit positions within the octet of each of the primitive types as defined in the message field specification shall be preserved. For example, the first field of the header of PTP messages is a single octet composed of two fields, one of type Nibble bit 4 through bit 7, and one of type Enumeration4 bit 0 through bit 3 (see 11.4.2 and 10.6.2).

#### 6.4.4.5 Derived data types

Derived data types defined as structs shall be formatted with the first member of the struct nearest to the beginning of the PDU followed by each succeeding member, without any padding. Each member shall be formatted according to its data type.

Derived data types defined as typedefs shall be formatted according to its referenced data type.

## 7. Time-synchronization model for a packet network

### 7.1 General

This clause provides a model for understanding the operation of the generalized precision time protocol (gPTP), which specifies the operation of time-aware systems on a packet network. Although this standard is based on the precision time protocol (PTP) described in IEEE Std 1588-2019 (and, indeed, is a proper profile of IEEE Std 1588-2019 in particular configurations), there are differences, which are summarized in 7.5.

Although this standard has been written as a stand-alone document, it is useful to understand the IEEE 1588 architecture as described in Clause 6 of IEEE Std 1588-2019.

### 7.2 Architecture of a time-aware network

#### 7.2.1 General

A time-aware network consists of a number of interconnected time-aware systems that support the gPTP defined within this standard. These time-aware systems can be any networking device, including, for example, bridges, routers, and end stations. A set of time-aware systems that are interconnected by gPTP-capable network elements is called a *gPTP network*. Each instance of gPTP that the time-aware systems support is in one *gPTP domain*, and the instances of gPTP are said to be part of that gPTP domain. A time-aware system can support, and therefore be part of, more than one gPTP domain. The entity of a single time-aware system that executes gPTP in one gPTP domain is called a *PTP Instance*. A time-aware system can contain multiple PTP Instances, which are each associated with a different gPTP domain. There are two types of PTP Instances:

- a) PTP End Instance, which, if not a Grandmaster PTP Instance, is a recipient of time information, and
- b) PTP Relay Instance, which, if not a Grandmaster PTP Instance, receives time information from the Grandmaster PTP Instance (perhaps indirectly through other PTP Relay Instances), applies corrections to compensate for delays in the local area network (LAN) and the PTP Relay Instance itself, and retransmits the corrected information.

This standard defines mechanisms for delay measurements using standard-based procedures for the following:

- c) IEEE 802.3 Ethernet using full-duplex point-to-point links (11)
- d) IEEE 802.3 EPON links (Clause 13)
- e) IEEE 802.11 wireless (Clause 12)
- f) Generic coordinated shared networks (CSNs, e.g., MoCA and G.hn) (Clause 16)

### 7.2.2 Time-aware network consisting of a single gPTP domain

Figure 7-1 illustrates an example time-aware network consisting of a single gPTP domain, using all the above network technologies [i.e., item c) through item f) of 7.2.1], where end stations on several local networks are connected to a Grandmaster PTP Instance on a backbone network via an EPON access network.

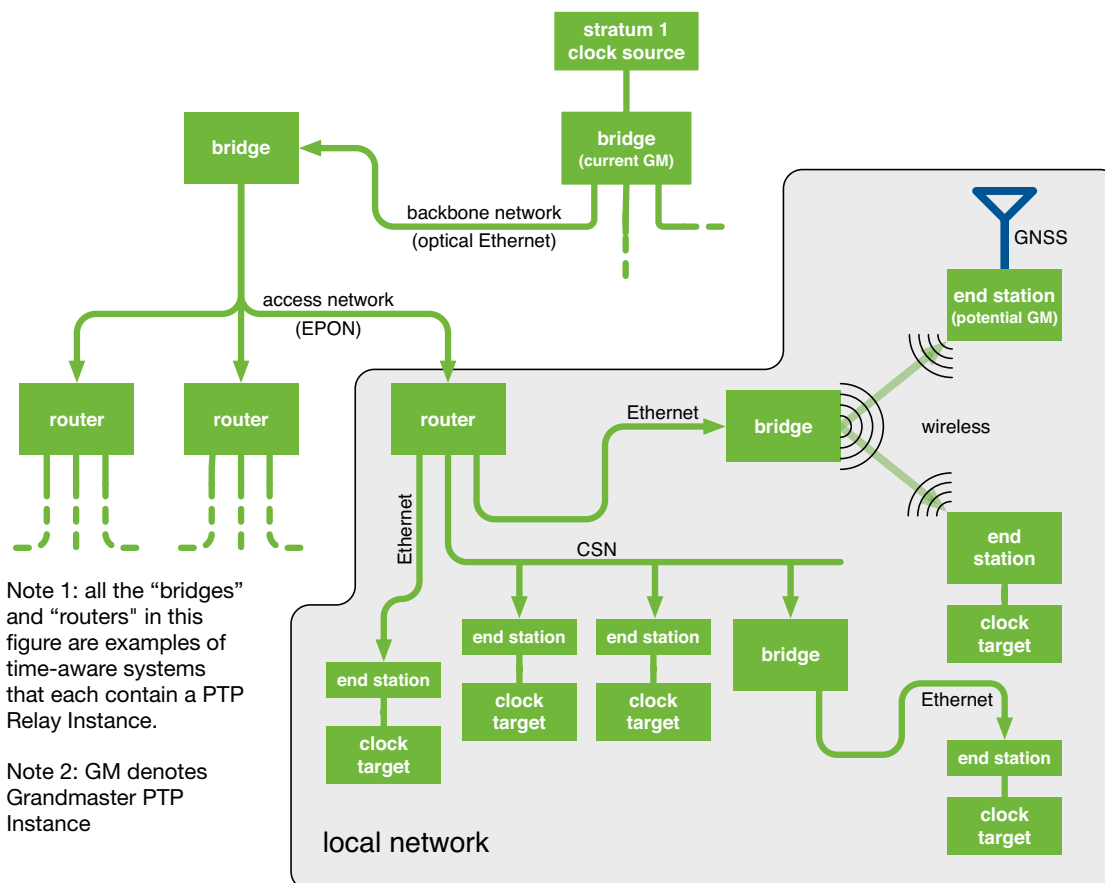


Figure 7-1—Time-aware network example

Any PTP Instance with clock sourcing capabilities can be a potential Grandmaster PTP Instance, and a selection method (the *best master clock algorithm*, or BMCA) ensures that all of the PTP Instances in a gPTP domain use the same Grandmaster PTP Instance.<sup>12</sup> The BMCA is largely identical to that used in IEEE Std 1588-2019, but somewhat simplified. In Figure 7-1 the BMCA process has resulted in the Grandmaster PTP Instance being on the network backbone. If, however, the access network fails, the systems on a local network automatically switch over to one of the potential Grandmaster PTP Instances on the local network that is as least as “good” as any other. For example, in Figure 7-2, the access network link has failed, and a potential Grandmaster PTP Instance that has a GNSS reference source has become the active Grandmaster PTP Instance. As a result, now two gPTP domains exist where there used to be one. Finally, note that when a time-aware system supports more than one domain, one of the domains supported must be domain 0 for backward compatibility with the 2011 edition of this standard, though domain 0 is not necessarily active in a time-aware system.

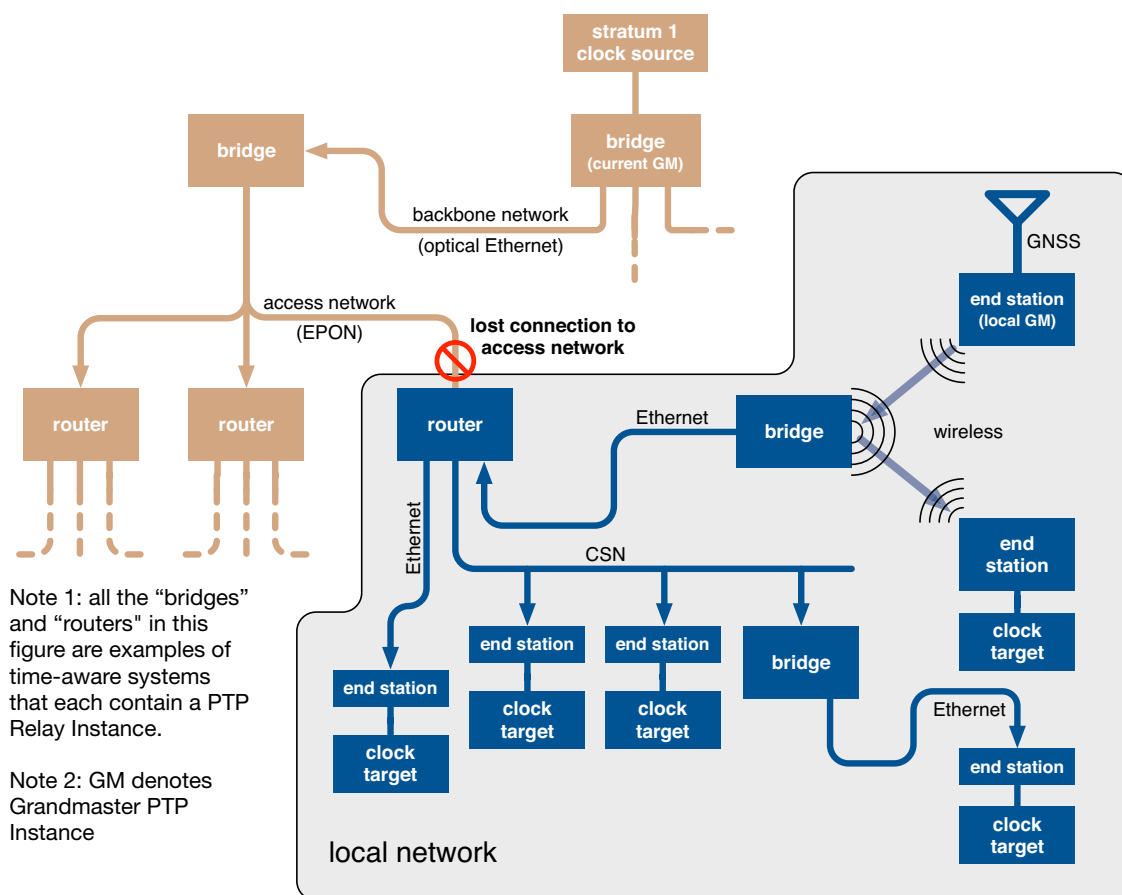


Figure 7-2—Time-aware network of Figure 7-1 after an access network link failure

<sup>12</sup> There are, however, short periods during network reconfiguration when more than one Grandmaster PTP Instance might be active while the BMCA process is taking place.

### 7.2.3 Time-aware network consisting of multiple gPTP domains

Figure 7-3 illustrates an example time-aware network consisting of multiple gPTP domains that could be used in an industrial application. Specifically, in this example the network has two timescales/domains, where domain 0 uses the PTP timescale and domain 1 uses the arbitrary (ARB) timescale (see 8.2). Notice that not all PTP Instances in domain 1 (within the blue shorter-dashed area) have domain 0 active in this example, even though every time-aware system supports domain 0 for backward compatibility with the 2011 edition of this standard. In addition, it is required that all PTP Instances belonging to the same domain have direct connections among them in their physical topology (e.g., time cannot be transported from one PTP Instance in domain 0 to another PTP Instance in domain 0 via a time-aware system that does not have domain 0 active). In addition, the time-aware systems for which both domains are active are depicted by slanted internal hatching, representing two independent, active PTP Instances.

As in the single-domain case, any of the network technologies of 7.2.1 can be used. The Grandmaster PTP Instance of each domain is selected by the BMCA; in this case, a separate, independent instance of the BMCA is invoked in each domain.

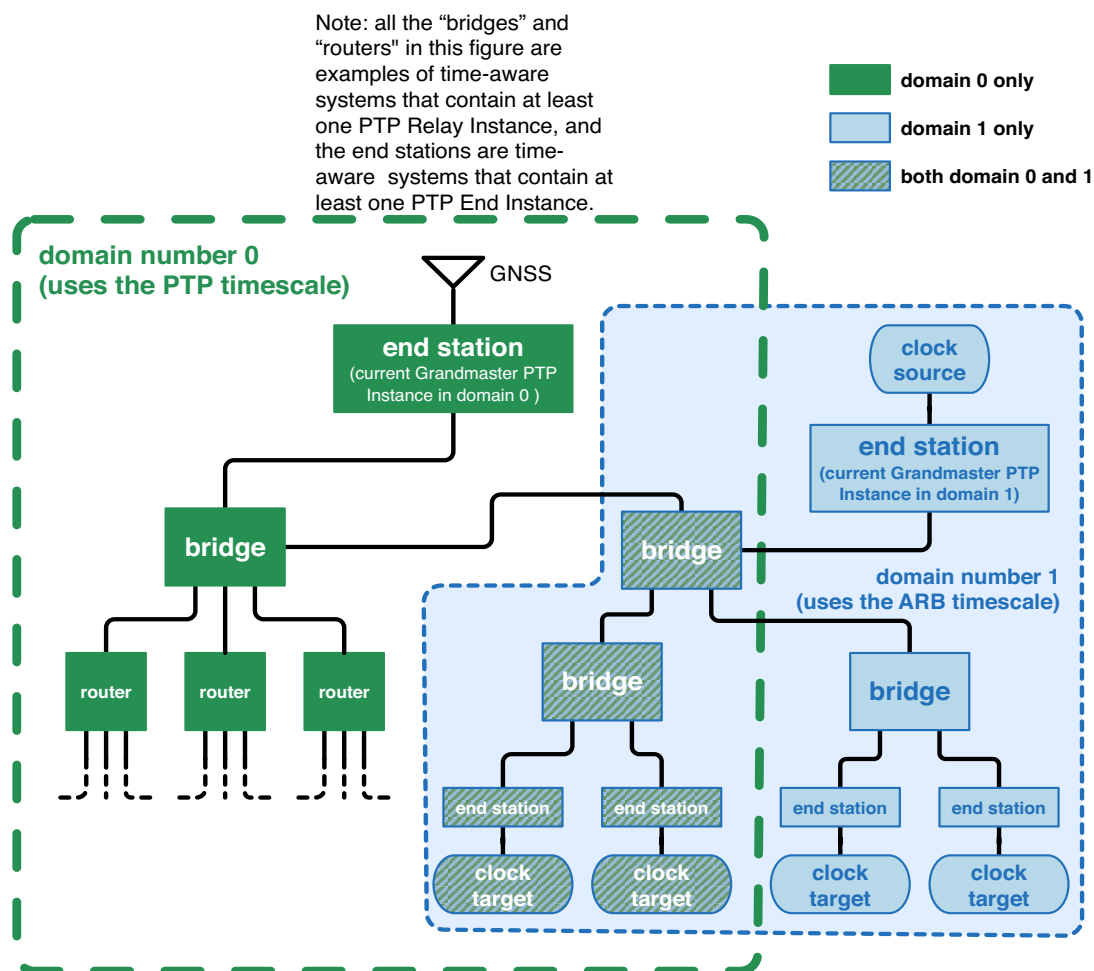


Figure 7-3—Time-aware network example for multiple gPTP domains

**7.2.4 Time-aware networks with redundant Grandmaster PTP Instances and/or redundant paths****7.2.4.1 General**

Redundancy has many levels of sophistication, performance, and cost. Therefore, the appropriate level and/or amount of redundancy required in a time-aware network can be very different for each application. Nonetheless, all solutions for redundancy consist of a detection component, a correction component, and an action component. The detection component detects that something is not working correctly. The correction component determines the appropriate corrective action. The action component performs the required action(s) to fix the detected problem.

**7.2.4.2 Redundancy specified in this standard (BMCA)**

This standard provides a basic level of redundancy as follows:

- A detection component that triggers when the current Grandmaster PTP Instance stops working (i.e., loss of Sync messages and Announce messages for a period of time) or if the link to the Grandmaster PTP Instance goes down (i.e., immediate loss of Sync messages and Announce messages).
- A correction component that triggers the Best Master Clock Algorithm (BMCA) and the sending of Announce messages so that a new Grandmaster PTP Instance can be elected.
- An action component, where the winning Grandmaster PTP Instance starts sending Announce messages and Sync messages and all the PTP Instances listen to this new Grandmaster PTP Instance.

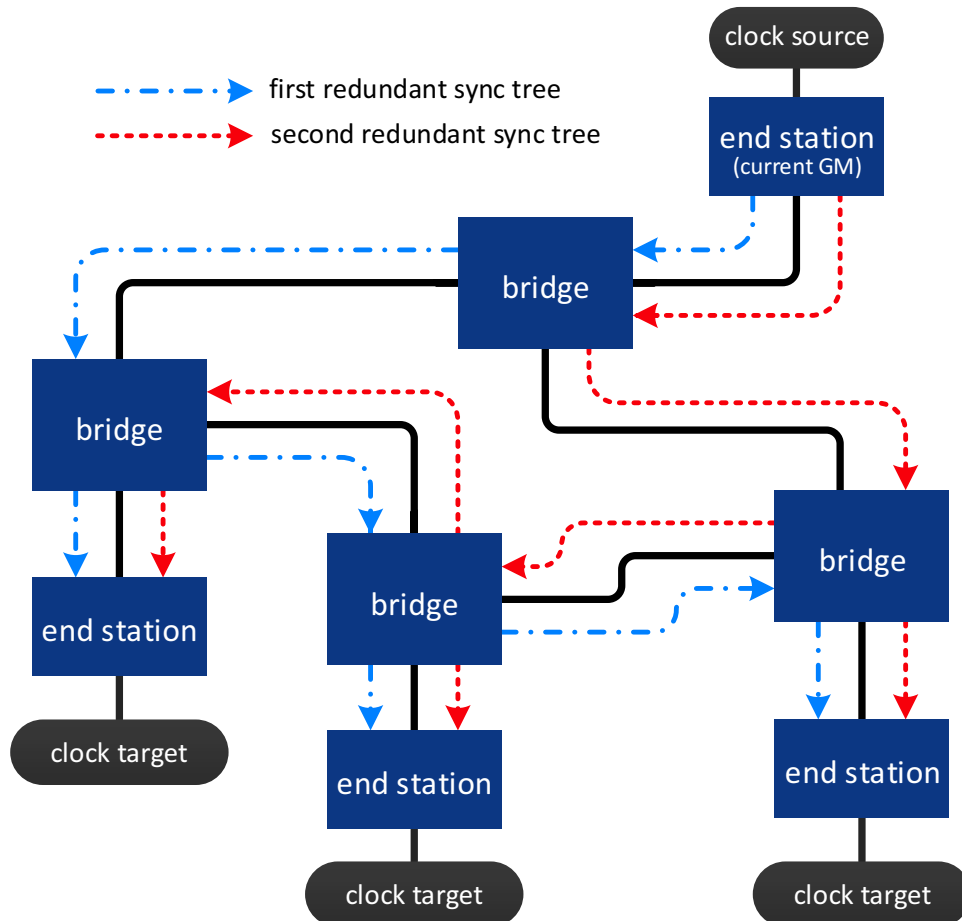
**7.2.4.3 Redundancy not fully specified in this standard**

In addition to providing the basic level of redundancy, this standard provides the ability to support more sophisticated network configurations that provide additional levels of Grandmaster PTP Instance and clock path redundancy. Figure 7-4 through Figure 7-6 are examples of such networks that provide these additional levels of redundancy as a way to deal with these failures. The information necessary to implement and configure these network configurations is contained in this standard.

In order to take advantage of these failure correction configurations, new types of fault detection are required. The category of fault detection where a Grandmaster PTP Instance completely fails and stops sending clock information is supported as mentioned above.

Other types of faults involve instability of the Grandmaster Clock, such as time glitches, excess jitter, or wander, or various other impairments that could occur in the Grandmaster Clock. Techniques for identifying these types of failures, and the appropriate correction necessary, are not specified in this standard. However, if other techniques or standards are used for detection and correction of these types of failures, this standard provides the means to recover from these errors.

Figure 7-4 shows an example network realizing two redundant synchronization trees from a single GM, with each synchronization tree in a different gPTP domain (there are a total of two gPTP domains).



Note 1: The methods used for merging the redundant sync msgs received at each end station are not specified in this standard.

Note 2: All the “bridges” in this figure are examples of time-aware systems that contain PTP Relay Instances, and the end stations are examples of time-aware systems that contain PTP End Instances.

Note 3: GM denotes Grandmaster PTP Instance

**Figure 7-4—Time-aware network example for synchronization path redundancy, with one clock source providing time to two domains**

The diagram illustrates the clock distribution architecture for two GMs (current and hot-standby) connected via bridges to multiple end stations.

**Legend:**

- .-.-> sync tree of the primary GM
- .-.-> sync tree of the hot standby GM

**Architecture Details:**

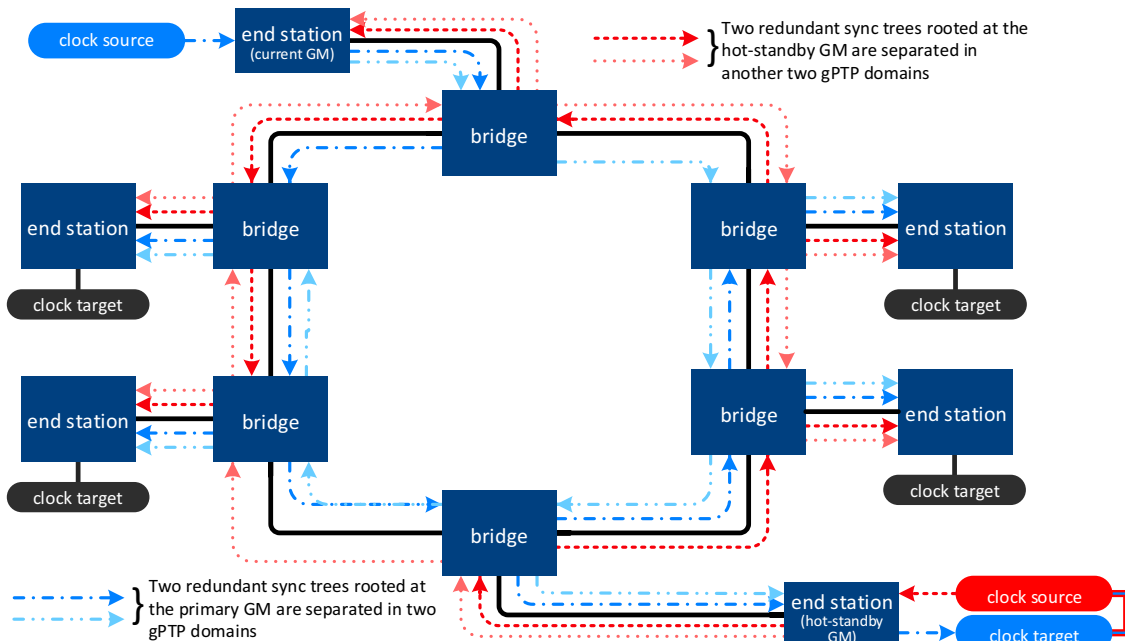
- Primary GM (Left):**
  - clock source:** A blue rounded rectangle at the top.
  - end station (current GM):** A blue rectangle below the clock source.
  - bridge:** A blue rectangle below the end station.
  - end stations:** Two blue rectangles below the bridge.
  - clock targets:** Two black rounded rectangles at the bottom, each connected to an end station.
- Hot-standby GM (Right):**
  - clock source:** A red rounded rectangle at the top.
  - clock target:** A blue rounded rectangle to the left of the clock source.
  - end station (hot-standby GM):** A blue rectangle below the clock source.
  - bridge:** A blue rectangle below the end station.
  - end stations:** Two blue rectangles below the bridge.
  - clock targets:** Two black rounded rectangles at the bottom, each connected to an end station.
- Interconnections:**
  - Primary GM Sync Tree (Blue dashed lines):**
    - From **clock source** to **end station (current GM)**.
    - From **end station (current GM)** to **bridge**.
    - From **bridge** to both **end stations**.
    - From each **end station** to its respective **clock target**.
    - From **bridge** to the **clock target** of the hot-standby GM.
  - Hot-standby GM Sync Tree (Red dashed lines):**
    - From **clock source** to **clock target**.
    - From **clock target** to **end station (hot-standby GM)**.
    - From **end station (hot-standby GM)** to **bridge**.
    - From **bridge** to both **end stations**.
    - From each **end station** to its respective **clock target**.
    - From **bridge** to the **clock target** of the primary GM.

Note 3: GM denotes Grandmaster PTP Instance

**AVA** Documents  
Document Sharing Hub

Figure 7-6 shows another example network, which involves ring topology, using the redundancy features of both Figure 7-4 and Figure 7-5.

For the techniques shown in the examples of Figure 7-4, Figure 7-5, and Figure 7-6, the detection component, correction component, and action component are not fully specified in this standard.



Note 1: The methods used for merging the redundant sync msgs received at each end station are not specified in this standard.

Note 2: All the “bridges” in this figure are examples of time-aware systems that contain PTP Relay Instances, and the end stations are examples of time-aware systems that contain PTP End Instances.

Note 3: GM denotes Grandmaster PTP Instance

**Figure 7-6—Time-aware network example for GM+synchronization path redundancy, with one primary and one hot-standby GM. Each GM establishes two sync trees, resulting in a total of four Sync trees that are separated in four gPTP domains**

## 7.3 Time synchronization

### 7.3.1 General

Time synchronization in gPTP is done the same way (in the abstract) as is done in IEEE Std 1588-2019: a Grandmaster PTP Instance sends information including the current synchronized time to all directly attached PTP Instances. Each of these PTP Instances must correct the received synchronized time by adding the propagation time needed for the information to transit the gPTP communication path from the Grandmaster PTP Instance. If the PTP Instance is a PTP Relay Instance, then it must forward the corrected time information (including additional corrections for delays in the forwarding process) to all the other attached PTP Instances.

To make this all work, there are two time intervals that must be precisely known: the forwarding delay (called the *residence time*), and the time taken for the synchronized time information to transit the gPTP communication path between two PTP Instances. The residence time measurement is local to a PTP Relay Instance and easy to compute, while the gPTP communication path delay is dependent on many things including media- dependent properties and the length of the path.

### 7.3.2 Delay measurement

Each type of LAN or gPTP communication path has different methods for measuring propagation time, but they are all based on the same principle: measuring the time that a well-known part of a message is transmitted from one device and the time that the same part of the same message is received by the other device, then sending another message in the opposite direction and doing the same measurement as shown in Figure 7-7.

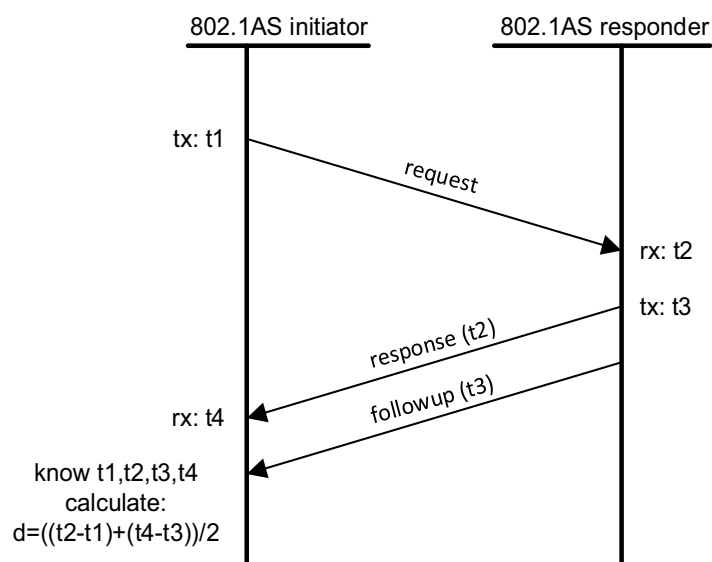


**Figure 7-7—Conceptual medium delay measurement**

This basic mechanism is used in the various LANs in the following ways:

- Full-duplex Ethernet LANs use the two-step peer-to-peer (P2P) path delay algorithm as defined in IEEE Std 1588-2019, where the messages are called Pdelay\_Req, Pdelay\_Resp, and Pdelay\_Resp\_Follow\_Up (see Figure 11-1).
- IEEE 802.11 wireless LANs use the Timing Measurement (TM) procedure or the Fine Timing Measurement (FTM) procedure defined in IEEE Std 802.11-2016. The Timing measurement messages are the “Timing Measurement frame” and its corresponding “Ack” (see Figure 12-1). The Fine Timing Measurement messages are the “initial FTM request frame” and the “Fine Timing Measurement frame” and its “Ack” (see Figure 12-2).
- EPON LANs use the discovery process, where the messages are “GATE” and “REGISTER\_REQ” (see Clause 64 and Clause 77 of IEEE Std 802.3-2018).
- CSNs either use the same mechanism as full-duplex Ethernet or use a method native to the particular CSN (similar to the way native methods are used by IEEE 802.11 networks and EPON) (see Figure 16-5).

### 7.3.3 Logical syntonization

The time-synchronization correction previously described is dependent on the accuracy of the delay and residence time measurements. If the clock used for this purpose is frequency locked (syntonized) to the Grandmaster Clock, then all the time interval measurements use the same time base. Since actually adjusting the frequency of an oscillator (e.g., using a phase-lock loop) is slow and prone to gain peaking effects, PTP Relay Instances can correct time interval measurements using the Grandmaster Clock frequency ratio.

Each PTP Instance measures, at each PTP Port, the ratio of the frequency of the PTP Instance at the other end of the link attached to that PTP Port to the frequency of its own clock. The cumulative ratio of the Grandmaster Clock frequency to the local clock frequency is accumulated in a standard organizational type, length, value (TLV) attached to the Follow\_Up message (or the Sync message if the optional one-step processing is enabled). The frequency ratio of the Grandmaster Clock relative to the local clock is used in computing synchronized time, and the frequency ratio of the neighbor relative to the local clock is used in correcting the propagation time measurement.

The Grandmaster Clock frequency ratio is measured by accumulating neighbor frequency ratios for two main reasons. First, if there is a network reconfiguration and a new Grandmaster PTP Instance is elected, the nearest neighbor frequency ratios do not have to be newly measured as they are constantly measured using the Pdelay messages. This results in the frequency offset relative to the new Grandmaster Clock being known when the first Follow\_Up message (or first Sync message if the optional one-step processing is enabled) is received, which reduces the duration of any transient error in synchronized time during the reconfiguration. This is beneficial to many high-end audio applications. Second, there are no gain peaking effects because an error in frequency offset at one PTP Relay Instance, and resulting residence time error, does not directly affect the frequency offset at a downstream PTP Relay Instance.

### 7.3.4 Grandmaster PTP Instance (best master) selection and network establishment

All PTP Instances participate in best master selection so that the IEEE 802.1AS protocol can determine the synchronization spanning tree. This synchronization spanning tree can be different from the forwarding spanning tree determined by IEEE 802.1Q™ Rapid Spanning Tree Protocol (RSTP) since the spanning tree determined by RSTP can be suboptimal or even inadequate for synchronization or can be for a different topology of nodes from the synchronization spanning tree.

gPTP requires that all systems in the gPTP domain be time-aware systems, i.e., the protocol does not transfer timing over systems that are not time-aware (e.g., those that meet the requirements of IEEE Std 802.1Q-2018, but do NOT meet the requirements of the present standard). A time-aware system uses the peer-to-peer delay mechanism on each PTP Port to determine if a non-time-aware system is at the other end of the link or between itself and the Pdelay responder. If, on sending Pdelay\_Req,

- a) No response is received,
- b) Multiple responses are received, or
- c) The measured propagation delay exceeds a specified threshold, then

the protocol concludes that a non-time-aware system or end-to-end Transparent Clock (TC) (see IEEE Std 1588-2019) is present. In this case, the link attached to the PTP Port is deemed not capable of running gPTP, and the BMCA ignores it. However, the PTP Port continues to attempt the measurement of propagation delay using the peer-to-peer delay mechanism (for full-duplex IEEE 802.3 links), multipoint control protocol (MPCP) messages (for EPON), or IEEE 802.11 messages (for IEEE 802.11 links), and periodically checks whether the link is or is not capable of running the IEEE 802.1AS protocol.

### 7.3.5 Energy efficiency

Sending PTP messages at relatively high rates when there is otherwise little or no traffic conflicts with the goal of reducing energy consumption. This standard specifies a way to request that a neighbor PTP Port reduce the rate of sending Sync (and Follow\_Up if optional one-step processing is not enabled), peer delay, and Announce messages and also to inform the neighbor not to compute neighbor rate ratio and/or propagation delay on this link. A time-aware system could do this when it enters low-power mode, but this standard does not specify the conditions under which this is done; it specifies only the actions a time-aware system takes.

## 7.4 PTP Instance architecture

The model of a PTP Instance is shown in Figure 7-8.

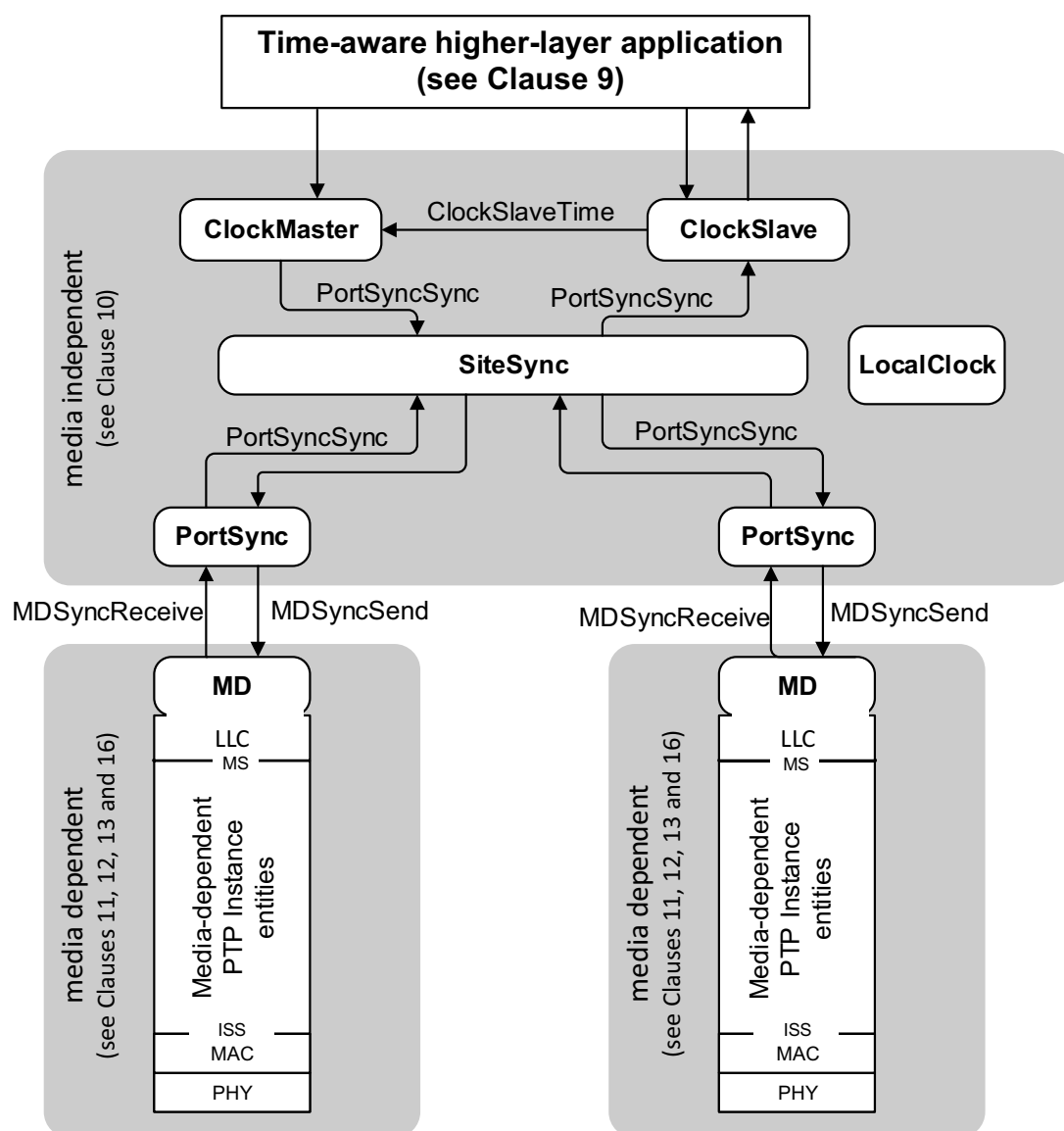


**Figure 7-8—PTP Instance model**

A PTP Instance consists of the following major parts:

- If the PTP Instance includes application(s) that either use or source time information, then they interface with the gPTP information using the application interfaces specified in Clause 9.
- A single media-independent part that consists of ClockMaster, ClockSlave, and SiteSync logical entities, one or more PortSync entities, and a LocalClock entity. The BMCA and forwarding of time information between logical ports and the ClockSlave and ClockMaster is done by the SiteSync

entity, while the computation of PTP Port-specific delays needed for time-synchronization correction is done by the PortSync entities.

- c) Media-dependent ports, which translate the abstract “MDSyncSend” and “MDSyncReceive” structures received from or sent to the media-independent layer and corresponding methods used for the particular LAN attached to the port.

For full-duplex Ethernet ports, IEEE 1588 Sync and Follow\_Up (or just Sync if the optional one-step processing is enabled) messages are used, with an additional TLV in the Follow\_Up (or the Sync if the optional one-step processing is enabled) used for communication of rate ratio and information on phase and frequency change when there is a change in Grandmaster PTP Instance. The path delay is measured using the two-step IEEE 1588 peer-to-peer delay mechanism. This is defined in Clause 11.

For IEEE 802.11 ports, timing information is communicated using the MAC Layer Management Entity to request a “Timing Measurement” or “Fine Timing Measurement” (as defined in IEEE Std 802.11-2016), which also sends everything that would be included in the Follow\_up message for full-duplex Ethernet. The Timing Measurement or Fine Timing Measurement result includes all the information to determine the path delay. This is defined in Clause 12.

For EPON, timing information is communicated using a “slow protocol” as defined in Clause 13. CSNs use the same communication system used by full-duplex Ethernet, as defined in Clause 16.

## 7.5 Differences between gPTP (IEEE Std 802.1AS) and PTP (IEEE Std 1588-2019)

- a) gPTP assumes all communication between PTP Instances is done only using IEEE 802 MAC PDUs and addressing, while IEEE Std 1588-2019 supports various layer 2 and layer 3-4 communication methods.
- b) gPTP specifies a media-independent sublayer that simplifies the integration within a single timing domain of multiple different networking technologies with radically different media access protocols. gPTP specifies a media-dependent sublayer for each medium. The information exchanged between PTP Instances has been generalized to support different packet formats and management schemes appropriate to the particular networking technology. IEEE Std 1588-2019, on the other hand, has introduced a new architecture based on media-independent and media-dependent sublayers (see 6.5.2, Figure 5, and Figure 6 of IEEE Std 1588-2019); however, this architecture is optional. The architecture of IEEE Std 1588-2008 [B10], which is not based on media-independent and media-dependent layers, has been retained for Internet Protocol (IP) version 4, IP version 6, Ethernet LANs, and several industrial automation control protocols. The intent in IEEE Std 1588-2019 is that the new architecture, based on media-independent and media-dependent layers, will be used for IEEE 802.11 networks, IEEE 802.3 EPON, and CSN using the specifications of gPTP, and that the architecture must be used for transports that define native timing mechanisms if those native timing mechanisms are used.
- c) In gPTP there are only two types of PTP Instances: PTP End Instances and PTP Relay Instances, while IEEE Std 1588-2019 has Ordinary Clocks, Boundary Clocks, end-to-end Transparent Clocks, and P2P Transparent Clocks. A PTP End Instance corresponds to an IEEE 1588 Ordinary Clock, and a PTP Relay Instance is a type of IEEE 1588 Boundary Clock where its operation is very tightly defined, so much so that a PTP Relay Instance with Ethernet ports can be shown to be mathematically equivalent to a P2P Transparent Clock in terms of how synchronization is performed, as shown in 11.1.3. In addition, a PTP Relay Instance can operate in a mode (i.e., the mode where the variable syncLocked is TRUE; see 10.2.5.15) where the PTP Relay Instance is equivalent to a P2P Transparent Clock in terms of when time-synchronization messages are sent. A time-aware system measures link delay and residence time and communicates these in a correction field. In summary, a PTP Relay Instance conforms to the specifications for a Boundary Clock in

IEEE Std 1588-2019, but a PTP Relay Instance does not conform to the complete specifications for a P2P Transparent Clock in IEEE Std 1588-2019 because:

- 1) When `syncLocked` is `FALSE`, the PTP Relay Instance sends Sync according to the specifications for a Boundary Clock, and
  - 2) The PTP Relay Instance invokes the BMCA and has PTP Port states.
- d) PTP Instances communicate gPTP information only directly with other PTP Instances. That is, a gPTP domain consists ONLY of PTP Instances. Non-PTP Relay Instances cannot be used to relay gPTP information. In IEEE Std 1588-2019, it is possible to use non-IEEE-1588-aware relays in an IEEE 1588 domain, although this slows timing convergence and introduce extra jitter and wander that must be filtered by any IEEE 1588 clock.
  - e) For full-duplex Ethernet links, gPTP requires the use of the peer-to-peer delay mechanism, while IEEE Std 1588-2019 also allows the use of end-to-end delay measurement.
  - f) For full-duplex Ethernet links, gPTP requires the use of two-step processing (use of `Follow_Up` and `Pdelay_Resp_Follow_Up` messages to communicate timestamps), with an optional one-step processing mode that embeds timestamps in the Sync “on the fly” as they are being transmitted (gPTP does not specify one-step processing for peer delay messages). IEEE Std 1588-2019 allows either two-step or one-step processing to be required (for both Sync and peer delay messages) depending on a specific profile.
  - g) All PTP Instances in a gPTP domain are logically syntonized; in other words, they all measure time intervals using the same frequency. This is done by the process described in 7.3.3 and is mandatory. Syntonization in IEEE Std 1588-2019 is optional. The syntonization method used by gPTP is supported as an option in IEEE Std 1588-2019, but uses a TLV standardized as part of IEEE Std 1588-2019 (this feature is new for IEEE Std 1588-2019), while gPTP uses the `ORGANIZATION_EXTENSION` TLV specified in 11.4.4.3.
  - h) Finally, this standard includes formal interface definitions, including primitives, for the time-aware applications (see Clause 9). IEEE Std 1588-2019 describes external interfaces without describing specific interface primitives.

## 8. IEEE 802.1AS concepts and terminology

### 8.1 gPTP domain

A gPTP domain, hereafter called simply a *domain*, consists of one or more PTP Instances and links that meet the requirements of this standard and communicate with each other as defined by the IEEE 802.1AS protocol. A gPTP domain defines the scope of gPTP message communication, state, operations, data sets, and timescale.

A domain is identified by two attributes: domain number and sdoId. The sdoId of a domain is a 12-bit unsigned integer. The sdoId is structured as a two-part attribute as follows:

- The most significant 4 bits are named the majorSdoId, and
- The least significant 8 bits are named the minorSdoId.

A time-aware system shall support one or more domains, each with a distinct domain number in the range 0 through 127. A time-aware system shall support the domain whose domain number is 0, and that domain number shall not be changed to a nonzero value. Unless otherwise specified in this standard, the operation of gPTP and the timescale in any given domain is independent of operation in any other domain.

The value of majorSdoId for a gPTP domain shall be 0x1. The value of minorSdoId for a gPTP domain shall be 0x00.

NOTE 1—The above requirements for majorSdoId and minorSdoId are for gPTP domains. The requirements for the Common Mean Link Delay Service (CMLDS) are given in 11.2.17.

Both the domainNumber and the sdoId are carried in the common header of all PTP messages (see 10.6.2.2).

NOTE 2—In the 2011 edition of this standard, the attribute majorSdoId was named transportSpecific, and its value was specified as 0x1 in 10.5.2.2.1 of Corrigendum 1. The attribute minorSdoId did not exist in the 2011 edition, but its location in the common header was a reserved field, which was specified to be transmitted as 0 and ignored on receipt.

Unless otherwise stated, information in the remainder of this document is per domain.

NOTE 3—In steady state, all PTP Instances in a gPTP domain are traceable to a single Grandmaster PTP Instance.

### 8.2 Timescale

#### 8.2.1 Introduction

The timescale for a gPTP domain is established by the Grandmaster Clock. There are two types of timescales supported by gPTP:

- The timescale PTP: The epoch is the PTP epoch (see 8.2.2), and the timescale is continuous. The unit of measure of time is the second defined by International Atomic Time (TAI) (for the definition of TAI, see Service de la Rotation Terrestre [B28], with further amplification in IAU [B27]; and for more information on TAI, see Jekeli [B22], international system of units (SI) brochure [B14], and Petit and Luzum [B26]). The timescale of domain 0 shall be PTP. See IEEE Std 1588-2019 for more details.
- The timescale ARB (arbitrary): The epoch is the domain startup time and can be set by an administrative procedure. Between invocations of the administrative procedure, the timescale is continuous. Additional invocations of the administrative procedure can introduce discontinuities in