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DATA COMMUNICATION OVER THE TELEPHONE NETWORK

ELECTRICAL CHARACTERISTICS FOR BALANCED DOUBLE-CURRENT INTERCHANGE CIRCUITS FOR INTERFACES WITH DATA SIGNALLING RATES UP TO 52 Mbit/s

ITU-T Recommendation V.12

(Previously "CCITT Recommendation")

FOREWORD

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The World Telecommunication Standardization Conference (WTSC), which meets every four years, establishes the topics for study by the ITU-T Study Groups which, in their turn, produce Recommendations on these topics.

The approval of Recommendations by the Members of the ITU-T is covered by the procedure laid down in WTSC Resolution No. 1 (Helsinki, March 1-12, 1993).

ITU-T Recommendation V.12 was prepared by ITU-T Study Group 14 (1993-1996) and was approved under the WTSC Resolution No. 1 procedure on the 29th of August 1995.

NOTES

1. In this Recommendation, the expression "Administration" is used for conciseness to indicate both a telecommunication administration and a recognized operating agency.
2. The status of annexes and appendices attached to the Series V Recommendations should be interpreted as follows:
 - an *annexes* to a Recommendation forms an integral part of the Recommendation;
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SUMMARY

This Recommendation specifies the electrical characteristics of a balanced digital interchange circuit, that may be employed when specified for the interchange of serial binary signals between Data Terminal Equipment (DTE) and Data Circuit-Terminating Equipment (DCE) or in any point-to-point interconnection of serial binary signals between data equipments. The balanced digital interchange circuit will normally be utilized on data and timing, or control circuits where the data signalling rate is up to a maximum limit of 52 Mbit/s.