

3.4.1 EPROM, Byte Wide

Release 1

3.4.1.1 – 4K BY 8 EPROM IN DIP, TYPE A

CAPACITY—4K WORDS OF 8 BITS, TYPE A
PACKAGE—24 PIN DIP, 0.6" WIDE
PIN ASSIGNMENT—Fig. 3.4.1-1

3.4.1.2 – 4K AND 8K BY 8 EPROM IN DIP,

CAPACITY—4K, 8K WORDS OF 8 BITS,
PACKAGE—24 PIN DIP, 0.6" WIDE
PIN ASSIGNMENT—Fig. 3.4.1-1

3.4.1.3 – 8K TO 64K BY 8 EPROM FAMILY IN DIP,

CAPACITY—8K, 16K, 32K, 64K WORDS OF 8 BITS,
PACKAGE—28 PIN DIP, 0.6" WIDE
PIN ASSIGNMENT—Fig. 3.4.1-2

3.4.1.4 – 2K TO 512K BY 8 EPROM FAMILY IN RCC

CAPACITY—2K, 4K, 8K, 16K, 32K, 64K, 128K, 256K, & 512K WORDS OF 8 BITS
PACKAGE—32 PAD (PIN) RCC, 0.450" BY 0.550"
PIN ASSIGNMENT—Fig. 3.4.1-3

3.4.1.5 – 32K TO 512K BY 8 EPROM FAMILY IN SOJ,

CAPACITY—32K, 64K, 128K, 256K, 512K WORDS OF 8 BITS,
PACKAGE—28 OR 32 PIN SOJ, 0.4" WIDE OR NOT DEFINED
PIN ASSIGNMENT—Fig. 3.4.1-4

3.4.1.6 – 128K TO 1M BY 8 EPROM FAMILY IN DIP,

CAPACITY—128K, 256K, 512K, 1M WORDS OF 8 BITS,
PACKAGE—32 PIN DIP, 0.6" WIDE
PIN ASSIGNMENT—Fig. 3.4.1-5

3.4.1.7 – 64K TO 512K BY 9 EPROM FAMILY IN DIP,

CAPACITY—64K, 128K, 256K, 512K WORDS OF 9 BITS,
PACKAGE—32 PIN DIP, 0.6" WIDE
PIN ASSIGNMENT—Fig. 3.4.1-6

3.4.1.8 – 2 TO 64 X 16K BY 8 PAGE SELECT EPROM FAMILY IN DIP,

CAPACITY—32K, 64K, 128K, 256K, 512K, 1M WORDS OF 8 BITS,
—2, 4, 8, 16, 32, 64 PAGES OF 16K WORDS OF 8 BITS,
LOGIC FEATURES—Selectable pages of 16K words
PACKAGE—28 PIN DIP, 0.6" WIDE
PIN ASSIGNMENT—Fig. 3.4.1-7

This device contains multiple pages of 16K words which can be selected and the address for which is stored in an internal register.

3.4.1.9 – 128K TO 512K BY 8 EPROM FAMILY IN TSOP-1,

CAPACITY—128K, 256K, 512K WORDS OF 8 BITS,
PACKAGE—32 PIN TSOP-1, 8 mm X 20 mm. PP=0.5 mm
PIN ASSIGNMENT—Fig. 3.4.1-8

