

4.20 - Dual Inline Memory Modules (DIMMs)

This section contains a series of standards that do not fit into the pattern established for those standards that are included in Sections 4.1 through 4.9. Many of them are complete Module specifications that use the standards contained in these sections and that also use memory devices defined in sections 3.11 of this Standard and that are also more completely defined in the companion Standards JESD79 (DDR), JESD79-2 (DDR2), JESD79-3 (DDR3), and JESD79-4 (DDR4).

4.20.1 - Standard Template for JEDEC Module Standards

4.20.2 - 168-Pin, PC133 SDRAM Registered DIMM Design Specification

4.20.3 - 144-Pin, PC133 SDRAM Unbuffered SO-DIMM, Reference Design Specification

4.20.4 - 184-Pin, PC1600/2100 DDR SDRAM Registered DIMM Design Specification

4.20.5 - 184-Pin, PC1600/2100 DDR SDRAM Unbuffered DIMM Design Specification

4.20.6 - 200-Pin, PC3200/PC2700 DDR SDRAM Unbuffered SO-DIMM Reference Design Specification

4.20.7- 184-Pin, PC2700/3200 DDR SDRAM Registered DIMM Reference Design Specification

4.20.8- 184-Pin, PC3200/PC2700 DDR SDRAM Unbuffered DIMM with TSOP-Based DRAMs Reference Design Specification

4.20.9- 100-Pin, DDR SDRAM Unbuffered 32b DIMM Reference Design Specification

4.20.10- 240-Pin, PC2-6400/PC2-5300/PC2-4200/PC2-3200 DDR2 SDRAM Registered DIMM Reference Design Specification

4.20.11- 200-Pin, PC2-6400/PC2-5300/PC2-4200/PC2-3200 DDR2 SDRAM Unbuffered SO-DIMM Reference Design Specification

4.20.12- 214-Pin, PC2-4200/PC2-3200 DDR2 SDRAM Unbuffered MicroDIMM Reference Design Specification

- 4.20.13- 240-Pin, PC2-5300/PC2-6400 DDR2 SDRAM Unbuffered DIMM Reference Design Specification**
- 4.20.14- 244-Pin, PC2-4200/PC2-3200 DDR2 SDRAM Registered Mini-DIMM Reference Design Specification**
- 4.20.15- 240-Pin, PC2-4200/PC2-5300/PC2-6400 DDR2 SDRAM Fully Buffered DIMM Reference Design Specification**
- 4.20.16- 144-Pin, EP2-2100 DDR2 SDRAM 32b-SO-DIMM Reference Design Specification**
- 4.20.17- 240-Pin, 214-Pin PC3-12800 DDR3 Unbuffered MicroDIMM Reference Design Specification**
- 4.20.18- 204-Pin, PC3-6400/PC3-8500/PC3-10600/PC3-12800 DDR3 Unbuffered SO-DIMM Reference Design Specification**
- 4.20.19- 240-Pin, PC3-6400/PC3-8500/PC3-10600/PC3-12800 DDR3 Unbuffered DIMM Reference Design Specification**
- 4.20.20- 240-Pin, PC3-6400/PC3-8500/PC3-10600/PC3-12800 DDR3 Registered DIMM Reference Design Specification**
- 4.20.21- 204-Pin, EP3-6400/EP3-8500/EP3-10600/EP3-12800 DDR3 SDRAM 72b-SO-DIMM Reference Design Specification**
- 4.20.22- 144-Pin DDR3 SDRAM Unbuffered 32b-SO-DIMM Design Specification**
- 4.20.23- 240-Pin, 72 bit-wide, PC3-6400/PC3-8500/PC3-10600/PC3-12800/PC3-14900/PC3-17000 DDR3 SDRAM Registered DIMM Design Specification**
- 4.20.24- 240-Pin, 72 bit-wide, PC3(L)-6400/PC3(L)-8500/PC3(L)-10600/PC3(L)-12800/PC3(L)-14900/PC3(L)-17000 DDR3 SDRAM Load Reduced DIMM Design Specification**
- 4.20.25- 260-Pin, 1.2 V (VDD), PC4-1600/PC4-1866/PC4-2133/PC4-2400/PC4-2666/PC4-3200 DDR4 SDRAM SO-DIMM Design Specification**
- 4.20.26- 288-Pin, 1.2 V (VDD), PC4-1600/PC4-1866/PC4-2133/PC4-2400/PC4-2666/PC4-3200 DDR4 SDRAM Unbuffered DIMM Design Specification**
- 4.20.27- 288-Pin, 1.2 V (VDD), PC4-1600/PC4-1866/PC4-2133/PC4-2400/PC4-2666/PC4-3200 DDR4 SDRAM Load Reduced DIMM Design Specification**