

3.10.1 Nibble Wide MPDRAM

Release NIL

3.10.1.1 – 64K BY 4 MPDRAM IN DIP, SOJ, & ZIP

CAPACITY—64K WORDS OF 4 BITS

LOGIC FEATURES—Multiplexed Address and
—SERIAL and RAM data ports

There are three versions of this part: in DIP, SOJ, and in ZIP

PACKAGE—24 PIN DIP, 0.400" WIDE

PIN ASSIGNMENTS—FIG. 3.10.1-1

PACKAGE—24 PIN SOJ, 0.300" WIDE

PIN ASSIGNMENTS—FIG. 3.10.1-1

PACKAGE—24 PIN ZIP

PIN ASSIGNMENT—Fig. 3.10.1-2

3.10.1.2 – 256K BY 4 MPDRAM IN DIP, SOJ, TSOP2, & ZIP

CAPACITY—256K WORDS OF 4 BITS

LOGIC FEATURES—Multiplexed Address and
—SERIAL and RAM data ports

There are three versions of this part: in DIP, SOJ, and in ZIP

PACKAGE—28 PIN DIP, 0.400" WIDE

PIN ASSIGNMENTS—FIG. 3.10.1-3

PACKAGE—28 PIN SOJ or TSOP2, 0.400" WIDE, 0.050" PIN PITCH

PIN ASSIGNMENTS—FIG. 3.10.1-3

PACKAGE—28 PIN ZIP

PIN ASSIGNMENT—Fig. 3.10.1-4

3.10.1.3 – 256K BY 4 TPD RAM IN SOJ

CAPACITY—256K WORDS OF 4 BITS

LOGIC FEATURES—Multiplexed Address and
—TWO SERIAL and ONE RAM data ports

PACKAGE—40 PIN SOJ, 0.400" WIDE

PIN ASSIGNMENTS—FIG. 3.10.1-5