

3.8 Psuedostatic Random Access Memory (PSRAM)

Release 1r15

3.8.1 PSRAM, Byte Wide

3.8.1.1 - 2K TO 8K BY 8 PSRAM FAMILY IN DIP

CAPACITY--2K, 4K, 8K WORDS OF 8 BITS,
PACKAGE--28-PIN DIP, 0.6" WIDE
PIN ASSIGNMENTS--Fig. 3.8-1

3.8.1.2 - 4K TO 16K BY PSRAM FAMILY IN RCC

CAPACITY--2K, 4K, 8K, 16K WORDS OF 8 BITS,
PACKAGE--32-PAD (PIN) RCC, 0.450" X 0.550"
PIN ASSIGNMENTS--Fig. 3.8-2

3.8.1.3 - 32K TO 512K BY 8 PSRAM FAMILY IN SOJ

CAPACITY--32K, 128K, 256K, 512K WORDS OF 8 BITS,
PACKAGE--28- OR 32-PIN SOJ, 0.4" WIDE OR NOT DEFINED
PIN ASSIGNMENTS--Figs. 3.8-3

3.8.2 PSRAM, WordWide

3.8.2.1 - 4K TO 32K BY 16 PSRAM IN DIP

CAPACITY--4K TO 32K WORDS OF 16 BITS,
PACKAGE--42-PIN DIP, 0.6" WIDE
PIN ASSIGNMENTS--Figs. 3.8-4

3.8.2.2 - 4K TO 256K BY 16 PSRAM IN SCC

CAPACITY--4K TO 256K WORDS OF 16 BITS,
PACKAGE--44-PAD (PIN) RCC, 0.650" X 0.650"
PIN ASSIGNMENTS--Figs. 3.8-5

3.8.2.3 - 8M TO 64M BY 16 BURST PSRAM IN BGA

CAPACITY--8M TO 64M WORDS OF 16 BITS,
PACKAGE--56-BALL BGA, 0.8 mm PITCH
PIN ASSIGNMENTS--Figs. 3.8-6

3.8.2.4 - 16Mb TO 512My BY 16 PSRAM IN VFBGA

CAPACITY--16M TO 512M WORDS OF 16 BITS,
PACKAGE--54-BALL VFBGA, 0.75 mm PITCH
PIN ASSIGNMENTS--Figs. 3.8-7

