

Master Checking Forms

For approval and authorisation of this document, please refer to the IECC Applications Manual Contents, RT/E/S/10040.

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Amendment Record

Issue	Date	Description
4	29/10/1999	Converted to Word format by AEA Technology Rail. Same as issue 3.
5	21/03/2003	Railtrack amended to Network Rail. Source Document Checking Form (Screen Layout Content) amended to include SPAD alarm screen layout requirements. ARS Internal Checking Form amended to include checks of SPAD alarm data. Minor typographical corrections.

Author	Approved	Date	Authorised	Date
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SAO-IEC-HD-15 Issue 5

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Source Document Checking Form

Scheme:	Working Paper Reference:
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Check

OK

Query

Interlocking Files

Have all signals an identity in the interlocking files?		
Have all signals, except fully automatic signals, a panel request in the interlocking files?		
Have all signals, except fully automatic signals, an associated route in the route tables?		
Have all latches in the interlocking files been identified on the SL?		
Have all latches on the SL been identified in the interlocking files?		
Have all points in the interlocking files been identified on the SL?		
Have all points on the SL been identified in the interlocking files?		
Have all points been associated with a specific interlocking?		
Have all flags been associated with a specific interlocking?		
Have all panel requests been associated with a specific interlocking?		
Have all hit area clashes been highlighted on the SL?		

Track Circuits

Have all track circuits on the SL been found in the interlocking files?		
Have all track circuits on the SP been found in the interlocking files?		
Have all track circuit identities listed in the interlocking files been found on the SL?		
Have all track circuit identities listed in the interlocking files been found on the SP?		
Have all track circuits in more than one interlocking been allocated to one interlocking?		
If track circuit name duplication exists, has the method of dealing with it been defined?		
Have all combined track circuits combined subroutes?		
Have all separate track circuits separate subroutes?		
Have all interlocking boundaries been identified?		
Have all isolation area boundaries across track circuits been identified?		

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Check

OK

Query

Signals

Have all signals in the interlocking been located on the SL?		
Have all signals in the interlocking been located on the SP?		
Are all signals not in the interlocking, but on the SL or SP, explicable?		
Have all signal aspects been found as expected?		

Routes

Have all routes been identified on the SL?		
Have all routes been identified on the SP?		
Have all route exit signals been identified?		
Have all route exit buttons been identified?		
Have all associated steps been found on the BS?		

Releases

Have all releases been identified?		
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Latches

Have all latches required for the signalling displays been identified?		
Are all latches operable?		

Alarms

Have all alarms been identified?		
Are all alarms operable?		

Train Describer Berths

Have all berths, apart from imaginary berths, on the BS been found on the SL?		
Are all berths types (normal, approach or departure) consistent?		
Have all non-accessible berths been identified?		

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Check

OK

Query

Train Describer Steps

Have all steps in the Stepping Control Tables been identified on the BS?		
Is there an adequate explanation for those steps not found on the BS?		
Has all the necessary trigger data been identified in the Stepping Control Tables?		

Screen Layout Overview and Detail View Consistency

Have all signal aspects been checked for consistency?		
Have all points been checked for consistency?		
Have all latches been checked for consistency?		
Have all signal auto-working buttons been checked for consistency?		
Have all signal emergency replacement been checked for consistency?		
Have all emergency alarms been checked for consistency?		
Have all alarms been checked for consistency?		
Have all buttons been checked for consistency?		
Have all berths been checked for consistency?		
Have all interlocking boundaries been checked for consistency?		
Have all track circuits been checked for consistency?		

Screen Layout Content

Have all points been labelled on the detail views?		
Have all signals been labelled on the detail views?		
Have all track circuits been labelled on the detail views?		
Are interlocking boundary lines correctly named?		
Are any items drawn in characters not available on the display character set?		
If no signalling alarm list exists, have all special indications on the SL been noted?		
Are there two spaces between the end of SPADable track circuits and adjacent exit buttons?		
Is there a blank space between platforms and the posts of signals with SPAD detection?		
Are 2- & 3-aspect signals with SPAD detection shown as 4-aspect character space on overviews?		
Is the character space above the posts of signals with SPAD detection blank on detail views?		
Are points and signals with SPAD detection positioned so that the point reminder indications		

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Signature	Name	Date	Data Area