

Display Validation Forms

For approval and authorisation of this document, please refer to the IECC Applications Manual Contents, RT/E/S/10040.

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Amendment Record

Issue	Date	Description
4	29/10/1999	Converted to Word format by AEA Technology Rail. Same as issue 3.
5	10/02/2003	Railtrack amended to Network Rail. New forms 25 and 26 added for checking SPAD inhibit indications at signals and SPAD indications on track sections. List of Forms updated in accordance with new forms added. Minor typographical errors corrected.

Author	Approved	Date	Authorised	Date
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SAO-IEC-HD-16 Issue 5

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Displays Validation Form 1

Scheme:	Working Paper Reference:
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Check	OK	Query
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Screen Layouts

Has view 1 been checked for consistency with the screen layout?		
Has view 2 been checked for consistency with the screen layout?		
Has view 3 been checked for consistency with the screen layout?		
Has view 4 been checked for consistency with the screen layout?		
Has view 5 been checked for consistency with the screen layout?		
Has view 6 been checked for consistency with the screen layout?		
Has view 7 been checked for consistency with the screen layout?		
Has view 8 been checked for consistency with the screen layout?		

Identity Labels

Have all signal identities on view 1 been checked?		
Has the blanking of signal identities on view 1 been checked?		
Have all point identities on view 1 been checked?		
Has the blanking of point identities on view 1 been checked?		
Have all track circuit identities on view 1 been checked?		
Has the blanking of track circuit identities on view 1 been checked?		
Have all signal identities on view 2 been checked?		
Has the blanking of signal identities on view 2 been checked?		
Have all point identities on view 2 been checked?		
Has the blanking of point identities on view 2 been checked?		
Have all track circuit identities on view 2 been checked?		
Has the blanking of track circuit identities on view 2 been checked?		
Have all signal identities on view 3 been checked?		
Has the blanking of signal identities on view 3 been checked?		
Have all point identities on view 3 been checked?		

Reference:	Version:	Date:
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Check	OK	Query
Has the blanking of point identities on view 3 been checked?		
Have all track circuit identities on view 3 been checked?		
Has the blanking of track circuit identities on view 3 been checked?		
Have all signal identities on view 4 been checked?		
Has the blanking of signal identities on view 4 been checked?		
Have all point identities on view 4 been checked?		
Has the blanking of point identities on view 4 been checked?		
Have all track circuit identities on view 4 been checked?		
Has the blanking of track circuit identities on view 4 been checked?		
Have all signal identities on view 5 been checked?		
Has the blanking of signal identities on view 5 been checked?		
Have all point identities on view 5 been checked?		
Has the blanking of point identities on view 5 been checked?		
Have all track circuit identities on view 5 been checked?		
Has the blanking of track circuit identities on view 5 been checked?		
Have all signal identities on view 6 been checked?		
Has the blanking of signal identities on view 6 been checked?		
Have all point identities on view 6 been checked?		
Has the blanking of point identities on view 6 been checked?		
Have all track circuit identities on view 6 been checked?		
Has the blanking of track circuit identities on view 6 been checked?		
Have all signal identities on view 7 been checked?		
Has the blanking of signal identities on view 7 been checked?		
Have all point identities on view 7 been checked?		
Has the blanking of point identities on view 7 been checked?		
Have all track circuit identities on view 7 been checked?		
Has the blanking of track circuit identities on view 7 been checked?		
Have all signal identities on view 8 been checked?		

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Check	OK	Query
Has the blanking of signal identities on view 8 been checked?		
Have all point identities on view 8 been checked?		
Has the blanking of point identities on view 8 been checked?		
Have all track circuit identities on view 8 been checked?		
Has the blanking of track circuit identities on view 8 been checked?		

Signature	Name	Date

Reference:	Version:	Date:
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Displays Validation Form 2

Scheme:	Working Paper Reference:
Interlocking:	

ARS Sub-Area	View	ARS Sub-Area Indication		
		Off	Flashing	On

Signature	Name	Date