

Ref:	SSI8003-10A
Issue:	9
Date:	03/03/2012
Compliance date:	N/A

SSI Applications Manual

SSI Data Preparation –Interlockings

Endorsement and Authorisation

For endorsement, authorisation and issue record of this document, please refer to the SSI Applications Manual Contents, NR/L2/SIG/17002.

This document is the property of Network Rail. It shall not be reproduced in whole or part nor disclosed to a third party without the written permission of Network Rail.

© Copyright 2012 Network Rail.

Uncontrolled copy once printed from its electronic source.

Published and Issued by Network Rail, Kings Place, 90 York Way, London. N1 9AG.



Ref:	SSI8003-10A
Issue:	9
Date:	03/03/2012
Compliance date:	N/A

Chapter B: Introductory Information

1 Introduction	5
2 The Structure of an SSI Central Interlocking	5
3 The Interlocking Processor	7
3.1 Signal Memory	7
3.2 Points Memory	9
3.3 Route Memory	10
3.4 Track Circuit Memory	10
3.5 Flag Memory	10
3.6 Elapsed Timer Memory	11
3.7 Start-up Conditions	11
4 The Geographical Data	13
5 The Data Compiler	16
Figure B1 – Structure of an SSI Central Interlocking	18
Figure B2 – TFM Message Data and Status	19
Figure B3 – Interlocking RAM Allocation	20
Figure B3 – (Contd)- Interlocking Ram Allocation	21