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Device embedding assembly technology

Part 2-9: Guidelines — Concept of JISSO level in the electronic assembly technology industries

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The UK participation in its preparation was entrusted to Technical Committee EPL/501, Electronic Assembly Technology.

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CONTENTS

FOREWORD 3
INTRODUCTION 5
1 Scope 6
2 Normative references 6
3 Terms 6
4 Historical Concept of Jisso (JISSO) 6
4.1 Some examples of terms on Jisso/JISSO used in the past 6
4.2 JISSO level versus Packaging Level (Interconnecting Level) 8
4.2.1 JISSO Level (typical) 8
4.2.2 Packaging Level 8
5 Concept of typical terms and description on JISSO Level 9
5.1 JISSO/Jisso 9
5.2 JISSO Level 0 – Intellectual information 10
5.3 JISSO Product Level 1 – Electronic element 10
5.4 JISSO Product Level 2 – Electronic package 12
5.5 JISSO Product Level 3 – Electronic module 12
5.6 JISSO Product Level 4 – Electronic unit 13
5.7 J ISSO Product Level 5 – Electronic system 14
Bibliography 15

Figure 1 – Concept of Jisso/JISSO 7
Figure 2 – Relation between JISSO Level versus Packaging Level 9
Figure 3 – JISSO Level Concept (1) preliminarily introduced by JNAC in 2004 9
Figure 4 – JISSO Level Concept (2) 10
Figure 5 – Examples of JISSO Product Level 1 – Electronic element 11
Figure 6 – Examples of JISSO Product Level 2 – Electronic package 12
Figure 7 – Examples of JISSO Product Level 3 – Electronic module 13
Figure 8 – Example of JISSO Product Level 4 – Electronic unit 14
Figure 9 – Examples of JISSO product Level 5 – Electronic system 14

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DEVICE EMBEDDING ASSEMBLY TECHNOLOGY –

Part 2-9: Guidelines – Concept of JISSO Level in the electronic assembly technology industries

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