PD IEC TS 62607-5-3:2020

BSI Standards Publication

Nanomanufacturing — Key control characteristics

Part 5-3: Thin-film organic/nano electronic devices — Measurements of charge carrier concentration

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National foreword

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NANOMANUFACTURING – KEY CONTROL CHARACTERISTICS –

Part 5-3: Thin-film organic/nano electronic devices – Measurements of charge carrier concentration

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Technical Specifications are subject to review within three years of publication to decide whether they can be transformed into International Standards.

IEC TS 62607-5-3, which is a Technical Specification, has been prepared by IEC technical committee 113: Nanotechnology for electrotechnical products and systems.

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The text of this Technical Specification is based on the following documents:

Draft TS	Report on voting
113/477/DTS	113/523/RVDTS

Full information on the voting for the approval of this Technical Specification can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC TS 62607 series, published under the general title *Nanomanufacturing – Key control characteristics*, can be found on the IEC website.

The committee has decided that the contents of this publication will remain unchanged until the stability date indicated on the IEC website under "http://webstore.iec.ch" in the data related to the specific publication. At this date, the publication will be

- transformed into an International Standard,
- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

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INTRODUCTION

Organic/nano thin-film devices are attracting much attention as promising candidates for light, low cost, flexible, and printable devices in large-area electronics applications. Recently, charge carrier doping techniques have been intensely studied and developed, in the same way as the mature silicon technologies. In organic light-emitting diodes (OLEDs) and organic thin-film transistors (OTFTs), which are typical organic/nano thin-film devices, carrier doping around contact electrode regions with molecular donor/acceptor dopants are often utilized to make ohmic-like contacts for the purpose of increasing electric current in the devices. While the great importance of carrier doping in organic/nano layers is well recognized, the carrier doping mechanisms have not been fully understood yet, and the evaluation method of charge carrier concentration in these materials has not been established.

Conventional representative methods for evaluating charge carrier concentrations (or dopant concentrations) and the type of charge carrier (electron or hole) in inorganic semiconductor materials are Hall-effect measurements and capacitance-voltage measurements. For example, the Hall-effect measurement based on the van der Pauw configuration enables one to get the above-mentioned physical parameters of the charge carrier in specimens with arbitrary shapes including thin-film structures. However, this versatile method cannot be utilized for higher resistance materials such as low-mobility organic semiconductors because of lower currents and sensitivities in the Hall effect. At the present time, the capacitance-voltage measurement based on metal/insulator/semiconductor structures is not applicable to highly-doped organic semiconductors that show some level of metallic behaviour. Therefore, standard methods and guidelines for measuring charge carrier concentration in organic semiconductor layers need to be developed.

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1 Scope

This part of IEC TS 62607, which is a Technical Specification, specifies sample structures for evaluating a wide range of charge carrier concentration in organic/nano materials. This specification is provided for both capacitance-voltage (C-V) measurements in metal/insulator/semiconductor stacking structures and Hall-effect measurements with the van der Pauw configuration. Criteria for choosing measurement methods of charge carrier concentration in organic semiconductor layers are also given in this document.

2 Normative references

There are no normative references in this document.

3 Terms and definitions

For the purposes of this document, the following terms and definitions apply.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at http://www.electropedia.org/
- ISO Online browsing platform: available at http://www.iso.org/obp

3.1
doping
addition of impurities to a semiconductor to control hole or electron concentrations

Note 1 to entry: For organic semiconductors, not only atomic, but also molecular impurities are utilized.

Note 2 to entry: The added impurities are called "dopants".

3.2
bottom-contact electrode
contact electrode located underneath the semiconductor layer to be tested

3.3
top-contact electrode
contact electrode located on top of the semiconductor layer to be tested

4 Sample structures

4.1 Metal/insulator/semiconductor (MIS) structure

Fabrication of a metal/insulator/semiconductor (MIS) structure as shown in Figure 1 is necessary for measuring a capacitance-voltage (C-V) curve in organic/nano semiconductor layers. This structure consists of a series capacitor of the insulator and a depletion region in the semiconductor layer.

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